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\*\*\*\*\* FIELD MAINTENANCE PRINT SET \*\*\*\*\*

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B-TC-DECSA-0-1	DECSA FIELD MAINTENANCE PRINT SET
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K-PL-7018762-0-DBP	POWER SUPPLY ASSY PARTS LIST
E-AD-7018763-0-0	AC INPUT ASSY
K-PL-7018763-0-DBP	AC INPUT ASSY PARTS LIST
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B-DD-M3110-0	
B-DD-M3111-0	
B-DD-M3112-0	ENCLOSE UA,EC,PL,CS,TD AND BD DRAWING ALSO
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B-DD-M3101-0	
B-DD-M3102-0	
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MP01484	FIELD MAINTENANCE PRINT SET (MDECS-AA)
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UNIT VARIATIONS
DECSA-AA
DECSA-BA
DECSA-CA
DECSA-DA
DECSA-EA
DECSA-FA

FIELD MAINTENANCE  
PRINT SET  
DECSA  
DIGITAL EQUIPMENT  
CORPORATION  
MP01385

REVISION HISTORY								
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV
JB	DECSA-TW002	B						

USED ON OPTION/MODEL		DRN.	DATE
		R.J. RILEY	2-11-83
		CHK'D	DATE
		R.J. RILEY	2-11-83
		PROJ. ENG.	DATE
		J. BUZYNSKI	2-11-83
		FIELD SERV.	DATE
SHEET	OF 2	T. RITTER	2-11-83

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TW

TITLE: digital  
FIELD MAINTENANCE PRINT SET  
DECSA

SIZE K	CODE TC	NUMBER DECSA-0-DBF	REV B
DIST.			



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\*\*\*\*\* FIELD MAINTENANCE PRINT SET \*\*\*\*\*

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D-AD-BC17C-0-0	CABLE MODEM EXTENSION
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D-AD-BC17D-0-0	CABLE NULL MODEM
B-DD-BC17L-0-DBP	EXTENSION CABLE
D-AD-BC17L-0-0	EXTENSION CABLE
B-DD-BC22D-0	CABLE, ASYNC NULL MODEM
D-UA-BC22D-0-0	CABLE, ASYNC NULL MODEM
B-DD-BC22E-0	CABLE, ASYNC MODEM
C-UA-BC22E-0-0	CABLE, ASYNC MODEM
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D-AD-BC17E-0-0	CABLE, V.35 MODEM
E-ID-7019937-0-0	INTERCONNECT DIAGRAM, DECSA

UNIT VARIATIONS

FIELD MAINTENANCE  
PRINT SET  
  
DECSA  
  
DIGITAL EQUIPMENT  
CORPORATION  
  
MP01385

REVISION HISTORY

REVISION HISTORY									USED ON OPTION/MODEL		DRN.	DATE
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV			R.J. RILEY	2-11-83
											CHK'D	DATE
											R.J. RILEY	2-11-83
											PROJ. ENG.	DATE
											J. BUZYNKSI	2-11-83
											FIELD SERV.	DATE
											T. RITTER	2-11-83
									SHEET 2 OF 2		digital	

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TW

TITLE:  
FIELD MAINTENANCE PRINT SET  
DECSA

SIZE	CODE	NUMBER	REV
K	TC	DECSA-0-DBF	B
DIST.			



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\*\*\* DRAWING DIRECTORY \*\*\*

UNIT VARIATIONS	
VAR	TITLE
DECSA-AA	COMMUNICATIONS SERVER
DECSA-BA	COMMUNICATIONS SERVER
DECSA-CA	COMMUNICATIONS SERVER
DECSA-DA	COMMUNICATIONS SERVER
DECSA-EA	COMMUNICATIONS SERVER
DECSA-FA	COMMUNICATIONS SERVER

REVISION HISTORY									USED ON OPTION/MODEL		DRN.	DATE		
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV			R.J. RILEY	5-13-83		
JB	DECSA-TW001	B									CHK'D	DATE		
JB	DECSA-TW002	C									R.J. RILEY	5-13-83		
											PROJ. ENG.	DATE		
											J. BUZYNSKI	5-13-83		
											PROD.	DATE		
											B. SWAN	7-27-83		
									SHEET 1 OF 2		d i g i t a l			
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											SIZE	CODE	NUMBER	REV
											K	DD	DECSA-0-DBF	C
											DIST.			



I T E M #	DRAWING NUMBER	DESCRIPTION	T Y P E	I T E M #	DRAWING NUMBER	DESCRIPTION	T Y P E			
1	K-PL-DECSA-Ø-DBP	COMMUNICATIONS SERVER UNA	E/M							
				9	K-SP-DECSA-Ø-DBF1	PAM ENG SPECIFICATION				
2	B-DD-7Ø19937-Ø-Ø	COMMUNICATIONS SERVER								
3	K-DD-DCSAX-L	LINE CARD OPTIONS, DECSA								
4	K-DD-DCSAX-UA	UPGRADE KIT, DECSA								
5	K-PL-DECSK-A-DBP	COUNTRY KIT, DECSA								
6	K-RM-DECSA-Ø	REVISION MATRIX, DECSA								
7	K-TC-DECSA-Ø	FIELD MAINT. PRINT SET, DECSA								
8	K-SP-M3112-Ø-DBF1	M3112 ENG. SPECIFICATION								
TYPE: E = ELECTRICAL M = MECHANICAL E/M = ELECTRO/MECHANICAL			TITLE: DRAWING DIRECTORY DECSA		SHEET 2 OF 2		SIZE K	CODE DD	NUMBER DECSA-Ø-DBF	REV C

digital



DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS																																																																																																																																																																																				
B-TC-DECSA-0 E-UA-7019937-0-DRU K-PL-7019937-0-DRP P-DD-M7133-YA-0	4 3 1	7019937-00	ETHERNET COMMUNICATIONS SERVER W/1 PAM SET, NO LINE CARDS, 120V/240V 512 KBYTE MEMORY	- - - - - E3																																																																																																																																																																																				
		7019937-01	ETHERNET COMMUNICATIONS SERVER W/2 PAM SETS, NO LINE CARDS, 120V/240V 512 KBYTE MEMORY	- - - - - E3																																																																																																																																																																																				
		7019937-04	ETHERNET COMMUNICATIONS SERVER W/1 PAM SET, NO LINE CARDS, 120V/240V 1 MEG MEMORY	- - - - - E3																																																																																																																																																																																				
		MP-J1385	COMMUNICATIONS SERVER (PRINT SET)	- - - - - B																																																																																																																																																																																				
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NOTES:			<table border="1"> <thead> <tr> <th></th> <th>A</th> <th>B</th> <th>C</th> <th>D</th> <th>E</th> <th>F</th> <th>H</th> <th>J</th> </tr> </thead> <tbody> <tr> <td>R</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>E</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>V</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>R</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>E</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>H</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>I</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>S</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>T</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>I</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>O</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>R</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>N</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>Y</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>D</td> <td>0</td> <td>0</td> <td>1</td> <td>1</td> <td>0</td> <td>0</td> <td>0</td> <td>1</td> </tr> <tr> <td>A</td> <td>7</td> <td>9</td> <td>0</td> <td>2</td> <td>3</td> <td>5</td> <td>8</td> <td>1</td> </tr> <tr> <td>T</td> <td>/</td> <td>/</td> <td>/</td> <td>/</td> <td>/</td> <td>/</td> <td>/</td> <td>/</td> </tr> <tr> <td>E</td> <td>3</td> <td>8</td> <td>8</td> <td>8</td> <td>8</td> <td>3</td> <td>8</td> <td>8</td> </tr> <tr> <td></td> <td>4</td> <td>4</td> <td>4</td> <td>4</td> <td>5</td> <td>5</td> <td>5</td> <td>6</td> </tr> </tbody> </table>			A	B	C	D	E	F	H	J	R									E									V									R									E									H									I									S									T									I									O									R									N									Y									D	0	0	1	1	0	0	0	1	A	7	9	0	2	3	5	8	1	T	/	/	/	/	/	/	/	/	E	3	8	8	8	8	3	8	8		4	4	4	4	5	5	5	6
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			CHK'D: J. FALKOWSKI	DATE: 11-SEP-84	TITLE																																																																																																																																																																																			
			<i>J. Falkowski</i>	16-DEC-86	COMMUNICATIONS SERVER																																																																																																																																																																																			
			DES. ENG.: J. DOHERTY	DATE: 02-JUL-84	SHEET	1	OF	1	EDIT:																																																																																																																																																																															
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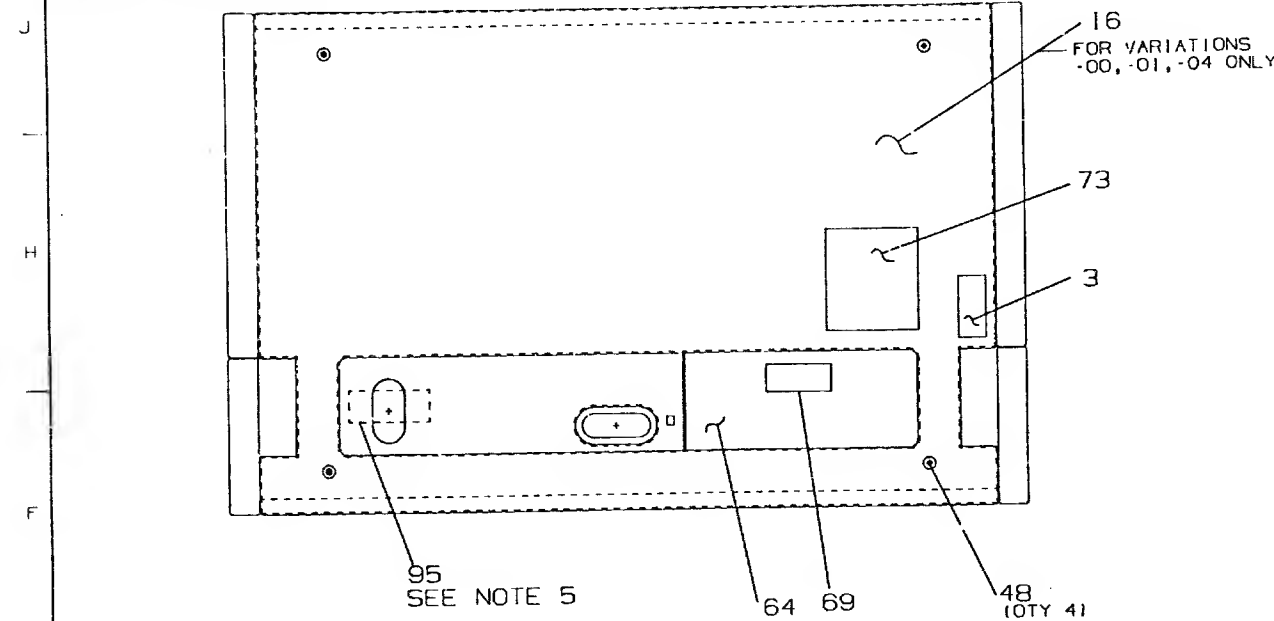


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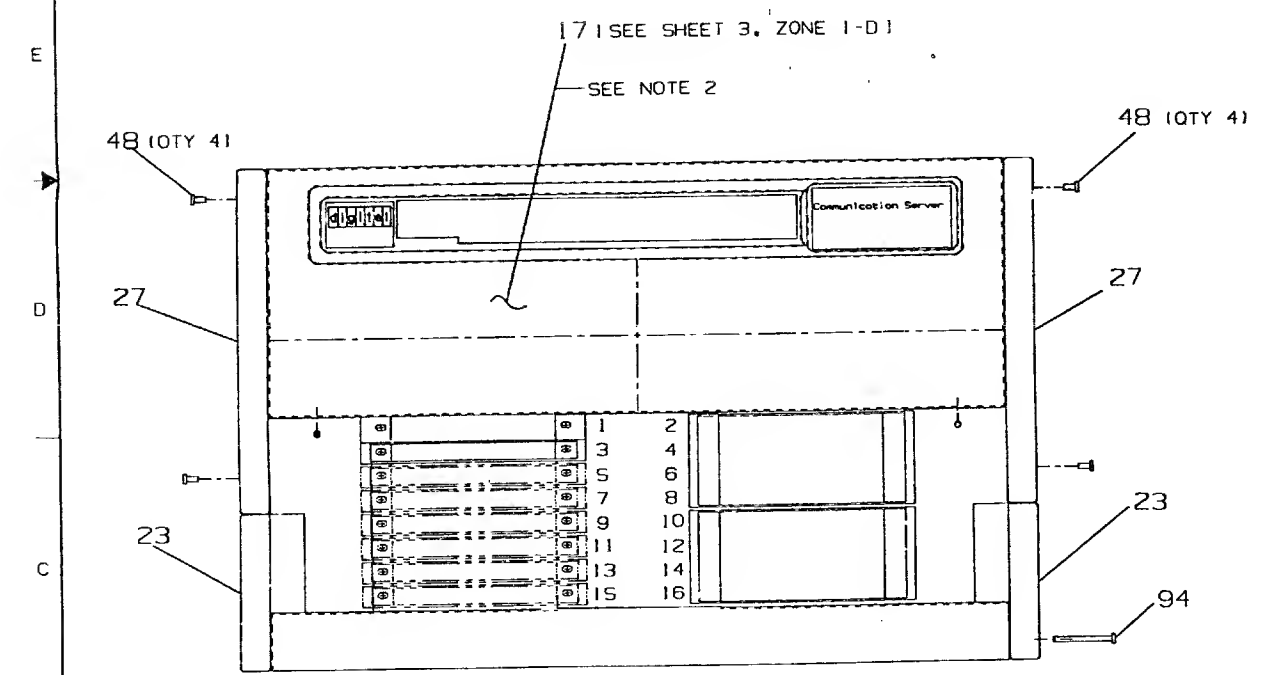
LEGEND		
PART NO.	REV	VARIATION
7019937-00	E3	UNA 1 PAM SET
7019937-01	E3	UNA 2 PAM SETS
7019937-02		DELETED
7019937-03		DELETED
7019937-04	E3	UNA 1 PAM, 1 MEG

NOTE:

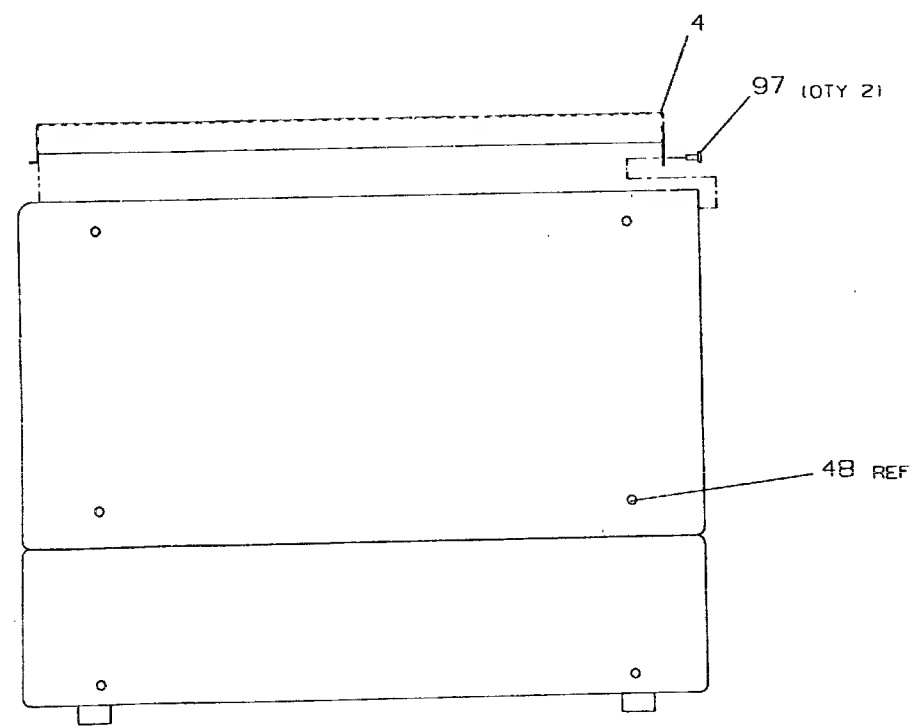
1. DELETED.
2. SCREW DRIVERS (ITEMS 87 AND 88), KEYS (PART OF ITEM 36), TURNAROUND CONNECTORS, (ITEM 90, QTY 2), POWER CORD (ITEM 93) AND CABLE CLIPS (ITEM 29, QTY 2) TO BE PLACED IN PLASTIC BAG (ITEM 84) AND TAPE TO TOP OF UNIT.
3. SET VOLTAGE SELECT SWITCH FOR 220/240; ALL VARIATIONS.
5. REMOVE ENTIRE REAR BACKING BEFORE INSTALLING LABEL.



REAR VIEW



FRONT VIEW  
VARIATION -01 SHOWN



RT. SIDE VIEW  
(LESS REAR PANEL)

PLOT AT .50

CAUTION: OFF SHEET PARTS LIST EXISTS.  
REFER TO K-PL-7019937-0-DBP.

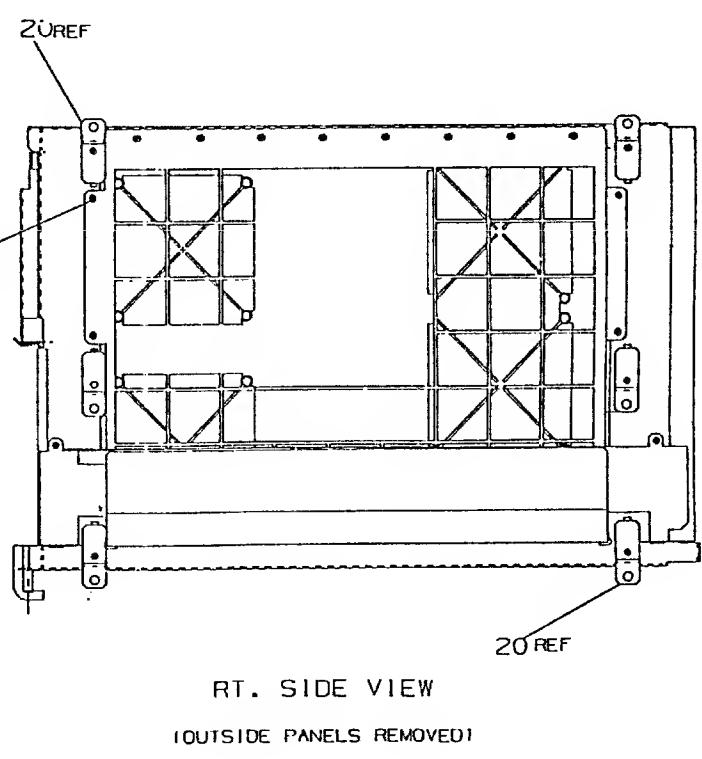
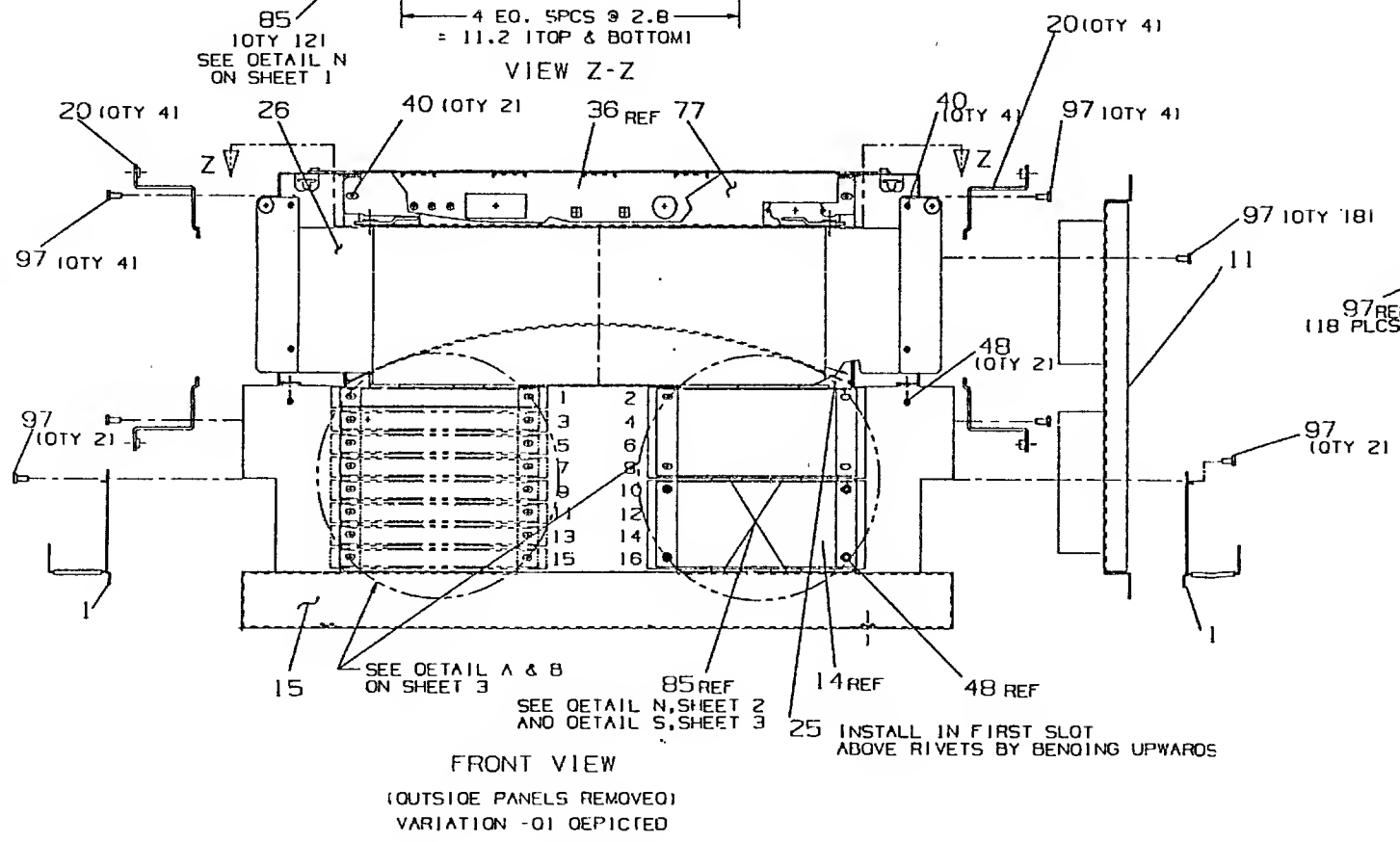
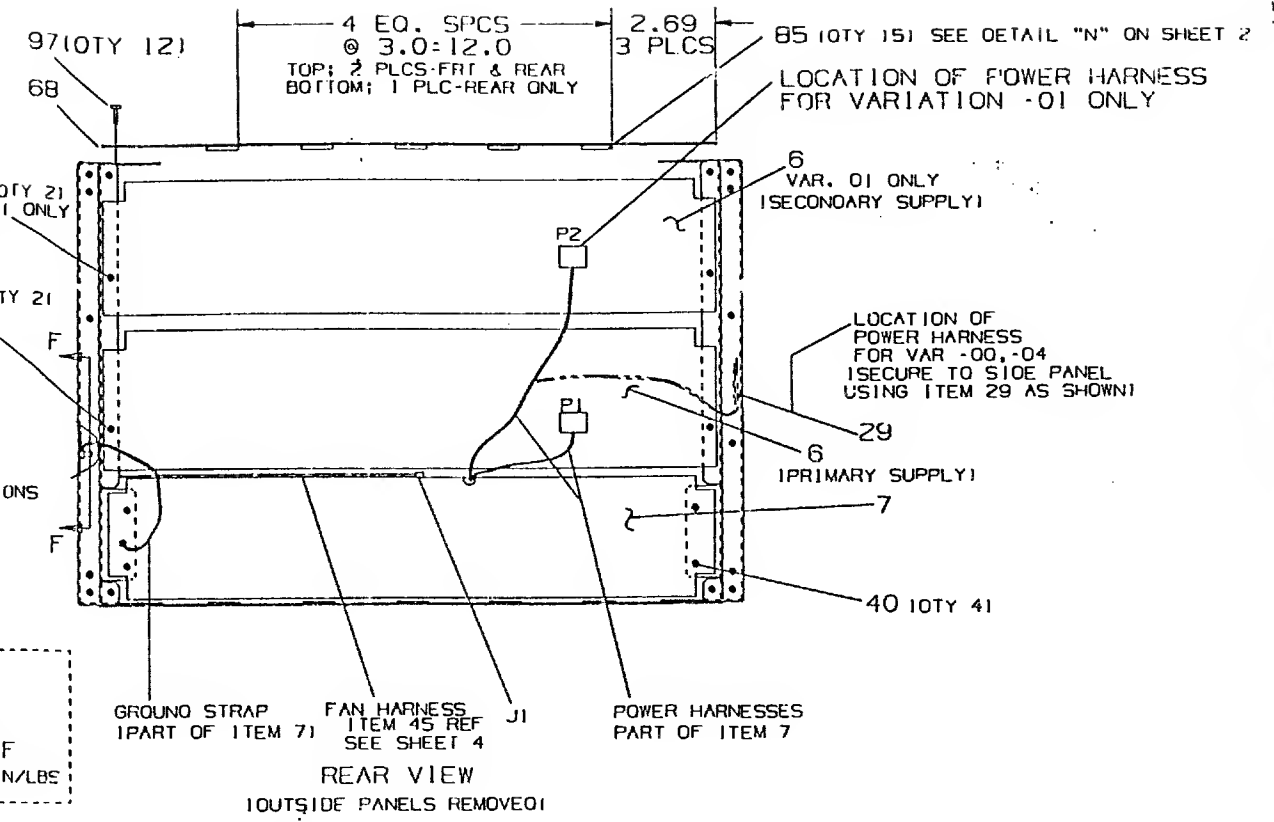
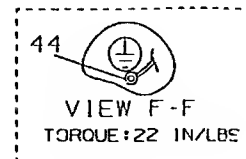
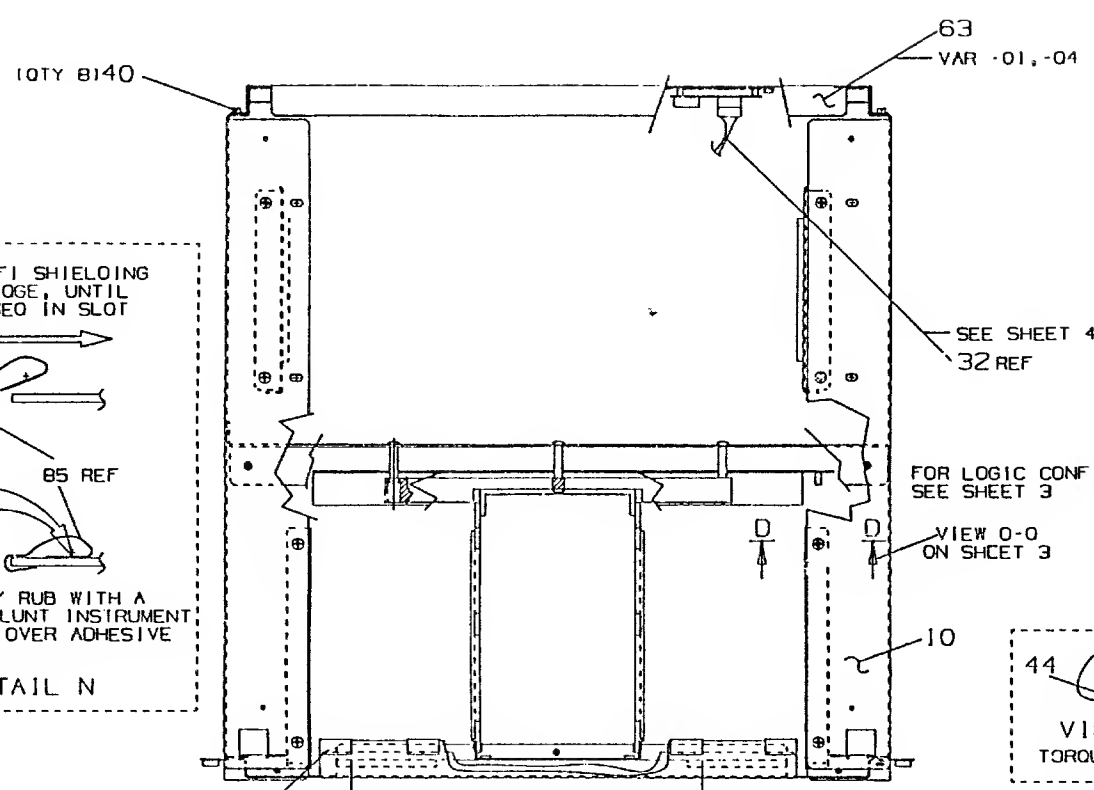
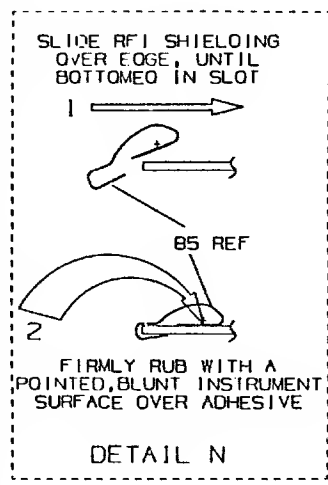
REV	DATE	BY	CHK	DESCRIPTION
1	10 OCT 82	G. GRAVES		INITIAL
2	18 JAN 83	S. BAUCOM		REVISION 2
3	18 JAN 83	S. BAUCOM		REVISION 3
4	18 JAN 83	J. BUZYNSKI		REVISION 4
5	18 JAN 83	V. ADAMETZ		REVISION 5
6	18 JAN 83	S. BAUCOM		REVISION 6
7	18 JAN 83	S. BAUCOM		REVISION 7
8	18 JAN 83	S. BAUCOM		REVISION 8
9	18 JAN 83	S. BAUCOM		REVISION 9
10	18 JAN 83	S. BAUCOM		REVISION 10
11	18 JAN 83	S. BAUCOM		REVISION 11
12	18 JAN 83	S. BAUCOM		REVISION 12
13	18 JAN 83	S. BAUCOM		REVISION 13
14	18 JAN 83	S. BAUCOM		REVISION 14
15	18 JAN 83	S. BAUCOM		REVISION 15
16	18 JAN 83	S. BAUCOM		REVISION 16

DESCRIPTION		DRAWING NO.		PART NO.		ITEM NO.	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE 1/4 INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 1141)							
DIMENSION RANGE IN INCHES		TOLERANCE		DIMENSION RANGE IN INCHES		TOLERANCE	
.001 - .004		±.001		.004 - .007		±.001	
.007 - .010		±.001		.010 - .015		±.001	
.015 - .020		±.001		.020 - .025		±.001	
.025 - .030		±.001		.030 - .035		±.001	
.035 - .040		±.001		.040 - .045		±.001	
.045 - .050		±.001		.050 - .055		±.001	
.055 - .060		±.001		.060 - .065		±.001	
.065 - .070		±.001		.070 - .075		±.001	
.075 - .080		±.001		.080 - .085		±.001	
.085 - .090		±.001		.090 - .095		±.001	
.095 - .100		±.001		.100 - .105		±.001	
.105 - .110		±.001		.110 - .115		±.001	
.115 - .120		±.001		.120 - .125		±.001	
.125 - .130		±.001		.130 - .135		±.001	
.135 - .140		±.001		.140 - .145		±.001	
.145 - .150		±.001		.150 - .155		±.001	
.155 - .160		±.001		.160 - .165		±.001	
.165 - .170		±.001		.170 - .175		±.001	
.175 - .180		±.001		.180 - .185		±.001	
.185 - .190		±.001		.190 - .195		±.001	
.195 - .200		±.001		.200 - .205		±.001	
.205 - .210		±.001		.210 - .215		±.001	
.215 - .220		±.001		.220 - .225		±.001	
.225 - .230		±.001		.230 - .235		±.001	
.235 - .240		±.001		.240 - .245		±.001	
.245 - .250		±.001		.250 - .255		±.001	
.255 - .260		±.001		.260 - .265		±.001	
.265 - .270		±.001		.270 - .275		±.001	
.275 - .280		±.001		.280 - .285		±.001	
.285 - .290		±.001		.290 - .295		±.001	
.295 - .300		±.001		.300 - .305		±.001	
.305 - .310		±.001		.310 - .315		±.001	
.315 - .320		±.001		.320 - .325		±.001	
.325 - .330		±.001		.330 - .335		±.001	
.335 - .340		±.001		.340 - .345		±.001	
.345 - .350		±.001		.350 - .355		±.001	
.355 - .360		±.001		.360 - .365		±.001	
.365 - .370		±.001		.370 - .375		±.001	
.375 - .380		±.001		.380 - .385		±.001	
.385 - .390		±.001		.390 - .395		±.001	
.395 - .400		±.001		.400 - .405		±.001	
.405 - .410		±.001		.410 - .415		±.001	
.415 - .420		±.001		.420 - .425		±.001	
.425 - .430		±.001		.430 - .435		±.001	
.435 - .440		±.001		.440 - .445		±.001	
.445 - .450		±.001		.450 - .455		±.001	
.455 - .460		±.001		.460 - .465		±.001	
.465 - .470		±.001		.470 - .475		±.001	
.475 - .480		±.001		.480 - .485		±.001	
.485 - .490		±.001		.490 - .495		±.001	
.495 - .500		±.001		.500 - .505		±.001	
.505 - .510		±.001		.510 - .515		±.001	
.515 - .520		±.001		.520 - .525		±.001	
.525 - .530		±.001		.530 - .535		±.001	
.535 - .540		±.001		.540 - .545		±.001	
.545 - .550		±.001		.550 - .555		±.001	
.555 - .560		±.001		.560 - .565		±.001	
.565 - .570		±.001		.570 - .575		±.001	
.575 - .580		±.001		.580 - .585		±.001	
.585 - .590		±.001		.590 - .595		±.001	
.595 - .600		±.001		.600 - .605		±.001	
.605 - .610		±.001		.610 - .615		±.001	
.615 - .620		±.001		.620 - .625		±.001	
.625 - .630		±.001		.630 - .635		±.001	
.635 - .640		±.001		.640 - .645		±.001	
.645 - .650		±.001		.650 - .655		±.001	
.655 - .660		±.001		.660 - .665		±.001	
.665 - .670		±.001		.670 - .675		±.001	
.675 - .680		±.001		.680 - .685		±.001	
.685 - .690		±.001		.690 - .695		±.001	
.695 - .700		±.001		.700 - .705		±.001	
.705 - .710		±.001		.710 - .715		±.001	
.715 - .720		±.001		.720 - .725		±.001	
.725 - .730		±.001		.730 - .735		±.001	
.735 - .740		±.001		.740 - .745		±.001	
.745 - .750		±.001		.750 - .755		±.001	
.755 - .760		±.001		.760 - .765		±.001	
.765 - .770		±.001		.770 - .775		±.001	
.775 - .780		±.001		.780 - .785		±.001	
.785 - .790		±.001		.790 - .795		±.001	
.795 - .800		±.001		.800 - .805		±.001	
.805 - .810		±.001		.810 - .815		±.001	
.815 - .820		±.001		.820 - .825		±.001	
.825 - .830		±.001		.830 - .835		±.001	
.835 - .840		±.001		.840 - .845		±.001	
.845 - .850		±.001		.850 - .855		±.001	
.855 - .860		±.001		.860 - .865		±.001	
.865 - .870		±.001		.870 - .875		±.001	
.875 - .880		±.001		.880 - .885		±.001	
.885 - .890		±.001		.890 - .895		±.001	
.895 - .900		±.001		.900 - .905		±.001	
.905 - .910		±.001		.910 - .915		±.001	
.915 - .920		±.001		.920 - .925		±.001	
.925 - .930		±.001		.930 - .935		±.001	
.935 - .940		±.001		.940 - .945		±.001	
.945 - .950		±.001		.950 - .955		±.001	
.955 - .960		±.001		.960 - .965		±.001	
.965 - .970		±.001		.970 - .975		±.001	
.975 - .980		±.001		.980 - .985		±.001	
.985 - .990		±.001		.990 - .995		±.001	
.995 - 1.000		±.001		1.000 - 1.005		±.001	
1.005 - 1.010		±.001		1.010 - 1.015		±.001	
1.015 - 1.020		±.001		1.020 - 1.025		±.001	
1.025 - 1.030		±.001		1.030 - 1.035		±.001	
1.035 - 1.040		±.001		1.040 - 1.045		±.001	
1.045 - 1.050		±.001		1.050 - 1.055		±.001	
1.055 - 1.060		±.001		1.060 - 1.065		±.001	
1.065 - 1.070		±.001		1.070 - 1.075		±.001	
1.075 - 1.080		±.001		1.080 - 1.085		±.001	
1.085 - 1.090		±.001		1.090 - 1.095		±.001	
1.095 - 1.100		±.001		1.100 - 1.105		±.001	
1.105 - 1.110		±.001		1.110 - 1.115		±.001	
1.115 - 1.120		±.001		1.120 - 1.125		±.001	
1.125 - 1.130		±.001		1.130 - 1.135		±.001	
1.135 - 1.140		±.001		1.140 - 1.145		±.001	
1.145 - 1.150		±.001		1.150 - 1.155		±.001	
1.155 - 1.160		±.001		1.160 - 1.165		±.001	
1.165 - 1.170		±.001		1.170 - 1.175		±.001	
1.175 - 1.180		±.001		1.180 - 1.185		±.001	
1.185 - 1.190		±.001		1.190 - 1.195		±.001	
1.195 - 1.200		±.001		1.200 - 1.205		±.001	
1.205 - 1.210		±.001		1.210 - 1.215		±.001	
1.215 - 1.220		±.001		1.220 - 1.225		±.001	
1.225 - 1.230		±.001		1.230 - 1.235		±.001	
1.235 - 1.240		±.001		1.240 - 1.245		±.001	
1.245 - 1.250		±.001		1.250 - 1.255		±.001	
1.255 - 1.260		±.001		1.260 - 1.265		±.001	
1.265 - 1.270		±.001		1.270 - 1.275		±.001	
1.275 - 1.280		±.001		1.280 - 1.285		±.001	
1.285 - 1.290		±.001		1.290 - 1.295		±.001	
1.295 - 1.300		±.001		1.300 - 1.305		±.001	
1.305 - 1.310		±.001		1.310 - 1.315		±.001	
1.315 - 1.320		±.001		1.320 - 1.325		±.001	
1.325 - 1.330		±.001		1.330 - 1.335		±.001	
1.335 - 1.340		±.001		1.340 - 1.345		±.001	
1.345 - 1.350		±.001		1.350 - 1.355		±.001	
1.355 - 1.360		±.001		1.360 - 1.365		±.001	
1.365 - 1.370		±.001		1.370 - 1.375		±.001	
1.375 - 1.380		±.001		1.380 - 1.385		±.001	
1.385 - 1.390		±.001		1.390 - 1.395		±.001	
1.395 - 1.400		±.001		1.400 - 1.405		±.001	
1.405 - 1.410		±.001		1.410 - 1.415		±.001	
1.415 - 1.420		±.001		1.420 - 1.425		±.001	
1.425 - 1.430		±.001		1.430 - 1.435		±.001	
1.435 - 1.440		±.001		1.440 - 1.445		±.001	
1.445 - 1.450		±.001		1.450 - 1.455		±.001	
1.455 - 1.460		±.001		1.460 - 1.465		±.001	
1.465 - 1.470		±.001		1.470 - 1.475		±.001	
1.475 - 1.480		±.001		1.480 - 1.485		±.001	
1.485 - 1.490		±.001		1.490 - 1.495		±.001	
1.495 - 1.500		±.001		1.500 - 1.505		±.001	
1.505 - 1.510		±.001		1.510 - 1.515		±.001	
1.515 - 1.520		±.001		1.520 - 1.525		±.001	
1.525 - 1.530		±.001		1.530 - 1.535		±.	



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J  
H  
F  
E  
D  
C  
B  
A



PLOT AT .25

REVISION HISTORY			DOCUMENT NUMBER		TITLE		SCALE		PAGE	
DATE	ECO. HISTORY	REV	SIZE	NO.	NAME	REV	SCALE	1/2	2	OF 4
			E	UA	701993/0-DBU	K	1/2	1	2	OF 4

COMMUNICATION SERVER

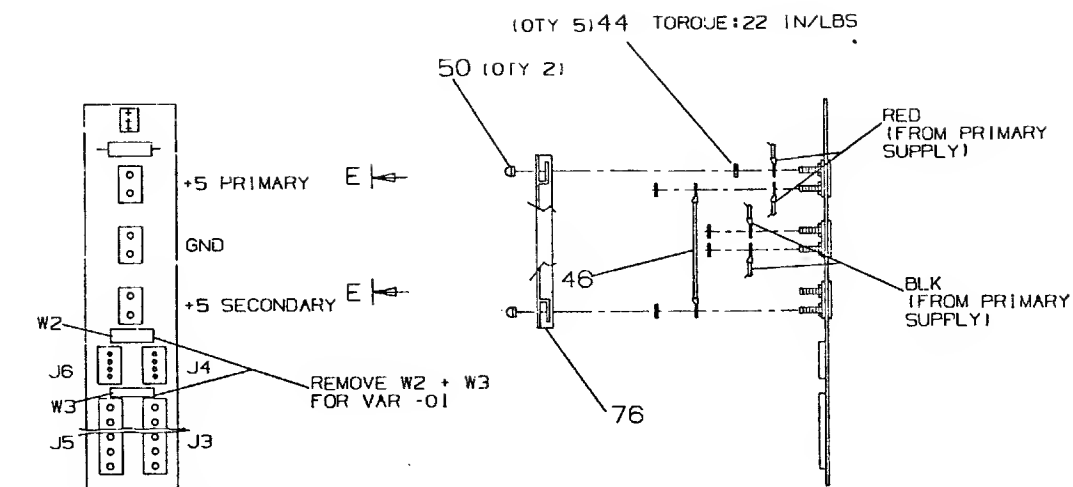
SCALE 1/2

1 2 OF 4

J  
H  
F  
E  
D  
C  
B  
A

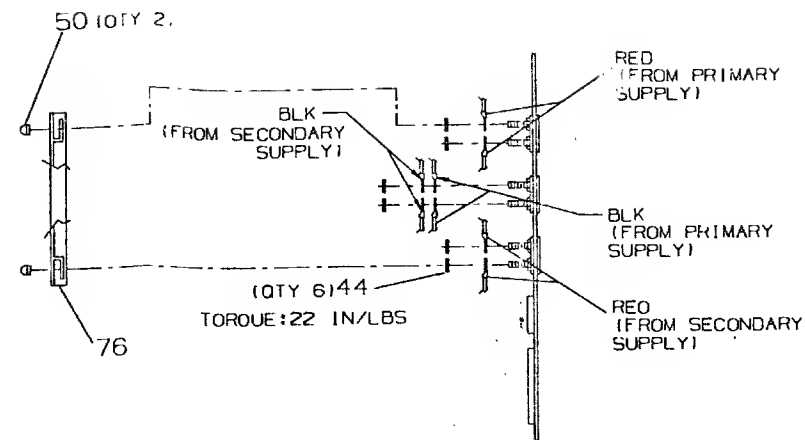


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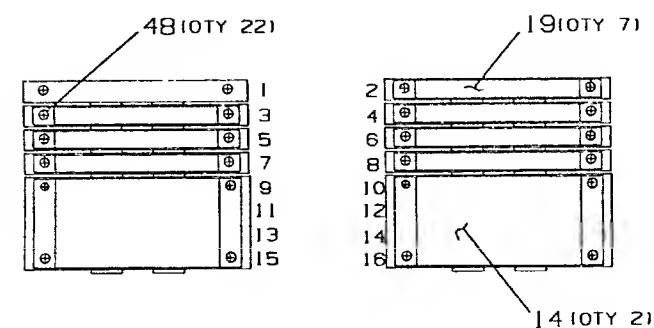
VIEW D-D  
BACKPLANE CONNECTIONS  
SCALE: NONE

VIEW E-E  
VARIATIONS -00, -04 ONLY  
SCALE: NONE

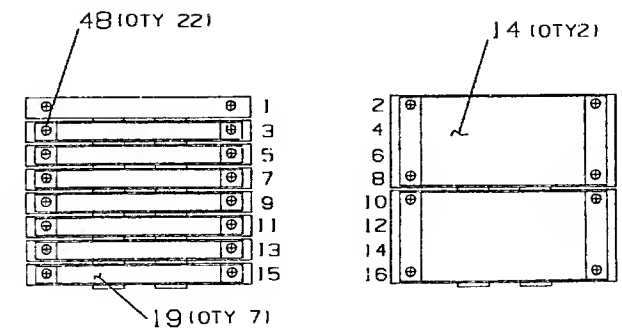


VIEW E-E  
VARIATION -01 ONLY  
SCALE: NONE

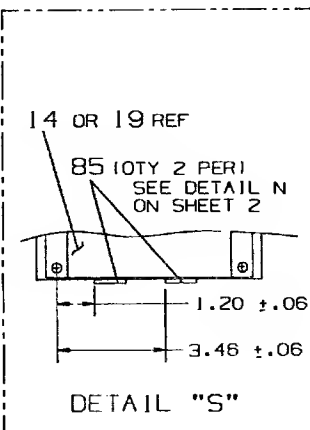
VIEW D-D CABLING	
SECONDARY +15 PWR HARNESS (7018768) TO J6	
SECONDARY PWR SUPPLY SIGNAL HARNESS (1700556-01) TO J5	
PRIMARY +15 PWR HARNESS (7018748) TO J4	
PRIMARY PWR SUPPLY SIGNAL HARNESS (1700336-01) TO J3	



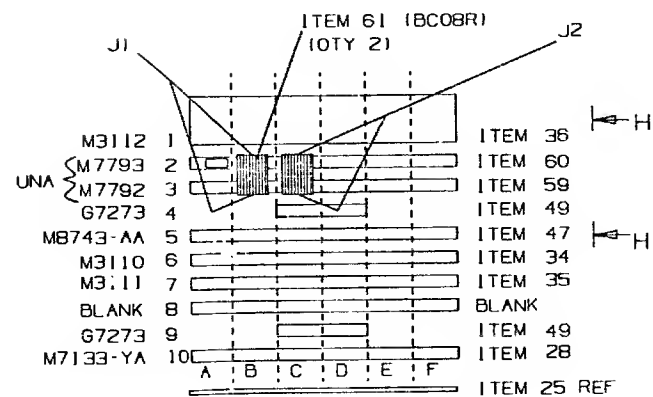
DETAIL "A"  
VARIATIONS -00, -04 ONLY  
SEE DETAIL "S"



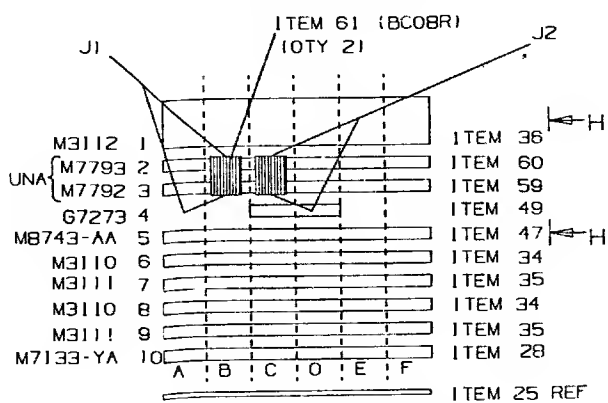
DETAIL "B"  
VARIATION -01 ONLY  
SEE DETAIL "S"



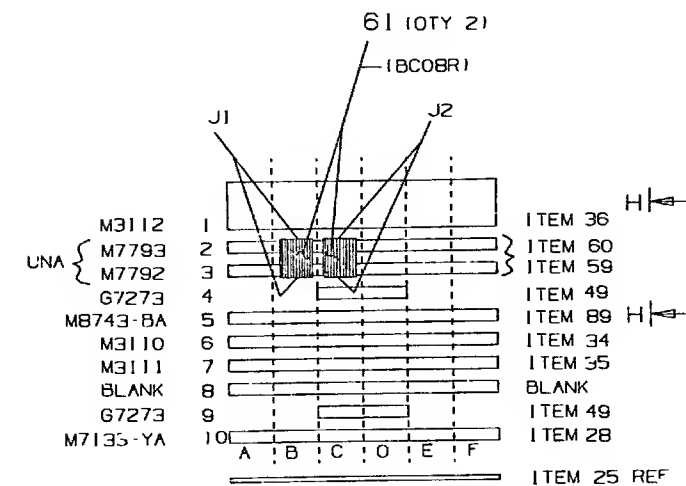
DETAIL "S"



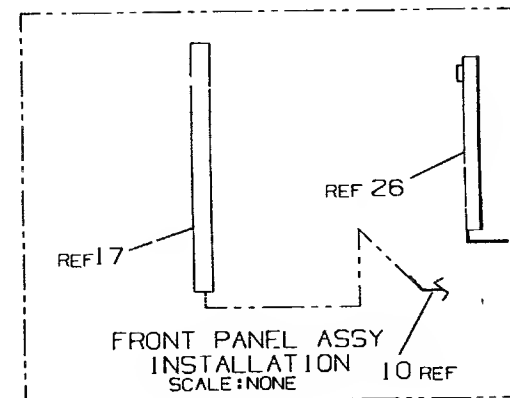
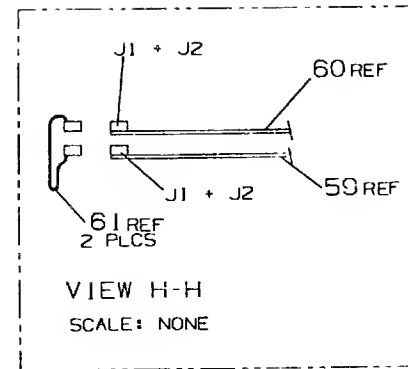
LOGIC CONFIGURATION  
VARIATION -00 ONLY



LOGIC CONFIGURATION  
VARIATION -01 ONLY



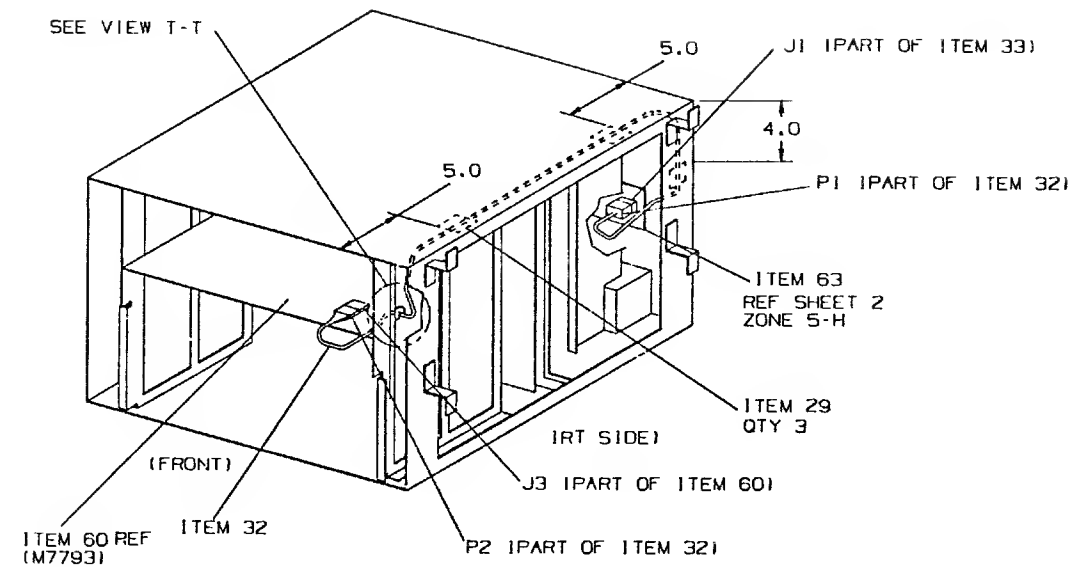
LOGIC CONFIGURATION  
VARIATION -04 ONLY



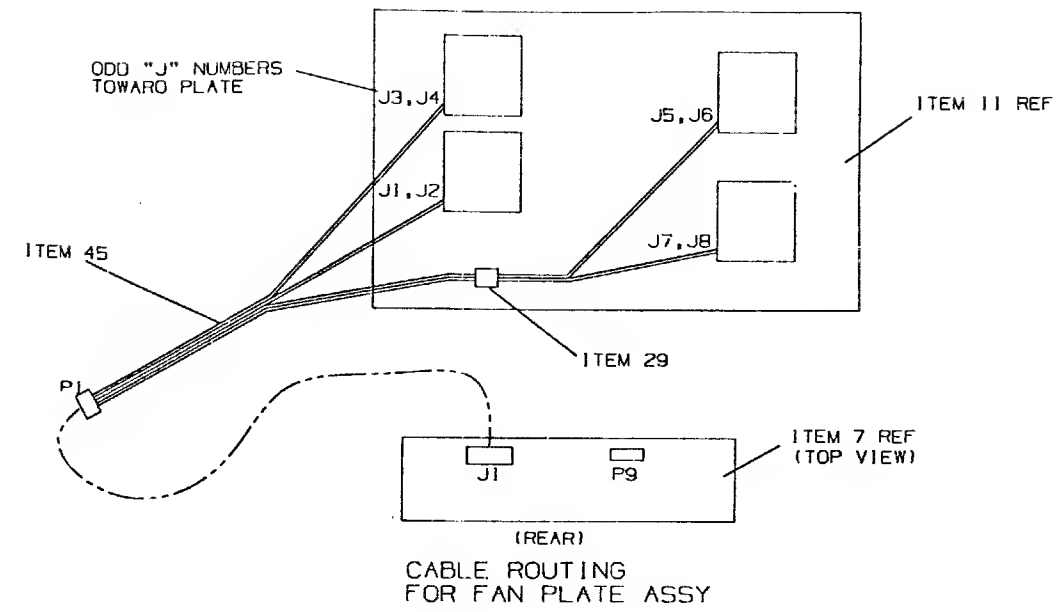
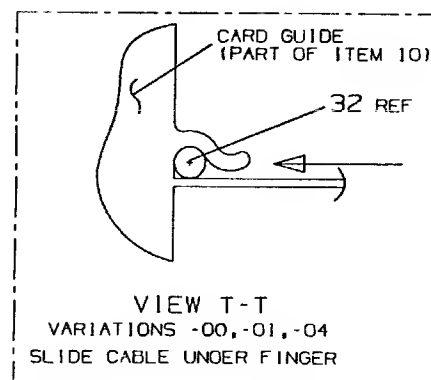
PLOT AT .25

REVISION HISTORY			DOCUMENT NUMBER		
DATE	ECO HISTORY	REV	SIZE CODE	NUMBER	REV
			E UA	7019937-0-DBU	K
			SCALE 1:2 SHEET 3 OF 4		





CABLE ROUTING  
VARIATIONS -00,-01,-04  
SCALE: NONE  
DIMENSIONS ARE APPROXIMATE



PLOT AT .25

REVISION HISTORY		
DATE	ECO HISTORY	REV

8

7

6

5

4

3

2

DOCUMENT NUMBER		
SIZE/COOR	NUMBER	REV
E UA	7019937-0-DBU	K
SCALE 1/2	SHEET 4	OF 4



LINE	ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION	00 E3	01 E3	04 E3
1	1	C-MD-7430432-0-DBU	74-30432-01		PLATE, CABLE, R.H.	1	1	1
2	2		70-19101-01		*** THIS ITEM IS NOT USED ***	-	-	-
3	3	A-PS-3616073-0-0	36-16073-00		LABEL, ID W/COPY VERTICAL	1	1	1
4	4	D-IA-7019563-0-DBU	70-19563-00		COVER ASSY (TOP)	1	1	1
5	5		70-19563-01		*** THIS ITEM IS NOT USED ***	-	-	-
6	6	E-AD-7013762-0-0	70-13762-00		POWER SUPPLY ASSY.	1	2	1
7	7	E-IA-7013763-0-0	70-13763-00		AC INPUT ASSY.	1	1	1
8	8		90-07032-00		*** THIS ITEM IS NOT USED ***	-	-	-
9	10	E-IA-7019090-0-DBU	70-19090-01		FRAME ASSY	1	1	1
10	11	E-AD-7020930-0-DBU	70-20930-01		FAN PLATE ASSY	1	1	1
11	12		90-09735-00		*** THIS ITEM IS NOT USED ***	-	-	-
12	14	B-MD-7426712-0-DBU	74-26712-01		PANEL, FILLER, L.C.	2	2	2
13	15	E-MD-7430503-0-DBU	74-30503-01		PANEL, LOWER FRONT	1	1	1
14	16	E-MD-7431196-0-DBU	74-31196-01		PANEL, REAR	1	1	1
15	17	E-IA-7020934-0-DBU	70-20934-01		UPPER FRONT PANEL ASSY.	1	1	1
16	18	B-MD-7427012-0-DBU	74-27012-01		*** THIS ITEM IS NOT USED ***	-	-	-
17	19	D-MD-7430547-0-DBU	74-30547-03		HANDLE, LC	7	7	7
18	20	D-IA-7427000-0-DBU	74-27000-01		TAB, MOUNTING	8	8	8
19	22		74-26979-01		*** THIS ITEM IS NOT USED ***	-	-	-
20	23	D-IA-7020953-0-DBU	70-20953-01		LOWER SIDE PANEL REWORK	2	2	2
21	24	E-MD-7426398-0-DBU	74-26398-02		*** THIS ITEM IS NOT USED ***	-	-	-
22	25	D-MD-7427950-0-DBU	74-27950-01		SHIELD, LOGIC	1	1	1
23	26	D-AD-7020935-0-DBU	70-20935-01		LOGIC COVER PLATE ASSY	1	1	1
24	27	D-IA-7020954-0-DBU	70-20954-01		LOGIC DIAGRAM, CONTROL MODULE	2	2	2
25	28	D-UA-47133-0-0	47133-YA		KDF11-UA CPU, LINE CLOCK, 2 SER LNS,	1	1	1
26	29		12-18912-00		CLIP, CABLE 1/2	7	6	7
27	31	A-PA-3700670-0-0	37-00670-01		PKG COMM SERVER 7019937-00	1	1	1
28	32	D-IA-7016798-0-0	70-16798-04		UNA BULKHEAD CABLE ASSY.	1	1	1
29	33	D-AD-7016799-0-0	70-16799-01		*** THIS ITEM IS NOT USED ***	-	-	-
30	34	D-UA-43110-0-0	M3110-00		PROTOCOL ASSIST #1 W SPECIAL CHAR	1	2	1
31	35	D-UA-M3111-0-0	M3111-00		PROTOCOL ASSIST #2 W SPECIAL CHAR	1	2	1
32	36	D-UA-M3112-0-0	M3112-00		CONSOLE BOOT TERMINATOR, HEX	1	1	1

REVISION HISTORY		KPL MATRIX FORMAT		SECTION A OF A		DRN: A.ROCHA		DIGITAL	
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 08-JUN-82				
---	INITIAL	A	[CA]	00,01,04	CHK'D: 3. CLARK	TITLE PARTS LIST			
JD	7019937-MK001	B	[CB]		DATE: 03-JUN-82 16-DEC-86	COMMUNICATION SERVER			
JD	7019937-MK003	C	[CC]		DES.ENG: M.CONNELL				
MJL	7019937-MK004	D	[CD]		DATE: 03-JUN-82	DOCUMENT NUMBER			
JD	7019937-MK005	E	[CE]			SIZE	CODE	NUMBER	REV
GG	7019937-MK006	F	[CF]		RESP.ENG.: J.BUZYNSKI				
GG	7019937-MK007	H	[CH]		DATE: 08-JUN-86	K	PL	7019937-0-DBP	K
ML	7019937-MK008	J	[CJ]		DATE: 12-12-86				
ML	7019937-MK009	K			MFG.ENG: R.WHITE	RELEASE DATE: 01-DEC-86			
					DATE: 08-JUN-86	RELEASE STATUS: RELEASED			
BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
7019937		E-UA-7019937-0-DBU		K-DD-7019937-0-0		7019937K.PLS		11	
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LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION		
						00 E3	01 E3	04 E3
33	37		90-07636-00		*** THIS ITEM IS NOT USED ***	-	-	-
34	39		90-08905-01		*** THIS ITEM IS NOT USED ***	-	-	-
35	40		90-00049-16		SCREW,SEMS PAN   PHIL                      8-32	10	10	10
36	41		90-06563-00		*** THIS ITEM IS NOT USED ***	-	-	-
37	42		90-06650-00		*** THIS ITEM IS NOT USED ***	-	-	-
38	44		90-06565-00		NUT,HEX EXT TOOTH LCKWSHR   10-32X	6	7	6
39	45	D-1A-7018765-0-0	70-11755-00		FAN HARNESS ASSY.	1	1	1
40	46	C-1A-7020291-0-03U	70-20291-00		WIRE, JUMPER	1	-	1
41	47		M0803-AA		ECC MOS RAM (M8743-AA)	1	1	-
42	48		90-00049-40		SCREW,SEMS PAN   PHIL                      8-32	28	28	28
43	49	D-UA-37273-0-0	G7273-00		BUS GRANT & NON-PROCESSOR GRANT JU	2	1	2
44	50		90-06610-00		NUT,CAP,RELOCK 10-32X .438AF NYLON	2	2	2
45	51		70-17306-01		*** THIS ITEM IS NOT USED ***	-	-	-
46	52		70-19307-01		*** THIS ITEM IS NOT USED ***	-	-	-
47	55		70-18250-0J		*** THIS ITEM IS NOT USED ***	-	-	-
48	56		BC086-05		*** THIS ITEM IS NOT USED ***	-	-	-
49	59	D-UA-M7792-0-0	M7792-00		DEUNA PORT MODULE(UNIBUS TO ETHERN	1	1	1
50	60	D-UA-M7793-0-0	M7793-00		PCL11-A,TXM REGISTERS HEX	1	1	1
51	61	D-UA-M0688-0-0	S.08R-0F		H855 TO H856 3M SHIELDED,REVERSED	2	2	2
52	62		74-27538-01		*** THIS ITEM IS NOT USED ***	-	-	-
53	63	D-A0-7020935-0-03U	70-20935-01		SHIELD REAR ASSY	1	1	1
54	64	A-PS-3620272-0-0	36-20272-02		LABEL,DECSA,SHORT	1	1	1
55	68	D-1A-7427540-0-03U	74-27540-01		PANEL,FRAME	1	1	1
56	69	A-PS-3617674-0-0	36-17674-00		LABEL,SERIAL/POWER W/O UL + CSA LO	1	1	1
57	73	A-PS-3618307-0-0	36-18307-05		ASSY,04,01	1	1	1
58	76	C-M0-7427624-0-03U	74-27624-01		SHIELD,BACKPLANE	1	1	1
59	77	A-PS-3620282-0-0	36-20282-01		LABEL,CONTROL PANEL ENGLISH	1	1	1
60	78		90-10139-01		*** THIS ITEM IS NOT USED ***	-	-	-
61	79		DMP11-A0		*** THIS ITEM IS NOT USED ***	-	-	-
62	80		74-26382-02		*** THIS ITEM IS NOT USED ***	-	-	-
63	81		74-26382-03		*** THIS ITEM IS NOT USED ***	-	-	-
64	82		74-27539-02		*** THIS ITEM IS NOT USED ***	-	-	-
65	83		74-27539-03		*** THIS ITEM IS NOT USED ***	-	-	-
66	84		99-06557-23		BAG,POLY RECLOSE   4 MIL THK CLEAR	1	1	1
67	85		12-21494-01		SHIELDING,RFI	47	47	47
68	86		90-05264-00		*** THIS ITEM IS NOT USED ***	-	-	-
69	87		47-00060-00		DRIVER,SCREW	1	1	1
70	88		47-00043-00		SCREWDRIVER,PHILLIPS #1	1	1	1
71	89	K-PL-MS11-P-08P1	MS11 -P0		1MB ECC MOS RAM (M8743-BA,512K X 2	-	-	1
72	90		12-15336-02		CONN,D SUB 25POS                                      SE	2	2	2
73	91	C-M0-7428987-0-03U	74-28989-01		*** THIS ITEM IS NOT USED ***	-	-	-
74	92		90-09718-00		*** THIS ITEM IS NOT USED ***	-	-	-
75	93		17-00033-02		PWR CORD,TERM   3-14 SJT                      125V	1	1	1
76	94		90-00062-63		SCREW,THD RL   PAN,SERATD,F/METL	4	4	4
77	95	A-PS-3617905-0-0	36-17905-17		LABEL,WARNING EQUIP. RATING,220-24	1	1	1
78	97		90-00062-23		SCREW,THD RL   PAN,SERATD,F/METL	58	58	58

1 GEN: 1. VARIATIONS -02 AND -03 ARE NOT USED.

! D ! I ! G ! I ! T ! A ! L !	! TITLE !	! SECTION A OF A !	! SIZE !	! CODE !	! DOCUMENT NUMBER !	! REV !
! ! ! ! ! ! ! !	! COMMUNICATION SERVER !	! ! ! ! ! ! ! !	! K !	! PL !	! 7019937-0-09P !	! K !



LINE	ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION		
						00	01	04
						E3	E3	E3

2 GEN: THE M7133-YA IS THE PREFERRED VALUE ENGINEERED PART. HOWEVER, PART NUMBER M7133-00 MAY BE USED.

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							COMMUNICATION SERVER		K	PL	7019937-0-DBP	K



LINE	ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION		
						00 E3	01 E3	04 E3
1	1	C-MD-7430432-0-DBU	74-30432-01		PLATE, CABLE, R.H.	1	1	1
2	2		70-19101-01		*** THIS ITEM IS NOT USED ***	-	-	-
3	3	A-PS-3616073-0-0	36-16073-00		LABEL, ID W/COPY VERTICAL	1	1	1
4	4	D-IA-7019563-0-DBU	70-19563-00		COVER ASSY (TOP)	1	1	1
5	5		70-19563-01		*** THIS ITEM IS NOT USED ***	-	-	-
6	6	E-AD-7018762-0-0	70-18762-00		POWER SUPPLY ASSY.	1	2	1
7	7	E-IA-7018763-0-0	70-18763-00		AC INPUT ASSY.	1	1	1
8	8		90-07032-00		*** THIS ITEM IS NOT USED ***	-	-	-
9	10	E-IA-7019090-0-DBU	70-19090-01		FRAME ASSY	1	1	1
10	11	E-AD-7020930-0-DBU	70-20930-01		FAN PLATE ASSY	1	1	1
11	12		90-09755-00		*** THIS ITEM IS NOT USED ***	-	-	-
12	14	E-MD-7426712-0-DBU	74-26712-01		PANEL, FILLER, L.C.	2	2	2
13	15	E-MD-7430503-0-DBU	74-30503-01		PANEL, LOWER FRONT	1	1	1
14	16	E-MD-7431196-0-DBU	74-31196-01		PANEL, REAR	1	1	1
15	17	E-IA-7020934-0-DBU	70-20934-01		UPPER FRONT PANEL ASSY.	1	1	1
16	18	E-MD-7427012-0-DBU	74-27012-01		*** THIS ITEM IS NOT USED ***	-	-	-
17	19	D-MD-7430547-0-DBU	74-30547-03		HANDLE, LC	7	7	7
18	20	D-IA-7427000-0-DBU	74-27000-01		TAB, MOUNTING	8	8	8
19	22		74-26979-01		*** THIS ITEM IS NOT USED ***	-	-	-
20	23	D-IA-7020953-0-DBU	70-20953-01		LOWER SIDE PANEL REWORK	2	2	2
21	24	E-MD-7426398-0-DBU	74-26398-02		*** THIS ITEM IS NOT USED ***	-	-	-
22	25	D-MD-7427950-0-DBU	74-27950-01		SHIELD, LOGIC	1	1	1
23	26	D-AD-7020935-0-DBU	70-20935-01		LOGIC COVER PLATE ASSY	1	1	1
24	27	D-IA-7020954-0-DBU	70-20954-01		LOGIC DIAGRAM, CONTROL MODULE	2	2	2
25	28	D-UA-77133-0-0	M7133-YA		KDF11-UA CPU, LINE CLOCK, 2 SER LNS,	1	1	1
26	29		12-13912-00		CLIP, CABLE 1/2	7	6	7
27	31	A-PA-3700670-0-0	37-00670-01		PKG COMM SERVER 7019937-00	1	1	1
28	32	D-IA-7018798-0-0	70-18798-04		UNA BULKHEAD CABLE ASSY.	1	1	1
29	33	D-AD-7018799-0-0	70-18799-01		*** THIS ITEM IS NOT USED ***	-	-	-
30	34	D-UA-43110-0-0	M3110-00		PROTOCOL ASSIST #1 W SPECIAL CHAR	1	2	1
31	35	D-UA-M3111-0-0	M3111-00		PROTOCOL ASSIST #2 W SPECIAL CHAR	1	2	1
32	36	D-UA-M3112-0-0	M3112-00		CONSOLE SOGT TERMINATOR, HEX	1	1	1

REVISION HISTORY		KPL MATRIX FORMAT		SECTION A OF A		DRN: A. ROCHA		DIGITAL	
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 08-JUN-82				
---	INITIAL	A	00, 01, 04		CHK'D: B. CLARK		TITLE PARTS LIST		
JD	7019937-MK001	B			DATE: 08-JUN-82 16-DEC-86		COMMUNICATION SERVER		
JD	7019937-MK003	C			DES. ENG: M. CONNELL				
MJL	7019937-MK004	D			DATE: 08-JUN-82		DOCUMENT NUMBER		
JD	7019937-MK005	E			RESP. ENG.: J. BUZYNSKI		SIZE CODE NUMBER REV		
GG	7019937-MK006	F			DATE: 08-JUN-86		K PL 7019937-0-DBP K		
GG	7019937-MK007	H			MFG. ENG: R. WHITE		RELEASE DATE: 01-DEC-86		
ML	7019937-MK008	J			DATE: 08-JUN-86		RELEASE STATUS: RELEASED		
ML	7019937-MK009	K							
BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
7019937		E-UA-7019937-0-DBU		K-DD-7019937-0-0		7019937K.PLS		11	
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LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION		
						00 E3	01 E3	04 E3
33	37		90-07636-00		*** THIS ITEM IS NOT USED ***	-	-	-
34	39		90-08905-01		*** THIS ITEM IS NOT USED ***	-	-	-
35	40		90-00049-16		SCREW,SEMS PAN PHIL 8-32	10	10	10
36	41		90-06563-00		*** THIS ITEM IS NOT USED ***	-	-	-
37	42		90-06660-00		*** THIS ITEM IS NOT USED ***	-	-	-
38	44		90-06565-00		NUT,HEX EXT TOOTH LCKWSHR 10-32X	6	7	6
39	45	D-1A-7018765-0-0	70-13765-00		FAN HARNESS ASSY.	1	1	1
40	46	C-1A-7020291-0-DBU	70-20291-00		WIRE, JUMPER	1	-	1
41	47		MDEC3-AA		ECC MOS RAM (M8743-AA)	1	1	-
42	48		90-00049-40		SCREW,SEMS PAN PHIL 8-32	28	28	28
43	49	D-UA-67273-0-0	67273-00		BUS GRANT & NON-PROCESSOR GRANT JU	2	1	2
44	50		90-06610-00		NUT,CAP,RELOCK 10-32X .438AF NYLON	2	2	2
45	51		70-19306-01		*** THIS ITEM IS NOT USED ***	-	-	-
46	52		70-19307-01		*** THIS ITEM IS NOT USED ***	-	-	-
47	55		70-18250-0J		*** THIS ITEM IS NOT USED ***	-	-	-
48	56		BC083-05		*** THIS ITEM IS NOT USED ***	-	-	-
49	59	D-UA-M7792-0-0	M7792-00		DEUNA PORT MODULE(UNIBUS TO ETHERN	1	1	1
50	60	D-UA-M7793-0-0	M7793-00		PCL11-A,TXM REGISTERS HEX	1	1	1
51	61	D-UA-PC088R-0-0	PC088R-00		H355 TO H856 3M SHIELDED,REVERSED	2	2	2
52	62		74-27538-01		*** THIS ITEM IS NOT USED ***	-	-	-
53	63	D-AD-7020955-0-DBU	70-20955-01		SHIELD,REAR ASSY	1	1	1
54	64	A-PS-3620278-0-C	36-20278-02		LABEL,DECSA,SHORT	1	1	1
55	68	D-1A-7427540-0-DBU	74-27540-01		PANEL,FRAME	1	1	1
56	69	A-PS-3617674-0-0	36-17674-00		LABEL,SERIAL/POWER W/O UL + CSA LO	1	1	1
57	73	A-PS-3618307-0-0	36-18307-05		ASSY,04,01	1	1	1
58	76	C-MD-7427624-0-DBU	74-27624-01		SHIELD,BACKPLANE	1	1	1
59	77	A-PS-3620282-0-0	36-20282-01		LABEL,CONTROL PANEL ENGLISH	1	1	1
60	78		90-10139-01		*** THIS ITEM IS NOT USED ***	-	-	-
61	79		DNP11-AD		*** THIS ITEM IS NOT USED ***	-	-	-
62	80		74-26382-02		*** THIS ITEM IS NOT USED ***	-	-	-
63	81		74-26382-03		*** THIS ITEM IS NOT USED ***	-	-	-
64	82		74-27539-02		*** THIS ITEM IS NOT USED ***	-	-	-
65	83		74-27539-03		*** THIS ITEM IS NOT USED ***	-	-	-
66	84		99-06557-23		BAG,POLY RECLOSE 4 MIL THK CLEAR	1	1	1
67	85		12-21494-01		SHIELDING,RFI	47	47	47
68	86		90-08264-00		*** THIS ITEM IS NOT USED ***	-	-	-
69	87		47-00060-00		DRIVER,SCREW	1	1	1
70	88		47-00043-00		SCREWDRIVER,PHILLIPS #1	1	1	1
71	89	K-PL-MG11-P-DBP1	MS11 -PB		1MB ECC MOS RAM (M8743-BA,512K X 2	-	-	1
72	90		12-15336-02		CONN,D SUB 25POS SE	2	2	2
73	91	C-MD-7428989-0-DBU	74-28989-01		*** THIS ITEM IS NOT USED ***	-	-	-
74	92		90-09718-00		*** THIS ITEM IS NOT USED ***	-	-	-
75	93		17-00083-02		PWR CORD,TERM 3-14 SJT 125V	1	1	1
76	94		90-00062-68		SCREW,THD RL PAN,SERATD,F/METL	4	4	4
77	95	A-PS-3617905-0-0	36-17905-17		LABEL,WARNING EQUIP. RATING,220-24	1	1	1
78	97		90-00062-23		SCREW,THD RL PAN,SERATD,F/METL	58	58	58

1 GEN: 1. VARIATIONS -02 AND -03 ARE NOT USED.

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							COMMUNICATION SERVER		K	PL	7019937-0-DBP	K

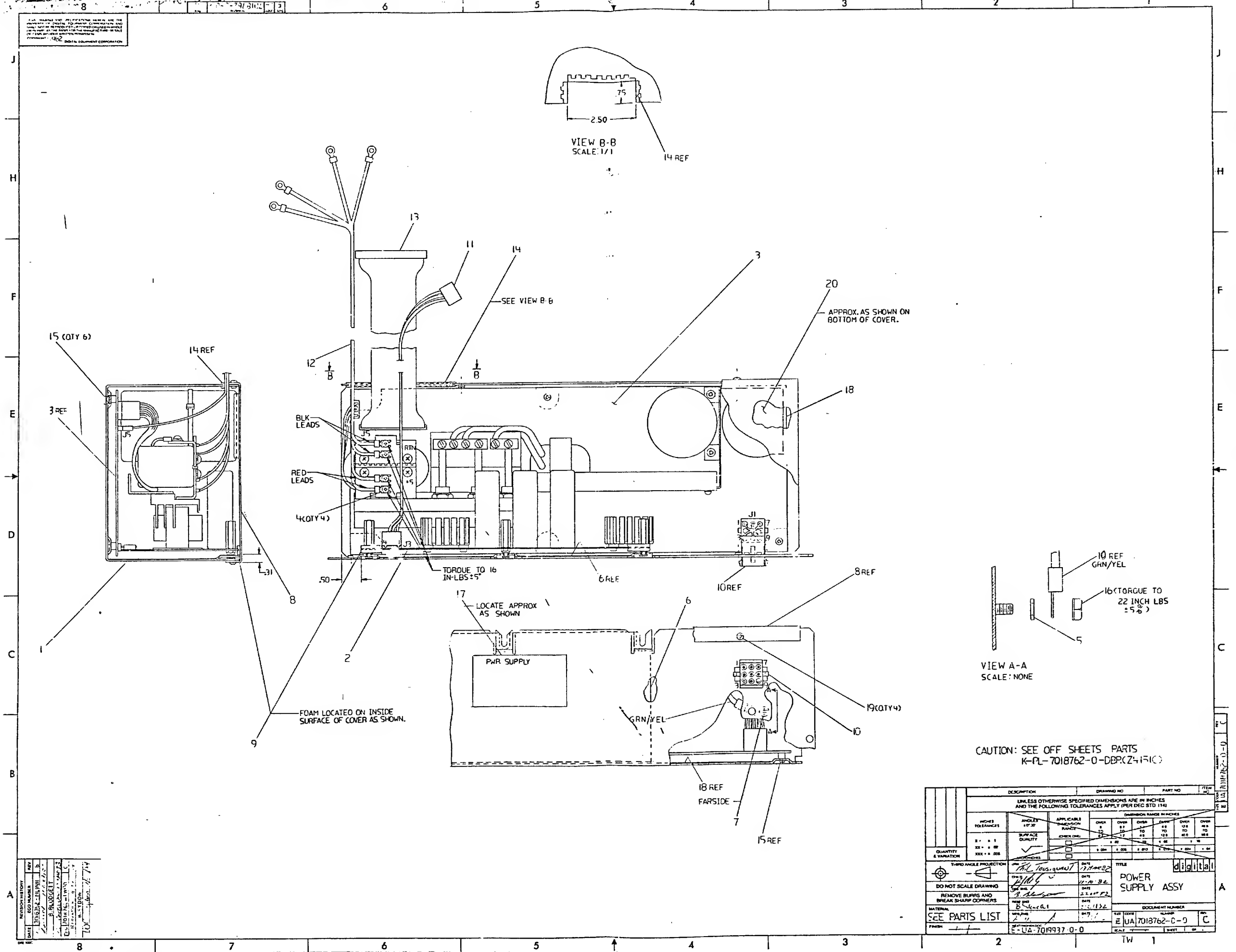


LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	00 E3	01 E3	04 E3	QUANTITY PER VARIATION/REVISION
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2 GEN: THE 47133-YA IS THE PREFERRED VALUE ENGINEERED PART. HOWEVER, PART NUMBER M7133-00 MAY BE USED.

D	I	G	I	T	A	L	TITLE	COMMUNICATION SERVER	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	7019937-0-DBP	K







					MIN			QTY PER VARIATION
LINE	ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION			00
VARIATION REVISION LEVEL:								00
1	1	E-IA-7426319-0-0	7426319-01		BRKT PWR SUPPLY MTG.			1
2	2		H7211-00		COMM OPT PS: +15V 2A,-15V 3A,+12			1
3	3		H7200-00		5V POWER MODULE: 5V 70A,300V 200			1
4	4		9010174-01		SCREW,SEMS PAN PHIL 8-			4
5	5		9007651-00		WASHER,LOCK EXTERNAL STEEL			1
6	6	C-MD-7425494-0-0	7425494-00		INSULATOR,P.C.BOARD			1
7	7		3612680-00		DECAL, GROUND SIGN PER 3E6 *			1
8	8	D-MD-7427005-0-0	7427005-01		COVER,POWER SUPPLY			1
9	9	SEE NOTE 1	9009059-01		TAPE,FOAM ADH .50 WDX .25			10
10	10	D-IA-7018784-0-0	7018784-00		AC INPUT EXTENDER			1
11	11	D-IA-7018768-0-0	7018768-00		COMM. PWR HARNESS			1
12	12	D-IA-7018767-0-0	7018767-00		5V PWR HARNESS			1
13	13		1700356-01		CABLE ASSY 24COND RIBBON 26"LG			1
14	14		9007035-00		GROMMET,CATERPILLAR POLYAMIDE			4
15	15		9009984-00		SCREW,SEMS PAN PHIL 6-			6
16	16		9006565-00		NUT,HEX EXT TOOTH LCKWSHR 10-32			1
17	17		9009255-00		LABEL, POWER SUPPLY, 2-15/16 " L			1
18	18	C-MD-7425401-0-0	7425401-00		INSULATOR,SHIELD			1
19	19		9010174-00		SCREW,SEMS PAN PHIL 6-			4
20	20	SEE NOTE	9009087-00		FOAM, TAPE, SINGLE SIDED 1/8 THK			4

REVISION HISTORY			BASIC PART NO: 7018762		DRN: A.ROCHA		DATE: 28-SEP-82		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A						TITLE PARTS LIST			
---	INITIAL	X03	SECTION VARIATION INDEX		CHK'D: A.ROCHA		DATE: 28-SEP-82		POWER SUPPLY ASSY			
---	INITIAL	X04	[A]00									
---	INITIAL	A										
BB	7018762-ML001	B	[B]		DES.ENG: B. BLODGETT		DATE: 28-SEP-82		DOCUMENT NUMBER			
WS	7018762-TW002	C	[C]						SIZE	CODE	NUMBER	REV
			[D]		RESP.ENG.: B.STANDISH		DATE: 28-SEP-82		K	PL	7018762-0-DBP	C
			[E]		MFG.ENG.: G.ABREU		DATE: 28-SEP-82		RELEASE DATE: 07-AUG-84			
			[F]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
					E-AD-7018762-0-0		E-UA-7019937-0-0		Z4151C.PLS		16	

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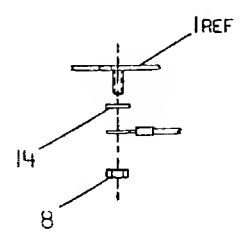
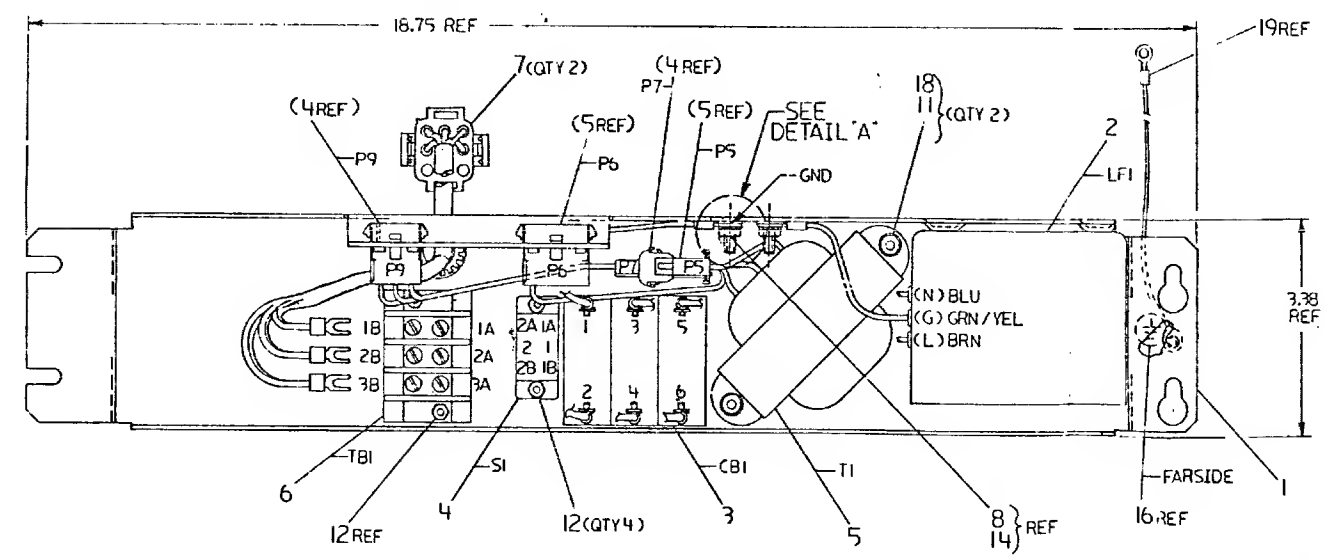
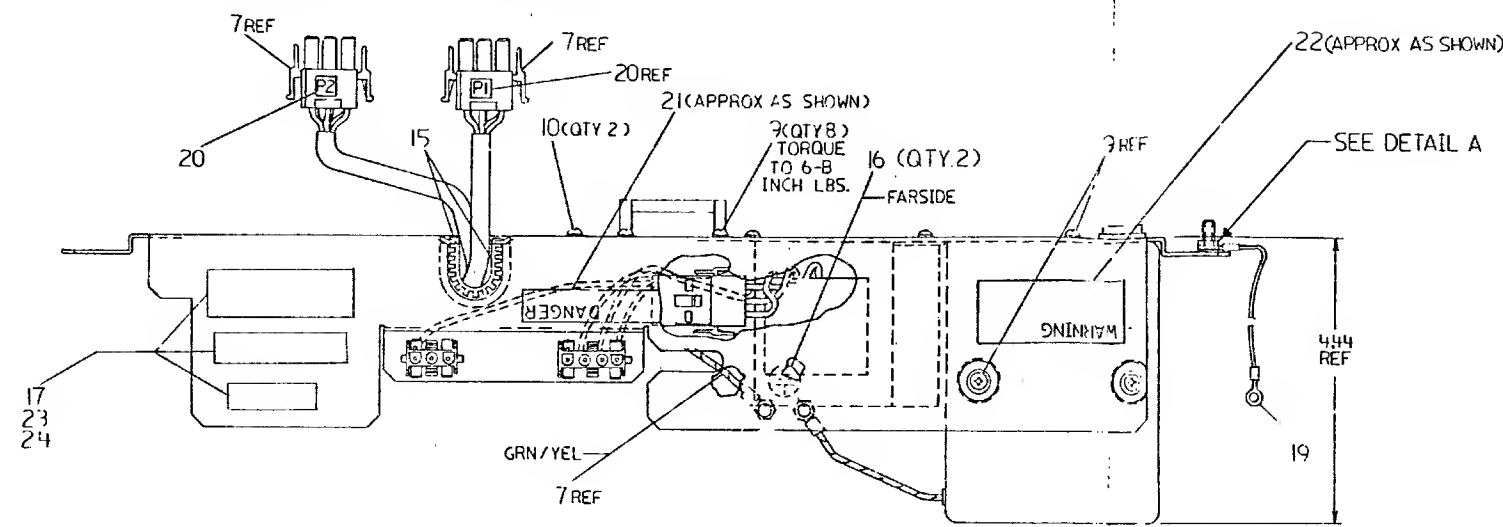
WIRE TABLE									
ITEM NO.	DESCRIPTION	AWG	COLOR	POINT	CONN	TERM	POINT	CONN	TERM
2	BLU			LFI-N					
2	GRN/YEL			LFI-G					
2	BRN			LFI-L					
4				P5					
1	BRN	17 REF		(P7-1)					
1	BRN	19 REF		(SI-2A)					
1	BRN	24 REF		CB1-2					
1	BRN	1 REF		TBI-2A					
1	BLU	19 REF		(SI-1)					
1	ORN	19 REF		(P7-4)					
1	ORN	2 REF		(SI-1B)					
1	ORN	2 REF		(SI-2B)					
1	RED	5 REF		(SI-1A)					
4	RED	6 REF		(P7-3)					

WIRE TABLE									
ITEM NO.	DESCRIPTION	AWG	COLOR	POINT	CONN	TERM	POINT	CONN	TERM
7	RED								
7	ORN								
7	BRN								
7	GRN/YEL								

WIRE TABLE									
ITEM NO.	DESCRIPTION	AWG	COLOR	POINT	CONN	TERM	POINT	CONN	TERM
7	RED								
7	ORN								
7	BRN								
7	GRN/YEL								

PART NO.	REV	VARIATION
7018763-00	B2	AS SHOWN

NOTES:  
1 WIRE LIST ENTITIES WITH POINT NUMBERS REFERENCED, I.E. (1 REF), ARE POINTS FROM THE D-1A-7018763-0-0, AC SWITCH ASSY, ITEM 4.



CAUTION: FOR PARTS LIST REFER TO K-PL-7018763-0-DBP.

DESCRIPTION		DRAWING NO.		PART NO.		ITEM NO.	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)							
INCHES TOLERANCES	ANGLES	SURFACE QUALITY	APPLICABLE DIMENSION RANGE	DIMENSIONS IN INCHES			
				OVER 12	OVER 10	OVER 8	OVER 6
XX ± .1	XX ± .05	XX ± .004	XX ± .004	XX ± .004	XX ± .004	XX ± .004	XX ± .004
QUANTITY & VARIATION				DATE			
THIRD ANGLE PROJECTION				DATE			
DO NOT SCALE				DATE			
REMOVE BURRS AND BREAK SHARP CORNERS				DATE			
MATERIAL				DATE			
PARTS LIST				DATE			
TITLE				DOCUMENT NUMBER			
AC INPUT ASSY				7018763-00			



AUTOMATED BY VAXKPL (V1.2)

PARTS LIST

SHEET A1 OF A1  
QUANTITY PER VARIATION/REVISION

LINE	ITEM	TOP DOCUMENT	PART NUMBER	DESCRIPTION	QUANTITY
1	1	E-IA-7426313-0-0	74-26313-01	PANEL, AC INPUT	1
2	2		12-25624-01	FILTER, LINE, LINE, EMI/RFI 115/250V	1
3	3		12-19732-01	CKT BKP 15/15/1.5A 3P MAGNETIC	1
4	4	D-IA-7018764-0-0	70-13764-00	AC SWITCH ASSY.	1
5	5		16-19934-01	XFMRA P=3 S=120V 300MA	1
6	6		90-07631-00	TERM BLOCK 03POS(2X02).437CC CL	1
7	7	D-IA-7019769-0-0	70-13769-00	AC POWER SUPPLY HARNESS	2
8	8		90-06565-00	NUT, HEX EXT TOOTH LCKWSHR 10-32X	3
9	9		90-10174-00	SCREW, SEMS PAN PHIL 6-32	8
10	10		90-09546-00	SCREW, SEMS PAN PHIL 4-40	2
11	11		90-06563-00	NUT, HEX EXT TOOTH LCKWSHR 8-32X	2
12	12		90-06557-00	NUT, HEX EXT TOOTH LCKWSHR 4-40X	4
13	13		90-06660-00	*** THIS ITEM IS NOT USED ***	-
14	14		90-07651-00	WASHER, LOCK EXTERNAL STEEL	3
15	15	SEE NOTE 1	90-07035-00	GROMMET, CATERPILLAR POLYAMIDE	1
16	16		36-12680-00	LABEL, GROUND SYMBOL BLACK	2
17	17		36-16073-00	LABEL, ID W/COPY VERTICAL	1
18	18		90-10174-01	SCREW, SEMS PAN PHIL 8-32	2
19	19		70-11412-0X	CABLE CHASSIS GND.	1
20	20	A-CC-740972-0-0	74-09872-02	PWR HARNESS DECAL, BLACK ON CLEAR	1
21	21		36-15037-03	LABEL, "DANGER-HIGH CURRENT"	1
22	22		36-19551-01	LABEL, WARNING REMOVE POWER	1
23	23		36-17674-00	LABEL, SERIAL/POWER W/O UL + CSA LO	1
24	24		36-13211-01	DECAL, CLEAR PREPRINTED CSA 1-1/4 X	1

1 GEN: QTY. FOR ITEM 15 IS SHOWN IN INCHES.

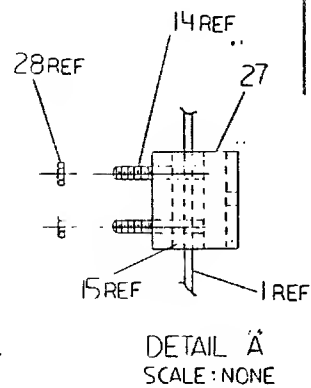
REVISION HISTORY		KPL MATRIX FORMAT		SECTION A OF A		DRN: A. ROCHA		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 10-AUG-82		TITLE PARTS LIST		
---	INITIAL	A	[A] 00		CHK'D: A. ROCHA		AC INPUT ASSY		
JB	7018763-TW001	B	[B]		DATE: 10-AUG-82 19-AUG-86				
ML	7018763-MK002	C	[C]		DES. ENG: B. BLODGETT				
			[D]		DATE: 10-AUG-82		DOCUMENT NUMBER		
			[E]				SIZE CODE NUMBER REV		
			[F]		RESP. ENG.: B. STANDISH				
			[H]		DATE: 10-AUG-82		K PL 7018763-0-DBP C		
			[J]		MFG. ENG: G. ABREU		RELEASE DATE: 18-AUG-86		
					DATE: 10-AUG-82		RELEASE STATUS: RELEASED		
BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
7018763		E-AD-7018763-0-0		E-UA-7019937-0-0		7018763C.PLS		1	
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COPYRIGHT 1982 DIGITAL EQUIPMENT CORPORATION"

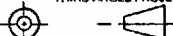
NOTES:

1. TORQUE ITEM28 (NUT) TO  $22 \frac{\text{IN}}{\text{LBS}} \pm 5\%$   
6 PLACES.
2. THESE COMPONENTS NOT INSTALLED.
- 3 REMOVE PINS FROM CONN J3 & J5  
BEFORE INSTALLING CONN.



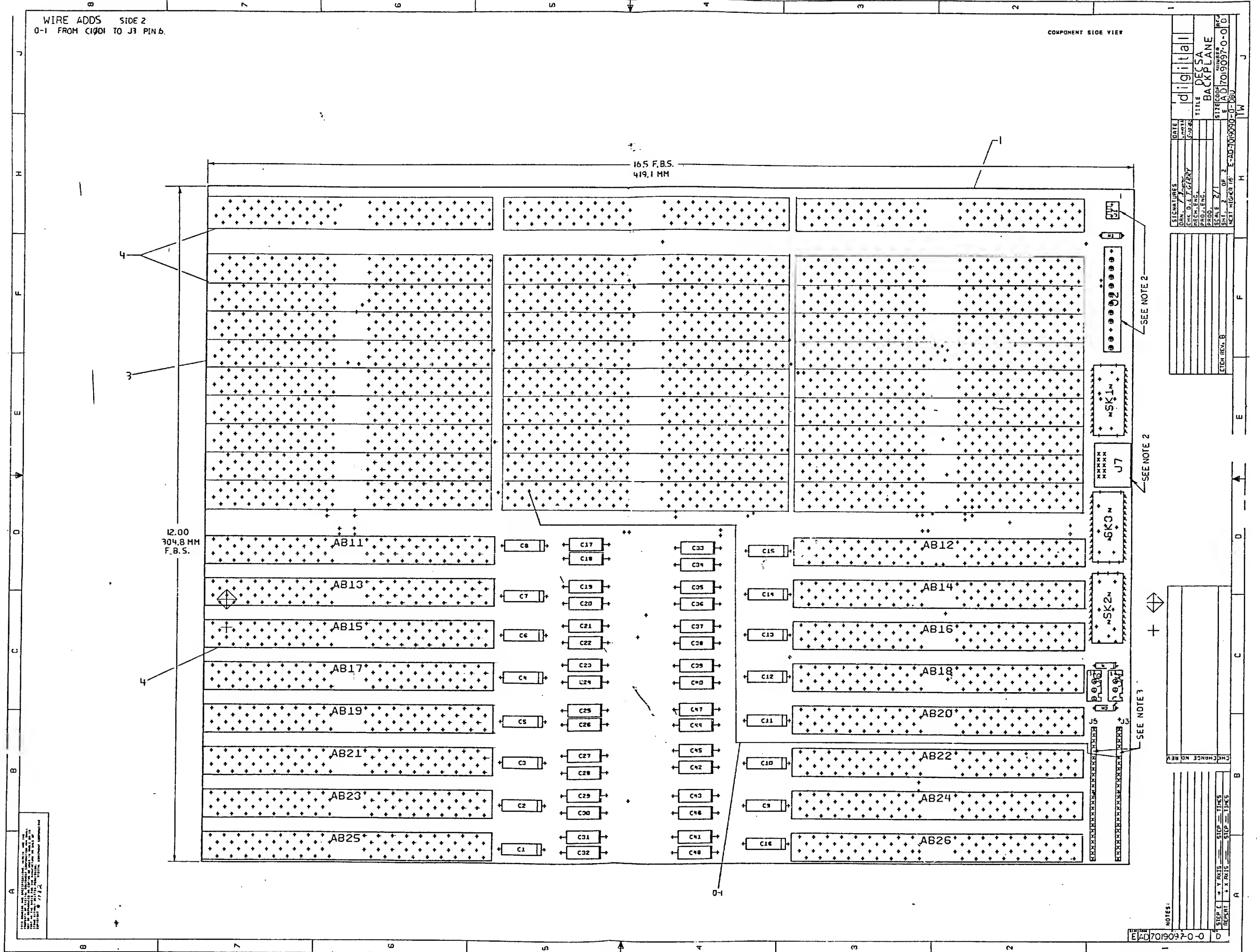
SHT 2 OF 2 'E' SIZE

CAUTION: SEE OFF SHEETS PARTS LIST  
K-PL-7019097-0-DBP(Z3440)

		DESCRIPTION		DRAWING NO.		PART NO.		ITEM NO.	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)									
INCHES TOLERANCES		ANGLES ±0°30'	APPLICABLE DIMENSION RANGE	DIMENSION RANGE IN INCHES					
				OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 10	OVER 10 TO 40	OVER 40 TO 120	OVER 120 TO 500
X ± .1		SURFACE QUALITY  ✓	(CHECK ONE)	± .02	± .03	± .05	± .10		
XX ± .02			<input type="checkbox"/>	± .004	± .008	± .012	± .018	± .024	± .04
XXX ± .005			<input type="checkbox"/>						
QUANTITY & VARIATION		MICROINCHES							
THIRD ANGLE PROJECTION		DWG	DATE		TITLE				
		Mr. B. F. [Signature]		7-2 July 82		digital			
DO NOT SCALE DRAWING		CHKD	DATE		DECSA BACKPLANE				
REMOVE BURRS AND BREAK SHARP CORNERS		DES. ENG.	DATE						
MATERIAL		REP. ENCL.	DATE						
FINISH		WFL. ENCL.	DATE						
		NEXT HIGHERCOD		DOCUMENT NUMBER					
		E-1A-701090-0-DBU		SIZE CODE		NUMBER		REV.	
				DAD		7010907-0-0		D	
				SCALE		NONE		SHEET 1 OF 2	



**COMPONENT SIDE VIEW**

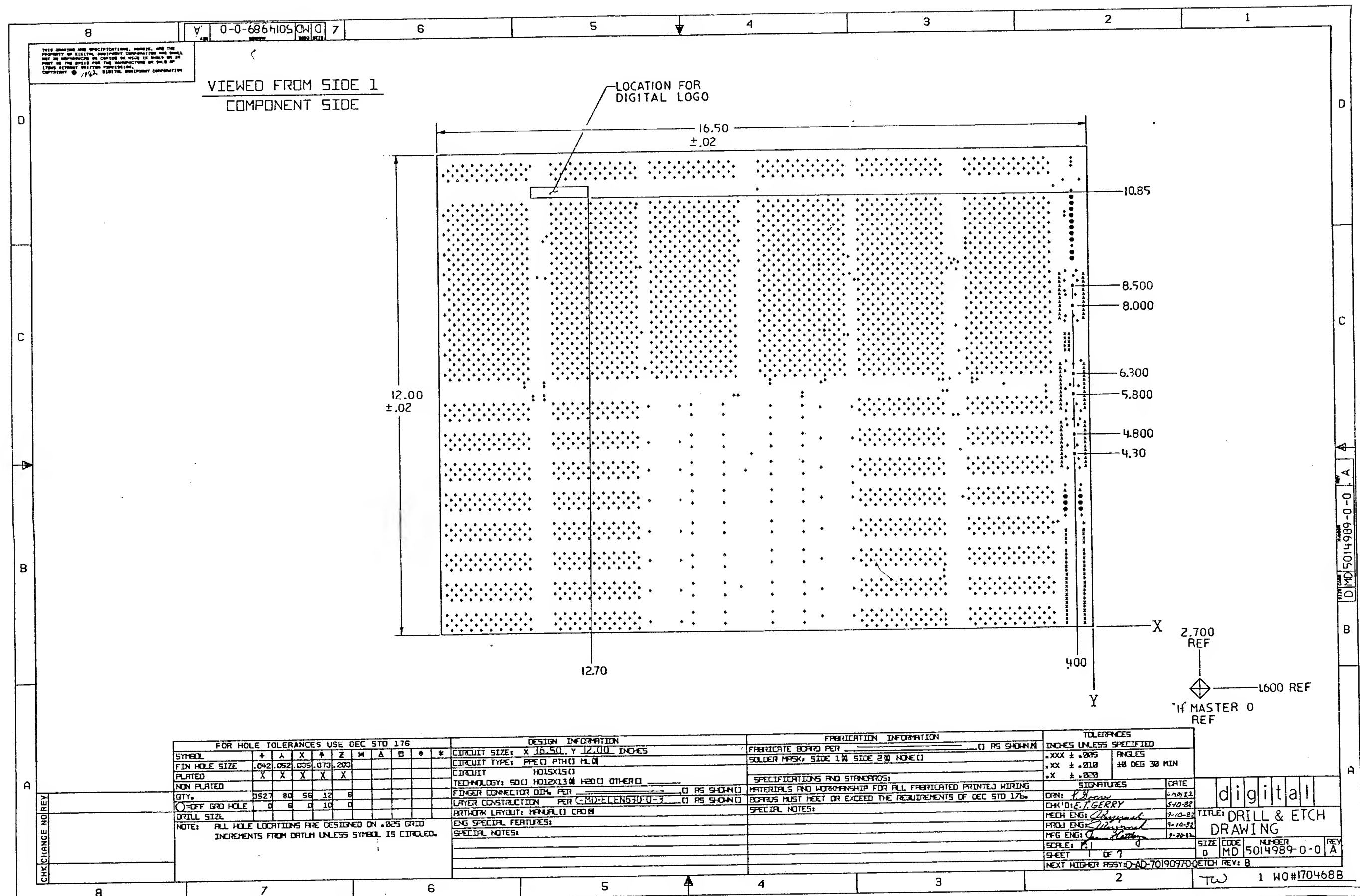




25 NOTE: ITEMS 22 & 23 ARE IN INCHES.

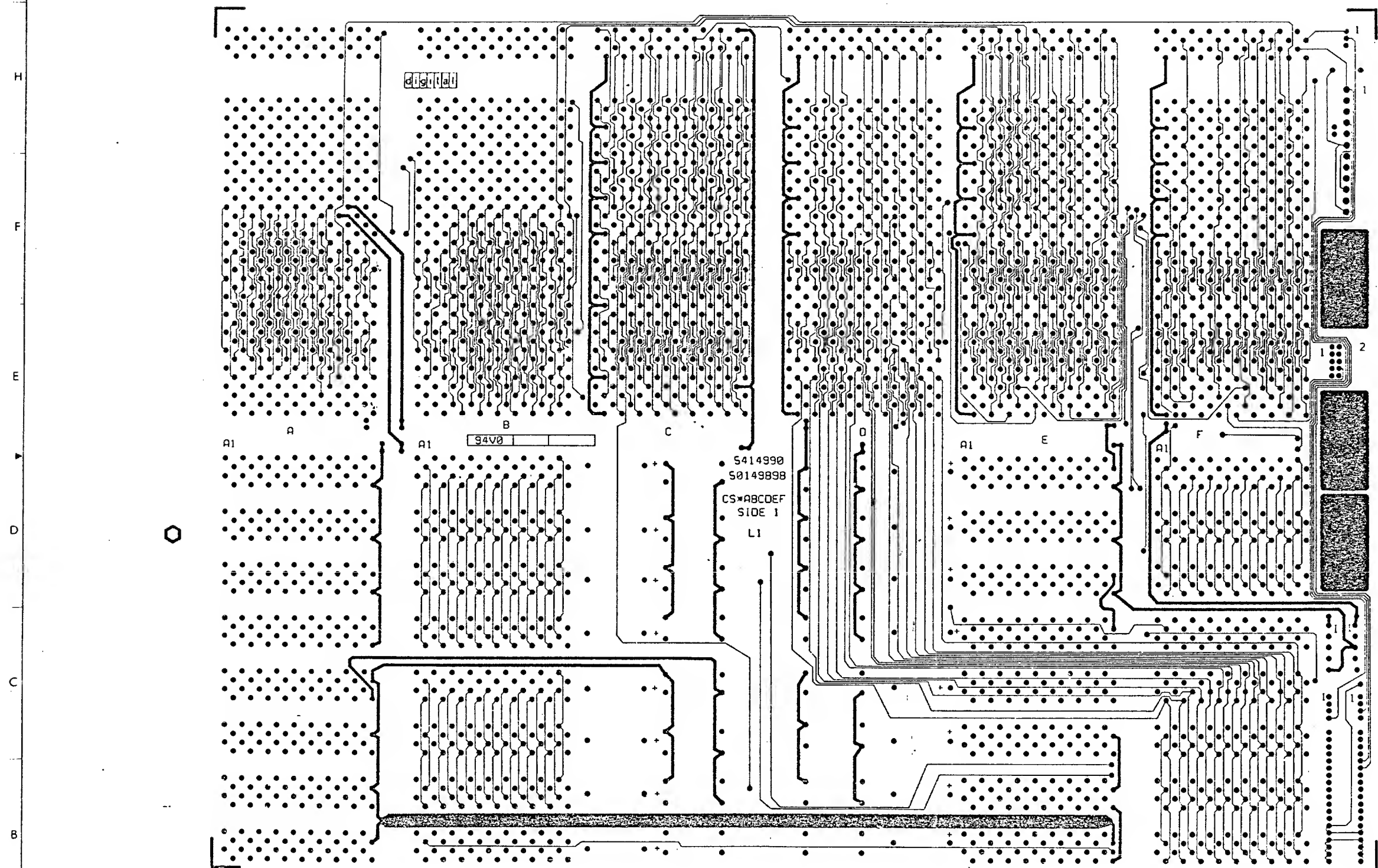
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LAYER 1

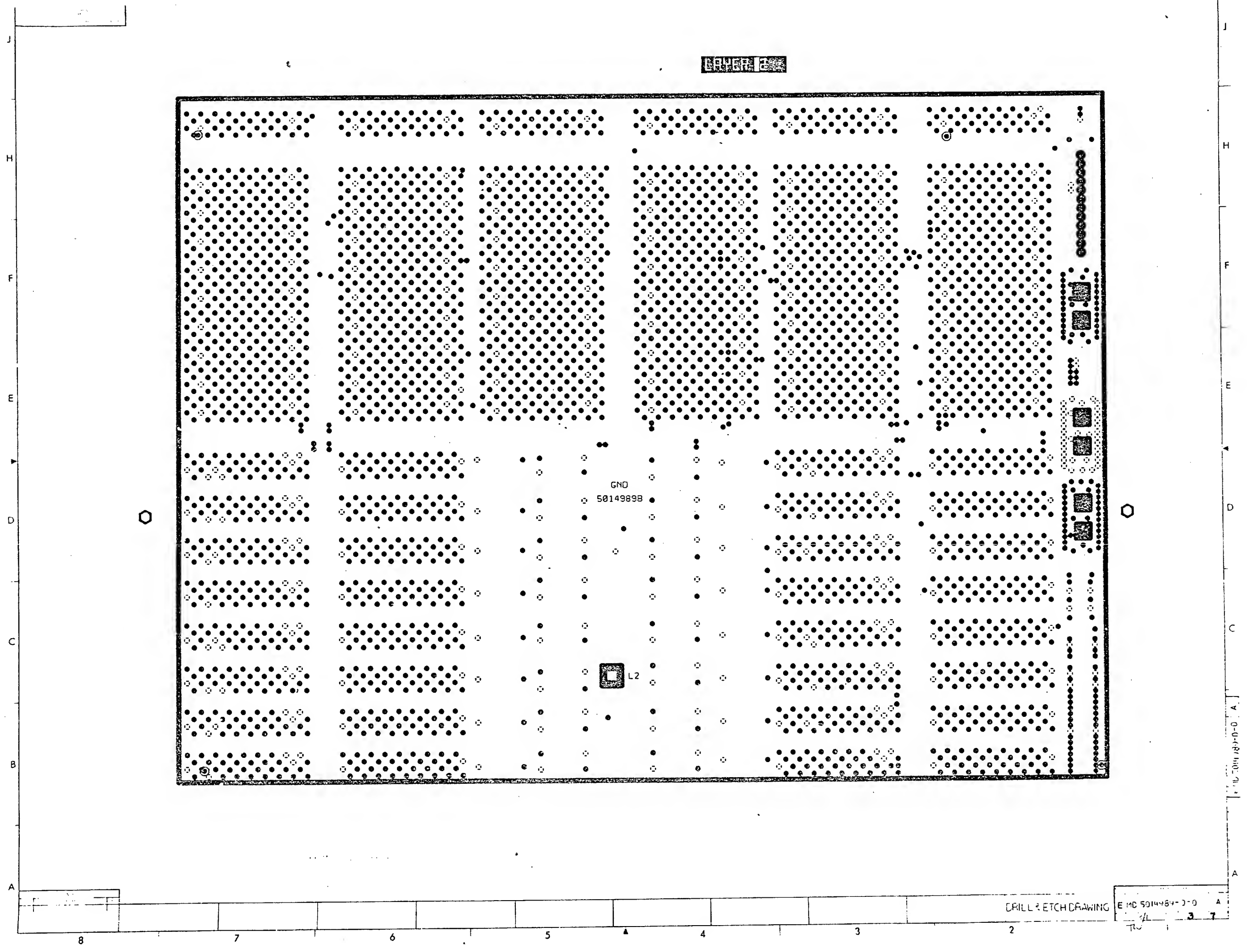


DRILL & ETCH DRAWING

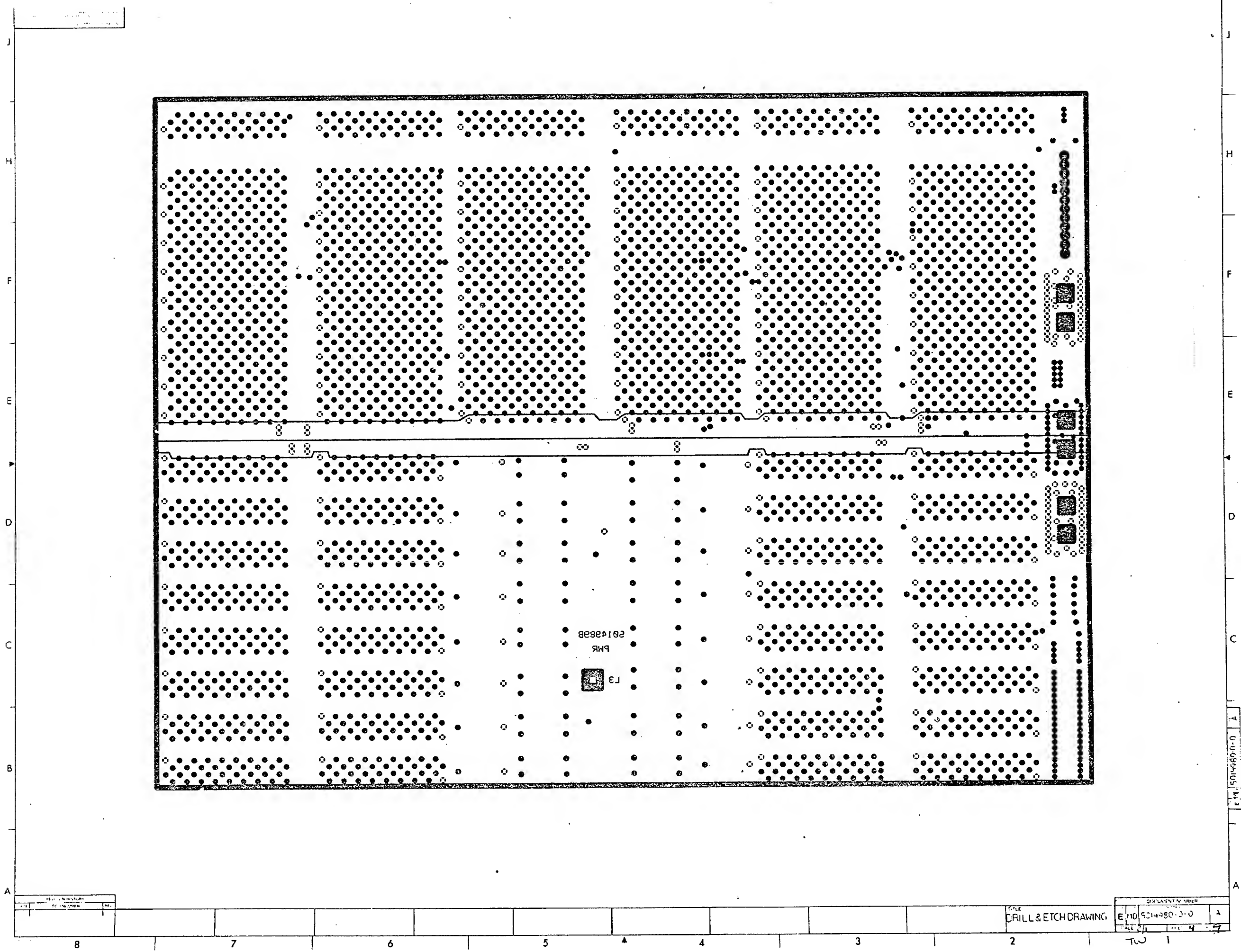
E MD 5014989-0-0 A

1 1









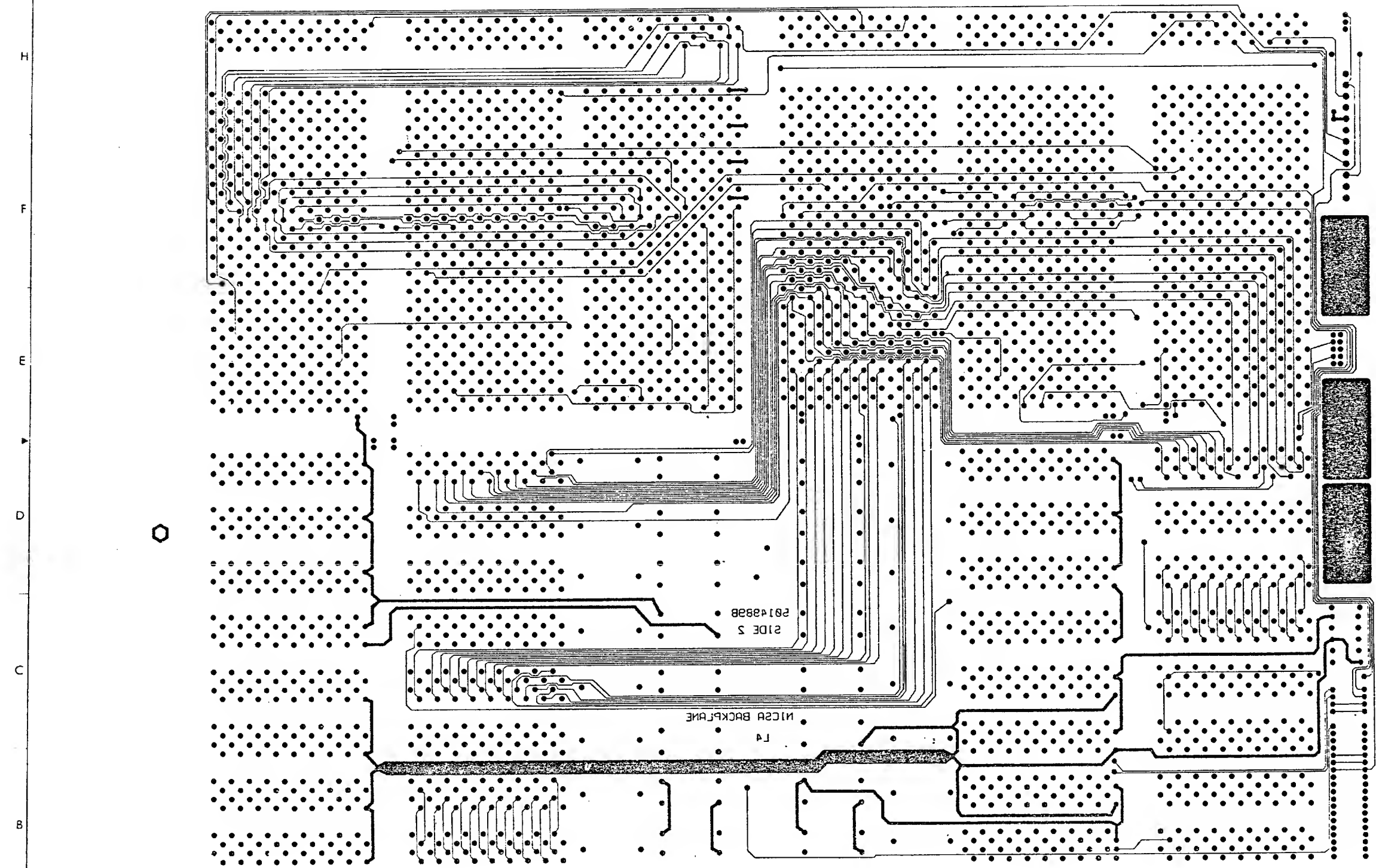
DATE	DESIGNER	CHK

DRILL & ETCH DRAWING

DOCUMENT NO.	DATE	REV
E 110 5014980-0-0	11/11/97	1

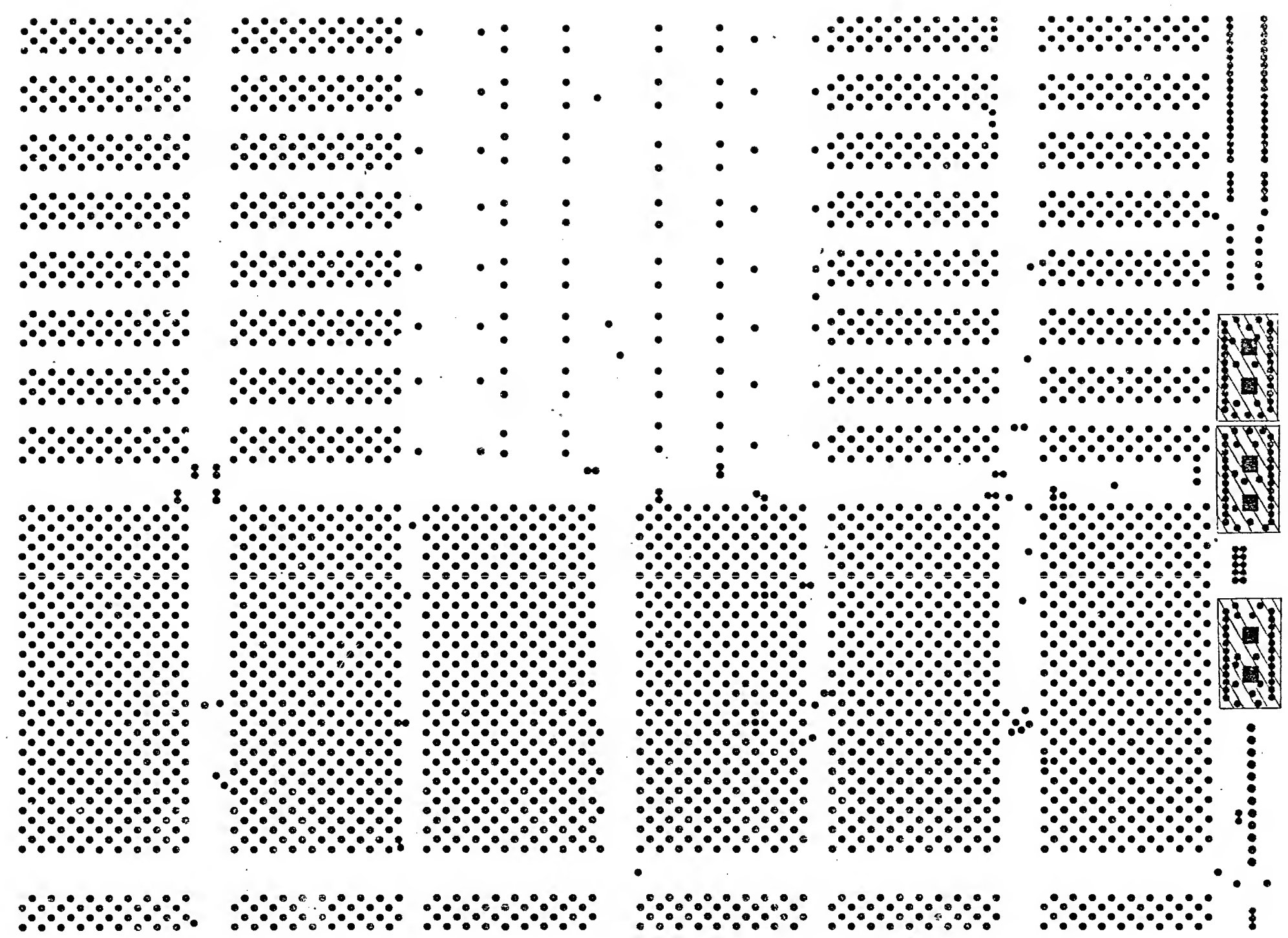
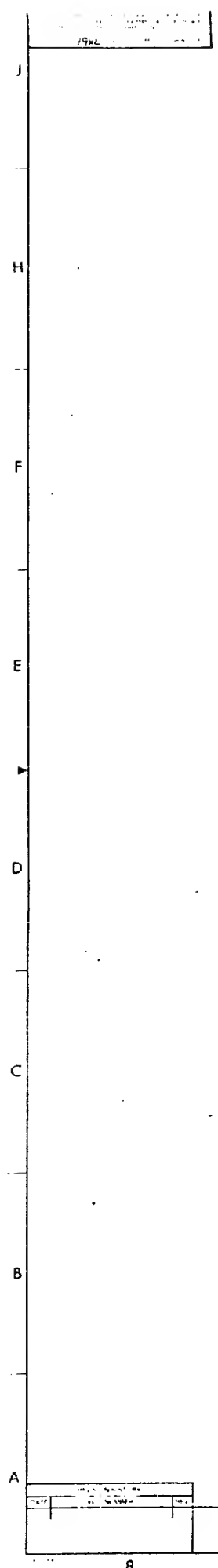


LAYER 4



SIDE 5  
MISER BACKPLANE





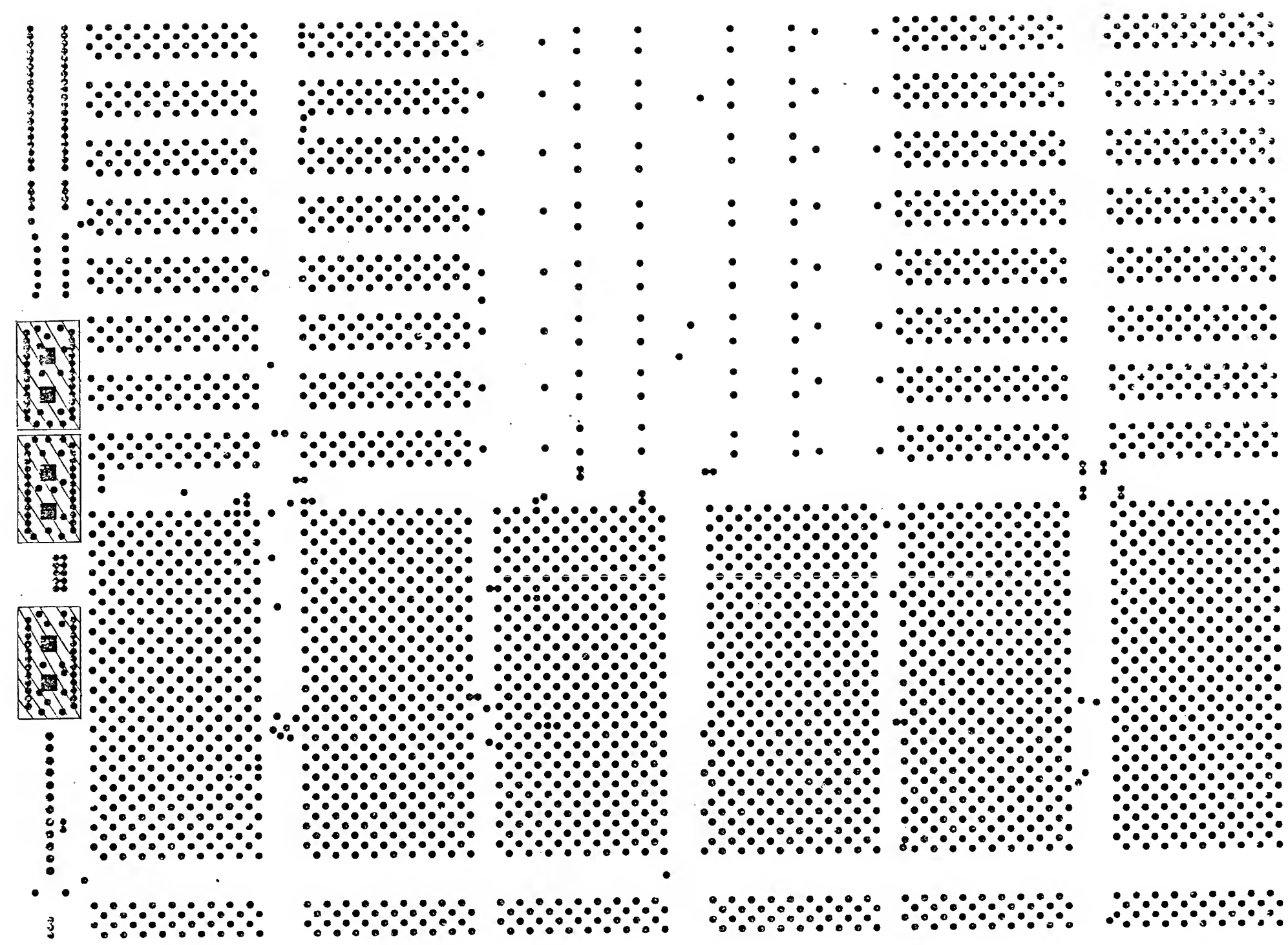
SOLDERMASK REWORK  
VIEWED FROM SIDE I

DRILL & ETCH DRAWING

E MD 501485 0-0 A  
6-7



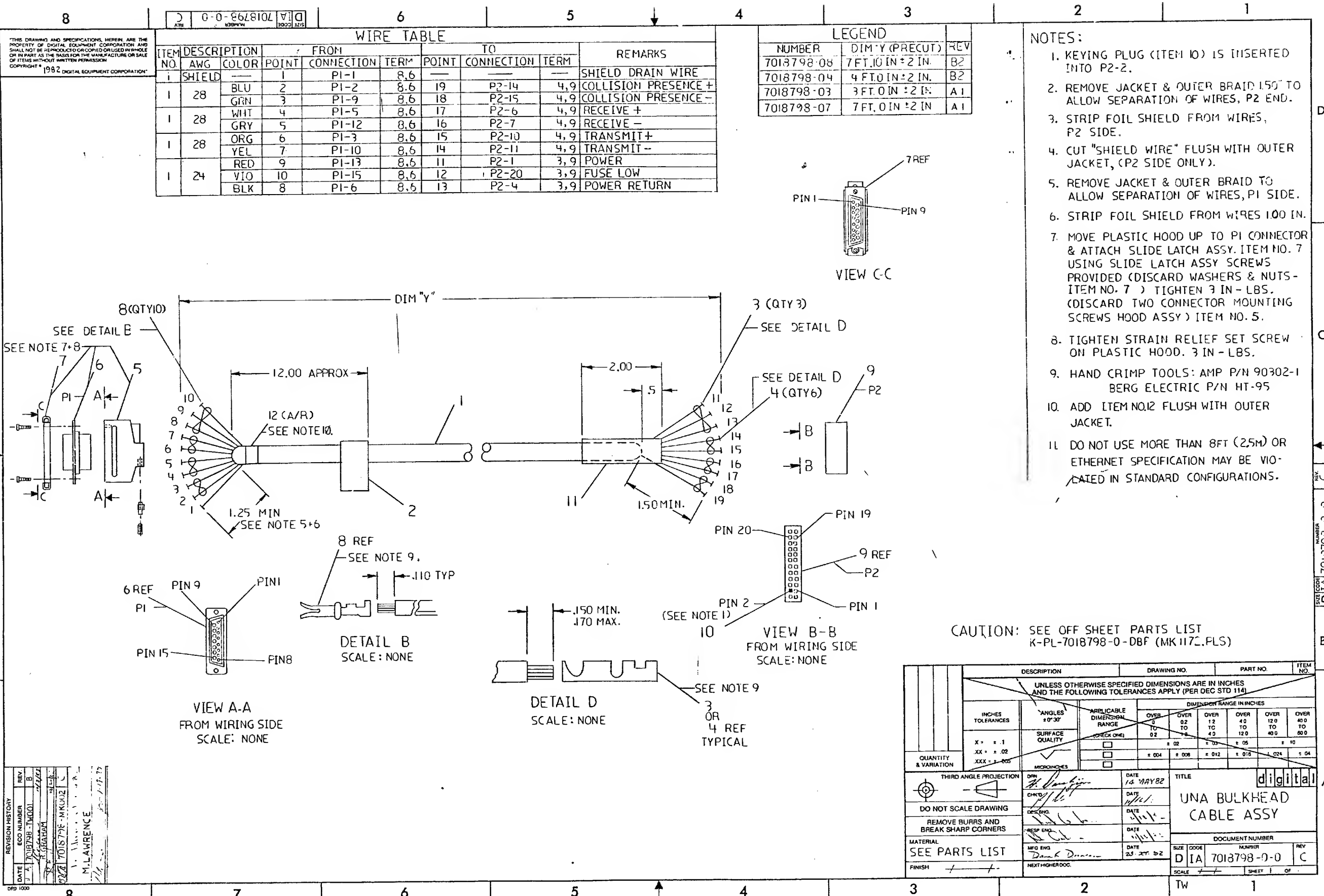
J  
H  
F  
E  
D  
C  
B  
A



1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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DRILL & ETCH DRAWING

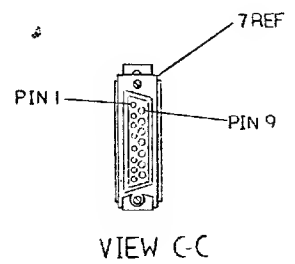




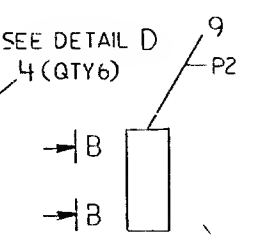
WIRE TABLE									
ITEM NO.	DESCRIPTION	AWG	COLOR	POINT	CONNECTION	TERM	POINT	CONNECTION	TERM
1	SHIELD			1	PI-1	8,6			
1	28		BLU	2	PI-2	8,6	19	P2-14	4,9
			GRN	3	PI-9	8,6	18	P2-15	4,9
1	28		WHT	4	PI-5	8,6	17	P2-6	4,9
			GRY	5	PI-12	8,6	16	P2-7	4,9
1	28		ORG	6	PI-3	8,6	15	P2-10	4,9
			YEL	7	PI-10	8,6	14	P2-11	4,9
1	24		RED	9	PI-13	8,6	11	P2-1	3,9
			VIO	10	PI-15	8,6	12	P2-20	3,9
			BLK	8	PI-6	8,6	13	P2-4	3,9
									REMARKS
									SHIELD DRAIN WIRE
									COLLISION PRESENCE +
									COLLISION PRESENCE -
									RECEIVE +
									RECEIVE -
									TRANSMIT +
									TRANSMIT -
									POWER
									FUSE LOW
									POWER RETURN

LEGEND		
NUMBER	DIM Y (PRECUT)	REV
7018798-08	7 FT. 10 IN ± 2 IN.	B2
7018798-04	4 FT. 0 IN ± 2 IN.	B2
7018798-03	3 FT. 0 IN ± 2 IN.	A1
7018798-07	7 FT. 0 IN ± 2 IN.	A1

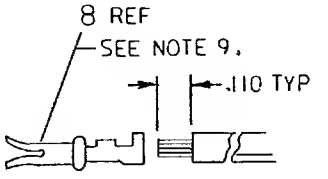
- NOTES:
- KEYING PLUG (ITEM 10) IS INSERTED INTO P2-2.
  - REMOVE JACKET & OUTER BRAID 150" TO ALLOW SEPARATION OF WIRES, P2 END.
  - STRIP FOIL SHIELD FROM WIRES, P2 SIDE.
  - CUT "SHIELD WIRE" FLUSH WITH OUTER JACKET, (P2 SIDE ONLY).
  - REMOVE JACKET & OUTER BRAID TO ALLOW SEPARATION OF WIRES, P1 SIDE.
  - STRIP FOIL SHIELD FROM WIRES 100 IN.
  - MOVE PLASTIC HOOD UP TO P1 CONNECTOR & ATTACH SLIDE LATCH ASSY. ITEM NO. 7 USING SLIDE LATCH ASSY SCREWS PROVIDED (DISCARD WASHERS & NUTS - ITEM NO. 7 ) TIGHTEN 3 IN - LBS. (DISCARD TWO CONNECTOR MOUNTING SCREWS HOOD ASSY) ITEM NO. 5.
  - TIGHTEN STRAIN RELIEF SET SCREW ON PLASTIC HOOD. 3 IN - LBS.
  - HAND CRIMP TOOLS: AMP P/N 90302-1 BERG ELECTRIC P/N HT-95
  - ADD ITEM NO. 12 FLUSH WITH OUTER JACKET.
  - DO NOT USE MORE THAN 8 FT (2.5M) OR ETHERNET SPECIFICATION MAY BE VIOLATED IN STANDARD CONFIGURATIONS.



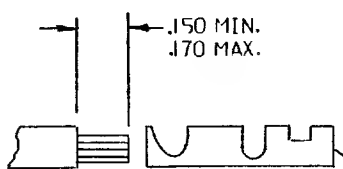
VIEW C-C



VIEW B-B FROM WIRING SIDE SCALE: NONE



DETAIL B SCALE: NONE



DETAIL D SCALE: NONE

CAUTION: SEE OFF SHEET PARTS LIST K-PL-7018798-0-DBF (MK 1172.FLS)

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)			
INCHES TOLERANCES	ANGLES ± 0° 30'	DIMENSION RANGE IN INCHES	
XX ± .02	SURFACE QUALITY	OVER 0 TO 0.2	
XXX ± .005	MICROINCHES	OVER 0.2 TO 0.4	
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	OVER 0.4 TO 0.6	
DO NOT SCALE DRAWING	REMOVE BURRS AND BREAK SHARP CORNERS	OVER 0.6 TO 0.8	
MATERIAL SEE PARTS LIST	FINISH	OVER 0.8 TO 1.0	
NEXT HIGHER DOC.		OVER 1.0 TO 1.2	
DATE 14 MAY 82		OVER 1.2 TO 1.4	
DATE 11/1/82		OVER 1.4 TO 1.6	
DATE 11/1/82		OVER 1.6 TO 1.8	
DATE 23 JUL 82		OVER 1.8 TO 2.0	
TITLE		DOCUMENT NUMBER	
UNA BULKHEAD CABLE ASSY		DIA 7018798-0-0 C	
SCALE 1/1		SHEET 1 OF 1	

REVISION HISTORY	
REV	DESCRIPTION
1	ECO NUMBER 7018798-12001
2	ECO NUMBER 7018798-12002
3	ECO NUMBER 7018798-12003
4	ECO NUMBER 7018798-12004
5	ECO NUMBER 7018798-12005
6	ECO NUMBER 7018798-12006
7	ECO NUMBER 7018798-12007
8	ECO NUMBER 7018798-12008
9	ECO NUMBER 7018798-12009
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11	ECO NUMBER 7018798-12011
12	ECO NUMBER 7018798-12012
13	ECO NUMBER 7018798-12013
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96	ECO NUMBER 7018798-12096
97	ECO NUMBER 7018798-12097
98	ECO NUMBER 7018798-12098
99	ECO NUMBER 7018798-12099
100	ECO NUMBER 7018798-12100



DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS
		5413867-JG	PART REVISION	E1 F1 F2
		5413867-YA	PART REVISION	E1 F1 F2
K-PL-5413867-J-DBP	4		H7211 COMMUNICATION REG	A B C D E F H
D-CS-5413867-J-1	2		H7211 COMMUNICATION REG	A B C D E F F
E-UA-5413867-Q-Q	3		H7211 COMMUNICATION REG	A B C D D E F
K-DJ-5013366-Q	1		DRAWING DIRECTORY	A
		5013866-JG	ETCHED BOARD	9 8 9 9 9 8 9
K-PC-5413867-J-DBG			PC DESIGN DATA BASE	8 8 8 8 9 8 8
NOTES:			B C D E F H	
			P E V	
			R E V I S I O N Y	
			E H I S I S U E C O N T A I N S T H E F O L L O W I N G T A B L E	
			D N D A N M J A O E P O A U T V C R V Y N E 8 8 8 8 8 8 1 2 3 5 6 6	
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			DATE: 25JUN86	
			d i g i t a l	
			CHK'D: B. DUBOIS	
			DATE: 25JUN86	
			TITLE H7211 COMMUNICATION REG	
			DES.ENG.: L. FIELD	
			DATE: 25JUN86	
			SHEET 1 OF 1	
			EDIT: 6	
			RESP.ENG.: L. FIELD	
			DATE: 25JUN86	
			DOCUMENT NUMBER	
			DD FILENAME: 5413867.DDF	
			MFG.ENG.: A. RIVERA	
			DATE: 25JUN86	
			SIZE CODE NUMBER REV	
			K DD 5413867-Q-3 H	

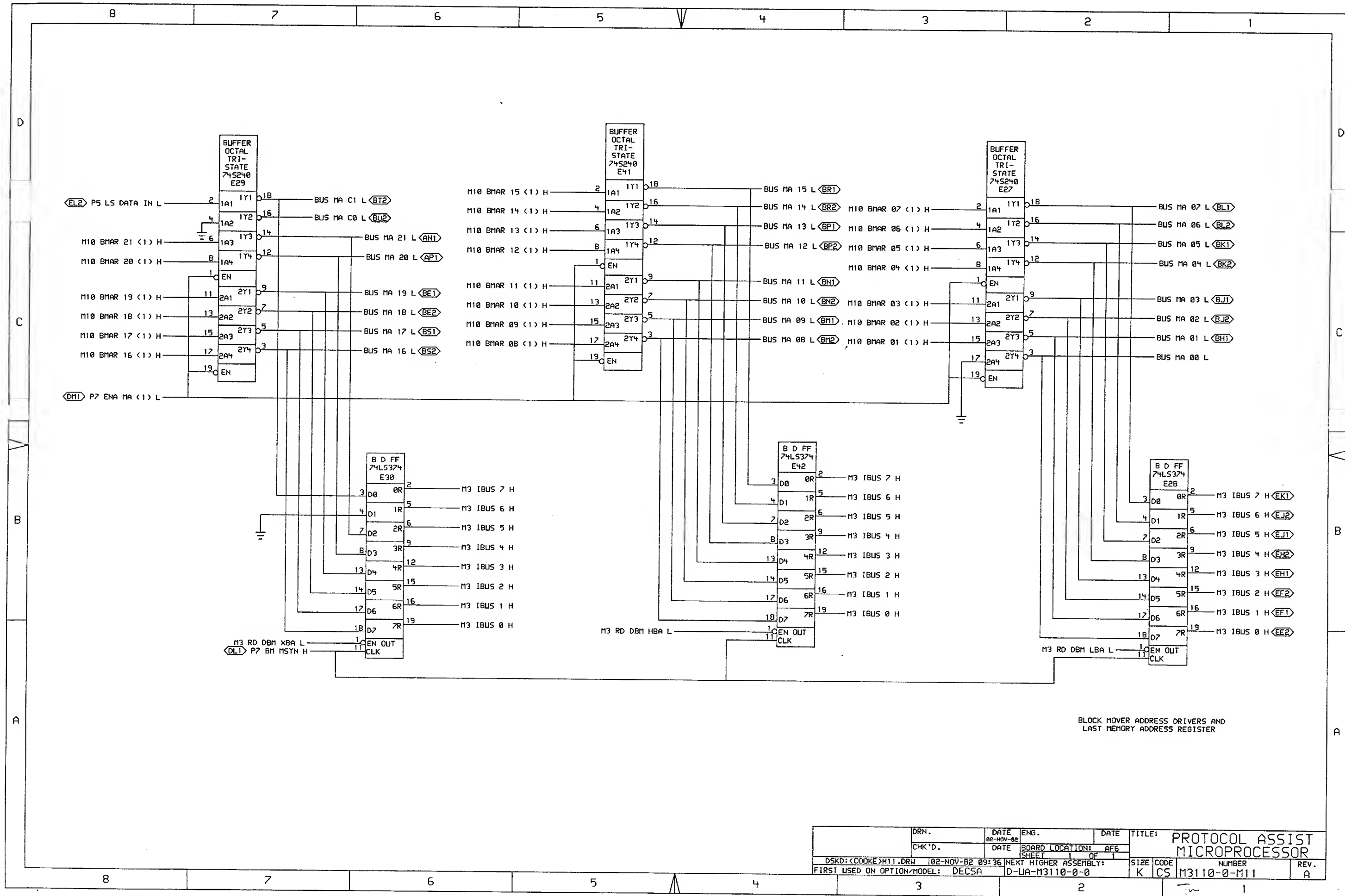


DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS
K-PL-5413857-0-00P	4	5413857	PART REV	- - - - - K1 K2 K3
D-UA-5413857-0-0	3		H7200 MAJOR BOARD	A B C D E F H J K L M
D-CS-5413857-0-1	3		H7200 MAJOR BOARD	A H H D E F H J J K L
K-DD-5013856-C	1	5013856	H7200 MAJOR BOARD	A B C D E F H J L L M
			ETCHED BOARD	- - - - - A
				C C C C C C C C C C
NOTES:				
				R E V
				A B C D E F H J K L M
				R E V I S I O N Y
				E I T T T T T T T T T M
				C N W W W W W W W W W K
				0 I 0 0 0 0 0 0 0 0 0 0
				T 0 0 0 0 0 0 0 1 1 1
				N 1 2 3 4 5 6 7 0 1 2
				D - 1 2 9 1 4 9 1 1 7 5
				A 1 / / / / / / 1 / /
				T / 8 8 8 8 3 8 / 8 3
				E 8 2 2 3 3 3 4 8 5 6
				1 4
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				DATE: 27-JUN-86
				d i g i t a l
CHK'D: R. DUBOIS				DATE: 27-JUN-86
				TITLE
				H7200 MAJOR BOARD
DES. ENG.: C. LANDINO				DATE: 16-NOV-81
				SHEET 1 OF 1
				EDIT: 1
RESP. ENG.: O. PENA				DATE: 27-JUN-86
				DOCUMENT NUMBER
				DD FILENAME: 13857M.DDF
MFG. ENG.: A. RIVERA				DATE: 27-JUN-86
				SIZE CODE NUMBER REV
				K DD 5413857-0 M





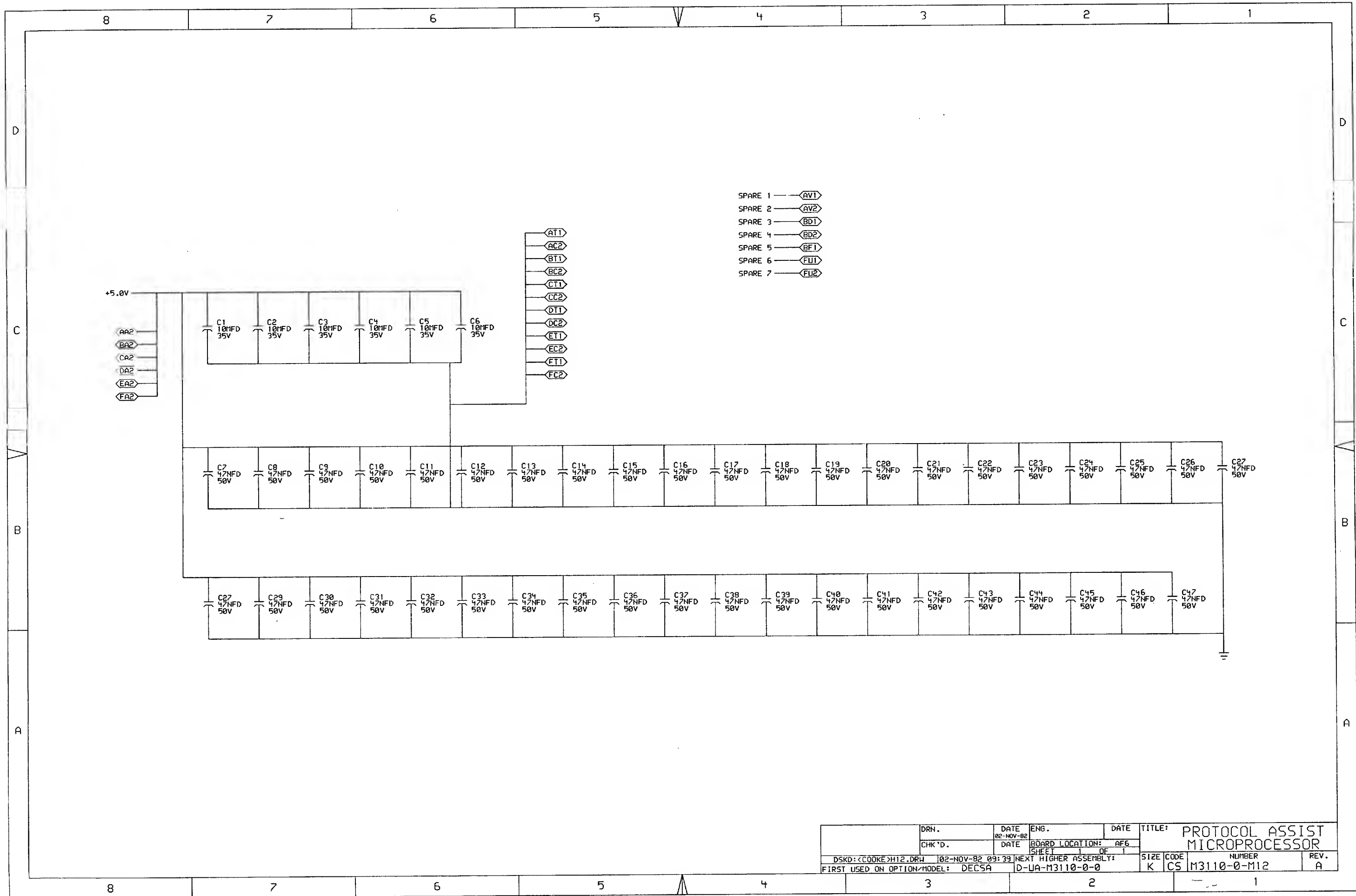




BLOCK MOVER ADDRESS DRIVERS AND  
LAST MEMORY ADDRESS REGISTER

DRN.	DATE	ENG.	DATE	TITLE:	PROTOCOL ASSIST
CHK'D.	02-NOV-82			BOARD LOCATION:	AF6
DSKD: <COOKE>H11.DRW	02-NOV-82 09:36	NEXT HIGHER ASSEMBLY:	SHEET 1 OF 1	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: DECSA	D-UA-M3110-0-0			K CS	M3110-0-M11
				REV.	A





DRN.	DATE	ENG.	DATE	TITLE: PROTOCOL ASSIST MICROPROCESSOR
CHK'D.	DATE	BOARD LOCATION: AF6		
DSKD: <COOKE>H12.DRW	102-NOV-82 09:39	NEXT HIGHER ASSEMBLY: D-WA-M3110-0-0	SIZE CODE NUMBER	REV.
FIRST USED ON OPTION/MODEL: DECSA			K CS M3110-0-M12	A







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DRAWING DIRECTORY

ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS									
						E2	E3	E4							
						E2	E3	E4							
						C	E	E							
						D1	D2	D3							
1			M3110-0	MODULE REV											
2	D-UA-M3110-0	2		P.A.M.											
3	K-PL-M3110-0-DBP	2		P.A.M.											
4	E-EC-5014976-0-0	3		ETCH CIRCUIT DRAWING											
5	D-MD-5014976-0-0	5		DRILL AND ETCH DRAWING											
6			5014976	ETCHED BOARD											
7	K-CS-M3110-0-M1	1		P.A.M.											
8	K-CS-M3110-0-M2	1		P.A.M.											
9	K-CS-M3110-0-M3	1		P.A.M.											
10	K-CS-M3110-0-M4	1		P.A.M.											
11	K-CS-M3110-0-M5	1		P.A.M.											
12	K-CS-M3110-0-M6	1		P.A.M.											
13	K-CS-M3110-0-M7	1		P.A.M.											
14	K-CS-M3110-0-M8	1		P.A.M.											
15	K-CS-M3110-0-M9	1		P.A.M.											
16	K-CS-M3110-0-M10	1		P.A.M.											
17	K-CS-M3110-0-M11	1		P.A.M.											
18	K-CS-M3110-0-M12	1		P.A.M.											
19	K-CS-M3110-0-M13	1		P.A.M.											
20	K-CS-M3110-0-M14	1		P.A.M.											
21	K-PC-M3110-0-DBI			PC DATA BASE											
22	K-CS-M3110-0-DBS			CS DATA BASE											

NOTES: SHEET 2 FOR ETCHED BOARD REV C, "SPECIAL REVS"

DD  
REV

H

J

K

REVISION HISTORY

CONT. REVISION HISTORY

MADE BY: T. WALSH  
DATE: 22 OCT 82

CHECKED BY: T. WALSH  
DATE: 22 OCT 82

DESIGN ENGINEER: J. BUZYNSKI  
DATE: 22 OCT 84

RESPONSIBLE ENG: M. LAWRENCE  
DATE: 22 OCT 82

PRODUCTION ENG: B. BROOKE  
DATE: 22 OCT 82

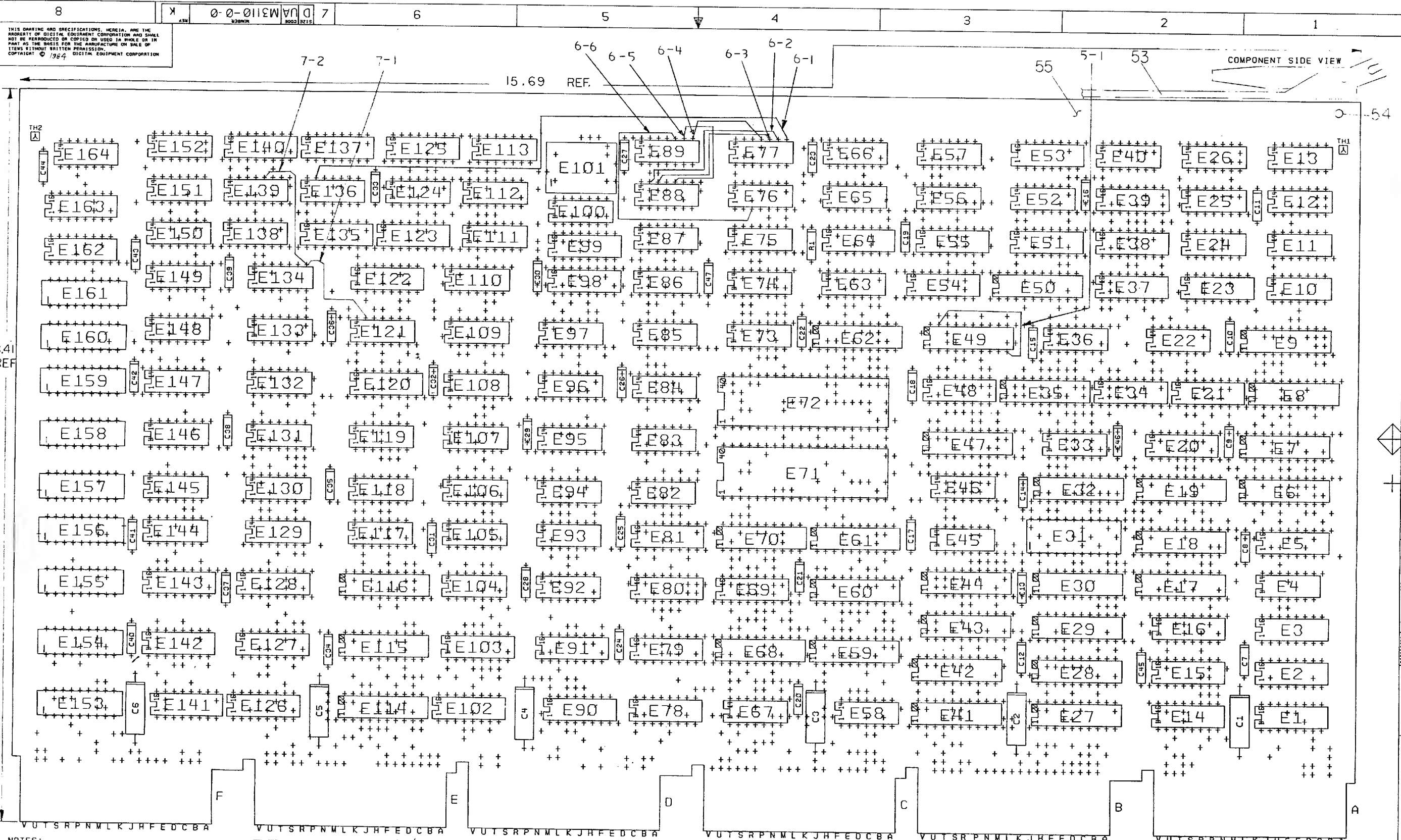
d i g i t a l

TITLE DRAWING DIRECTORY

PROTOCOL ASSIST MICROPROCESSOR

SIZE CODE DOCUMENT NUMBER REV.  
K DD M3110-0-0 K





NOTES:

STEP E	↑ Y AXIS	STEP O	TIMES
REPEAT	→ X AXIS	STEP O	TIMES

CHANGE NO	REV	DATE	BY	CHKD	APPD
M3110	1	4-13-84	F		
M3110	2	4-13-84	F		
M3110	3	4-13-84	F		
M3110	4	4-13-84	F		
M3110	5	4-13-84	F		
M3110	6	4-13-84	F		
M3110	7	4-13-84	F		
M3110	8	4-13-84	F		
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M3110	163	4-13-84	F		
M3110	164	4-13-84	F		

ETCH REV. DIP 2
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SIGNATURES		DATE	digital			
DRN. P. GROSSE		4-13-84				
CHK'D. F. GAROFALO		4-13-84				
MECH. ENG. <i>[Signature]</i>		4-13-84				
PROJ. ENG. <i>[Signature]</i>		4-13-84				
PROD. <i>[Signature]</i>		4-6-84	TITLE PROTOCOL			
SCALE 2:1			ASSIST MICRO PROC			
SHT. 1 OF 2		SIZE	CODE	NUMBER	REV	
NEXT HIGHER ASSY. K-DD-M3110-0		0	UA	M3110-0-0	K	



8

REV	0-0-011EW	SIZE	D
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1

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REWORK INSTRUCTIONS: M3110-MK005.

WIRE ADDS SIDE 1: (P/N 9105740-55)  
5-1 FROM E49-10 TO E49-19.

REWORK INSTRUCTIONS: M3110-MK006: ETCH REV. D1.

WIRE ADDS SIDE 1 (P/N 9105740-55) SEE E-EC-5214976-0-0 REV. F FOR ETCH CUTS.

- 6-1 FROM E77-8 TO E136-13.
- 6-2 FROM E77-9 TO E88-13.
- 6-3 FROM E77-10 TO E88-12.
- 6-4 FROM E77-11 TO E89-8.
- 6-5 FROM E89-9 TO E88-10.
- 6-6 FROM E89-10 TO E76-3.

REWORK INSTRUCTIONS: M3110-MK007: ETCH REV D1

WIRE ADDS SIDE 1: (P/N 9105740-55) (SEE E-EC-5014976-0-0 REVH FOR ETCH CUTS)  
7-1 FROM E134-8 TO E121-13  
7-2 FROM E134-8 TO E139-10

D

C

B

A

D

C

B

A

REV	K
NUMBER	2-2-2
SIZE CODE	D

REVISION HISTORY		
REV	ECO NUMBER	DATE

8

7

6

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2

1

TITLE  
PROTOCOL  
ASSIST MICRO PROC

DOCUMENT NUMBER			
SIZE CODE	NUMBER	REV.	
DUA	M3110-0-0	K	
SCALE	NONE	SHEET	2 OF 2

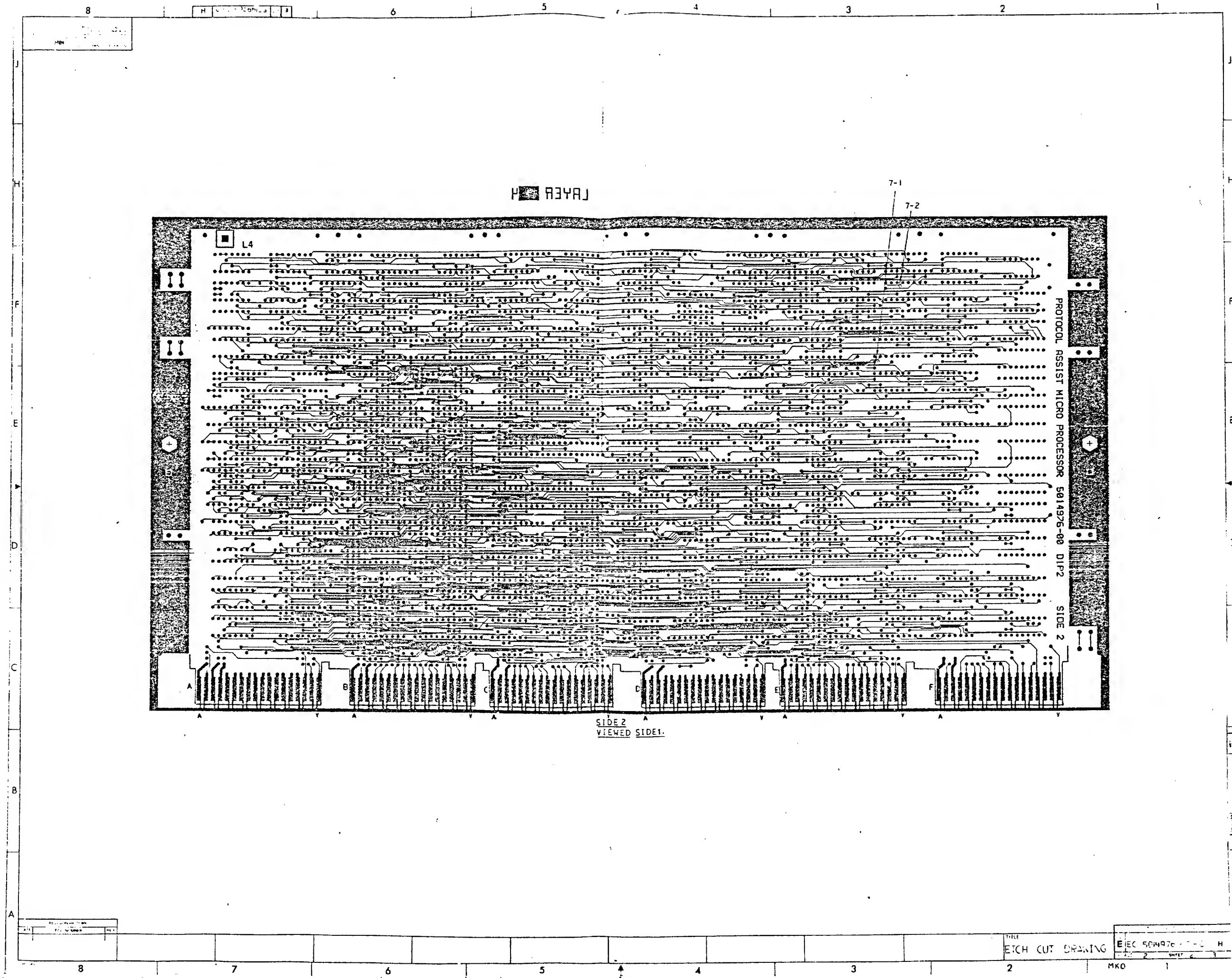
MKO

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P 83YAJ

L4

PROTOCOL ASSIST MICRO PROCESSOR 5014976-00 DIP2 SIDE 2

SIDE 2  
VIEWED SIDE 1

ETCH CUT DRAWING  
EIEC 5014976-00  
MKO



A	REVISIONS		
	TIME	CHANGE NO.	BY

REWORK INSTRUCTIONS: MP112-MP226 ETCH REV. D1:

ETCH CUTS SIDE 1:

6-1 AT E196-17.

REWORK INSTRUCTIONS: M3110-MK007 ETCH REV. 01

ETCH CUTS SIDE 2:

7-1 AT E134-6

7-2 AT E139-10

TITLE	ETCH CUT DRAWING
-------	------------------

SIZE CODE	NUMBER	REV
E	21-1-1	4



LINE	ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION	MIN	QTY PER VARIATION	REFERENCE DESIGNATOR
						00	00	
						VARIATION REVISION LEVEL:		
1	55	D-MD-5014976-0-0	5014976-00		DRILL & ETCH BD		1	
2	50		1012784-00		.047 MFD 50V +80-20% CER		41	C7-C47
3	49		1017472-00		10 MFD 35V +75-10% AL EL		6	C1-C6
4	53		1216988-02		HANDLE,MODULE,HEX TWO EJECTORS		1	
5	37		1300005-01		R NETWORK 13-1K 5.0 % 14PIN		2	E96,E117
6	47		1300316-00		470.0 .25 W 5.0 % CF		1	R1
7	52		1811660-39		OSCILLATOR, XTAL 40.5504 MHZ		1	E101
8	13		1910532-00		74S00 NAND GATE-QUAD 2IN		6	E25,E66,E93,E108,E133,E139
9	7		1910534-00		74S04 INVERTER GATE-HEX 1I		5	E11,E76,E118,E121,E130
10	12		1910536-00		74S10 NAND GATE-TRIPLE 3IN		3	E24,E77,E136
11	27		1910537-00		74S11 AND GATE-TRIPLE 3INP		4	E52,E92,E106,E149
12	29		1910539-00		74S20 NAND GATE-DUAL 4INPU		4	E56,E75,E88,E134
13	8		1910542-00		74S64 A-O-I GATE 4-2-3-2		5	E12,E13,E26,E131,E150
14	2		1910544-00		74S74 FF-D DUAL,EDGE TRIGG		7	E4,E74,E83,E85,E94,E107,E119
15	48		1910544-01		74S74-60GG-D DUAL,EDGE TRIG		4	E86,E95,E113,E124
16	39		1910545-00		74S112 FF-JK DUAL,EDGE TRIG		10	E98,E99,E120,E122,E123,E125, E135,E137,E162,E163
17	11		1910548-00		74S157 MUX 1 OF 2 (QUAD)		3	E23,E34,E48
18	3		1910550-00		74S174 FF-D HEX		2	E5,E20
19	20		1910956-00		74S151 MUX 1 OF 8		1	E37
20	21		1910957-00		74S175 FF-D QUAD COMMON CLO		3	E38,E51,E53
21	36		1911675-00		74S138 DECODER/DEMUX 3-8 LI		2	E90,E102
22	46		1911712-00		74S51 AND-OR GATE-INVERT D		1	E146
23	40		1912388-00		74S02 NOR GATE-QUAD 2IN,PO		4	E109,E110,E111,E152
24	32		1912389-00		74S08 AND GATE-QUAD 2IN,PO		5	E65,E82,E89,E105,E138
25	42		1912647-00		LS257 MUX 1 OF 2 (QUAD)		3	E126,E127,E128
26	35		1912660-00		74S253 MUX 1 OF 4 (DUAL)		6	E78,E79,E80,E81,E91,E103
27	28		1912728-00		74S251 MUX 1 OF 8 TRI-STA		3	E54,E55,E69
28	38		1912746-00		DEC 74S37 NAND GATE-QUAD 2IN		3	E97,E112,E148
29	24		1912799-00		LS00 NAND-GATE-QUAD 2IN,P		2	E46,E144

REVISION HISTORY			BASIC PART NO: M3110		DRN: P.TELLIER		DATE: 22-NOV-82		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D: <i>J. Gargano</i>		DATE: 22-NOV-82		TITLE PARTS LIST	
---	INITIAL	A	SECTION VARIATION INDEX		F. GARGANO		31-AUG-84		PROTOCOL ASSIST MICRO PROC	
JB	M3110-TW002	B	[A] 00							
JB	M3110-TW03A	C	[B]							
JB	M3110-TW04A	D	[C]		DES. ENG: J. COOKE		DATE: 22-NOV-82		DOCUMENT NUMBER	
JD	M3110-MK005	E	[D]						SIZE CODE NUMBER	
			[E]		RESP. ENG: <i>J. Cooke</i>		31-AUG-84			
			[F]				DATE: 06-AUG-84		K PL M3110-0-DHP	
			[H]							
			[J]		MFG. ENG.: R. WHITE		DATE: 06-AUG-84		RELEASE DATE: 31-AUG-84	
			[K]							
			[L]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	
			[M]		D-UA-M3110-0-0		B-DD-M3110-0		MK1134.PLS	
			[N]						EDIT #	
									26	

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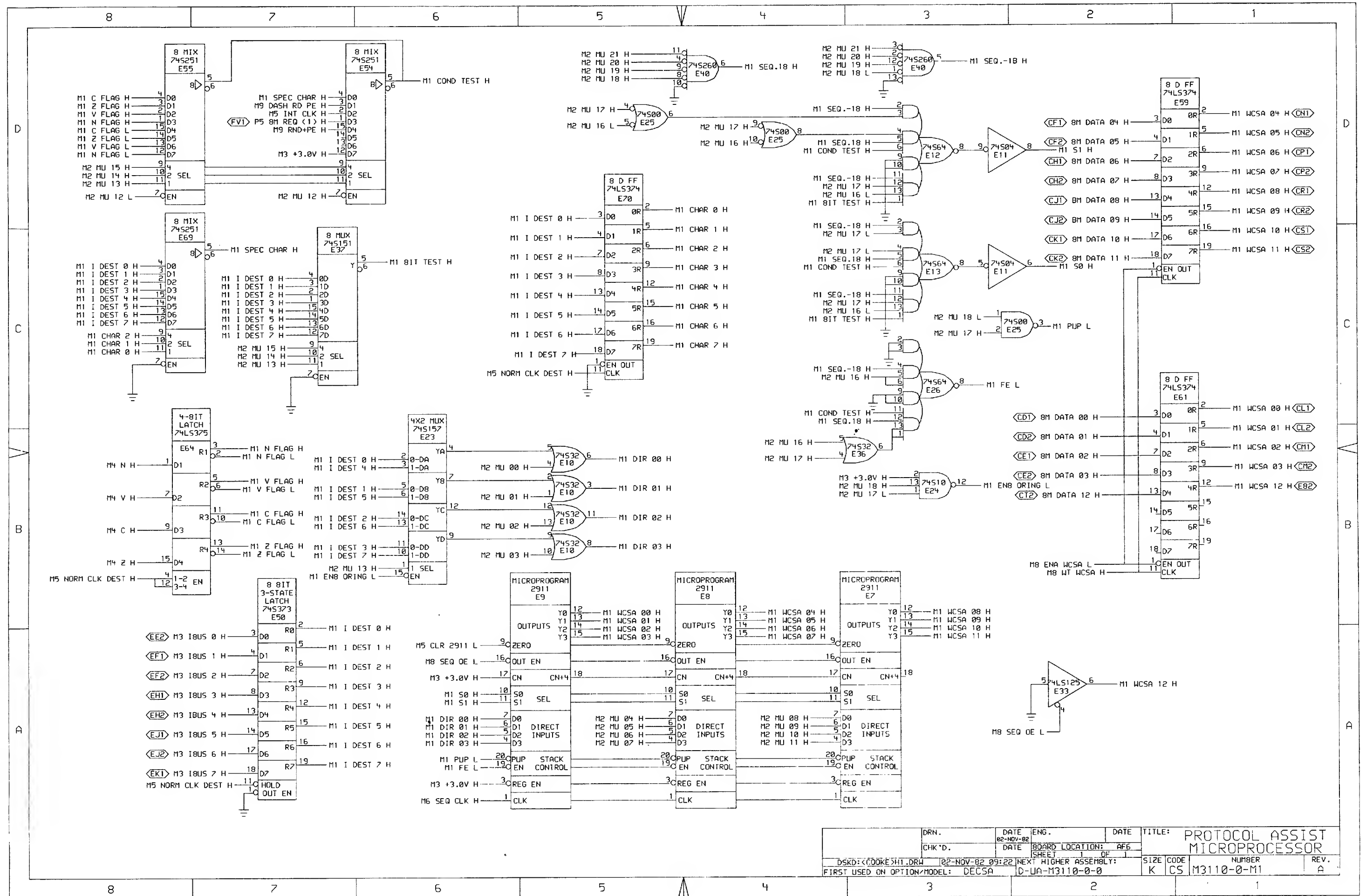


LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					VARIATION REVISION LEVEL:	00	
30	23		1912805-00		LS08 AND GATE-QUAD 2IN,PO	2	E45,E73
31	43		1912808-00		LS11 AND GATE-TRIPLE 3IN	1	E129
32	44		1912813-00		LS27 NOR GATE-TRIPLE 3IN	1	E132
33	30		1912824-00		LS74 FF-D DUAL,EDGE TRIGG	2	E58,E63
34	45		1912833-00		LS109 FF-JK DUAL,POS EDGE	1	E140
35	31		1912871-00		LS375 LATCH	1	E64
36	34		1913245-00		2901 BIPOLAR MICROPROCESSO	2	E71,E72
37	6		1913340-00		74S32 OR GATE-QUAD 2IN	5	E10,E36,E87,E104,E145
38	14		1913462-00		74S240 OCTAL BUFFER,INVERTI	3	E27,E29,E41
39	26		1913670-00		74S373 LATCH,8BIT TRANS TRI	2	E50,E62
40	41		1913671-00		74S374 FF-D,OCTAL,TRI STATE	2	E115,E116
41	1		1913939-00		LS191 COUNTER,SYNCHR. UP/D	9	E1,E2,E3,E14,E15,E16,E141,E142,
							E143
42	22		1914085-00		74S260 NOR GATE-DUAL,POS	6	E40,E57,E84,E100,E147,E151
43	15		1914214-00		LS374 FF-D OCTAL EDGE TRIG	6	E28,E30,E42,E59,E61,E70
44	9		1914866-00	AM	93S48PCGEN/CHECK PARITY, 4B	2	E21,E39
45	19		1915193-00		LS244 DRIVER,LINE,OCTAL,T	3	E35,E47,E114
46	17		1915218-00		LS245 TRANSCEIVER,BUS,OCT	3	E32,E44,E60
47	18		1915932-00		LS125 BUFFER,QUAD BUS	1	E33
48	5		1916680-01		2911A MICROPROGRAM SEQUENC	3	E7,E8,E9
49	51		2114523-00		4K MOS RAM 55NS 18PIN	8	E153-E160
50	4		2119250-00		4KX4 STATIC RAM 55NS	6	E6,E17,E18,E19,E43,E68
51	25		2308381-00	B1-01		1	E49
52	16		2306502-00	D2-01		1	E31
53	54		9009000-00		EYELET,ROLLED 0.1210DX0.156	12	
54	57		1215006-05		SKT,IC 22PIN DIP TIN SOLD	1	XE31
55	58		1914086-00		74S30 NAND GATE-POS 8IN	1	E164
56	59		1921315-01		74F86 EXCLUSIVE OR GATE,QU	1	E22
57	60		2114523-03		2147H-1 4K MOS RAM 35NS 18	1	E161
58	61		1921332-01		74F280 PARITY GEN/CHECK,9-B	1	E67
59	62		9105740-55		WIRE(WRAP) 30AWG KYNAR UL14	A/R	

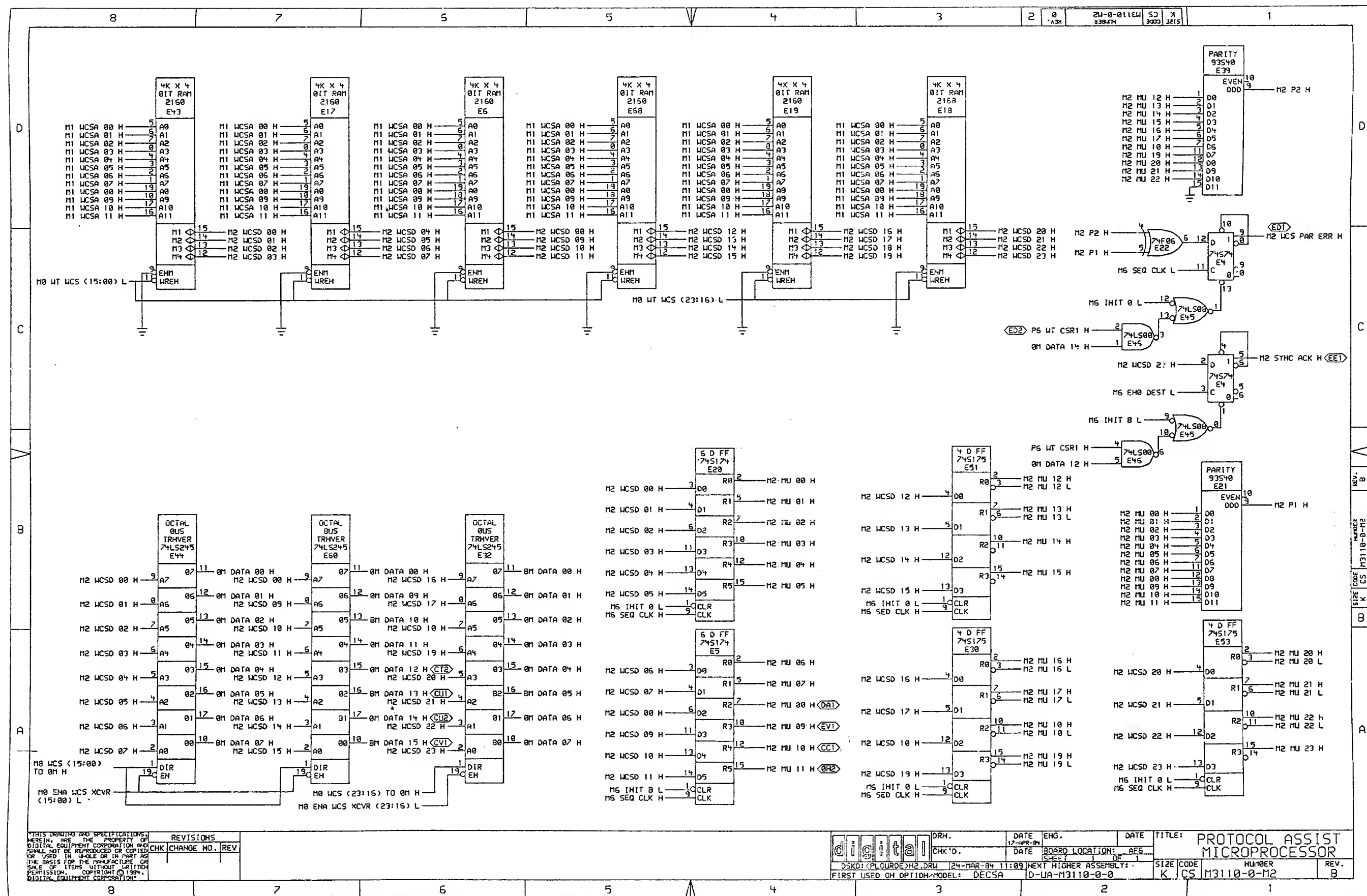
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D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
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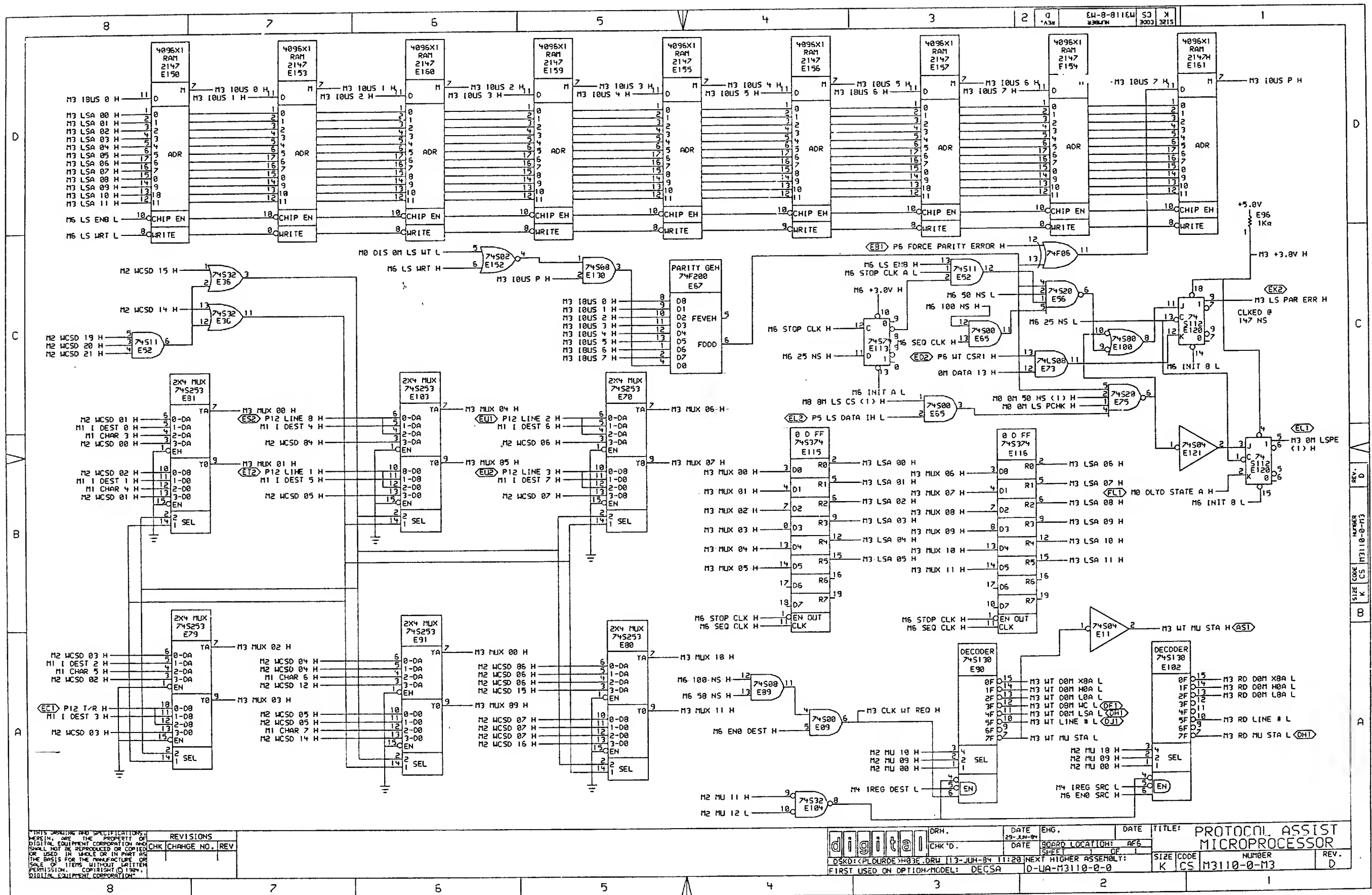












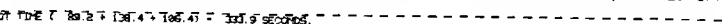
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REVISIONS		
CHK	CHANGE NO.	REV

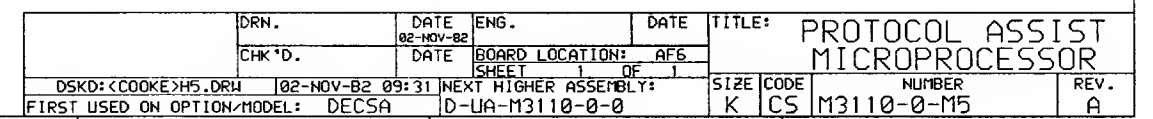
digital	DRH.	DATE	ENG.	DATE	TITLE: PROTOCOL ASSIST MICROPROCESSOR
	CHK'D.	DATE	BOARD LOCATION: AF5	SHEET	NUMBER
	FIRST USED ON OPTION/MODEL: DEC5A 10-0A-M3110-0-0				

SIZE	CODE	NUMBER	REV.
K	CS	M3110-0-M3	D

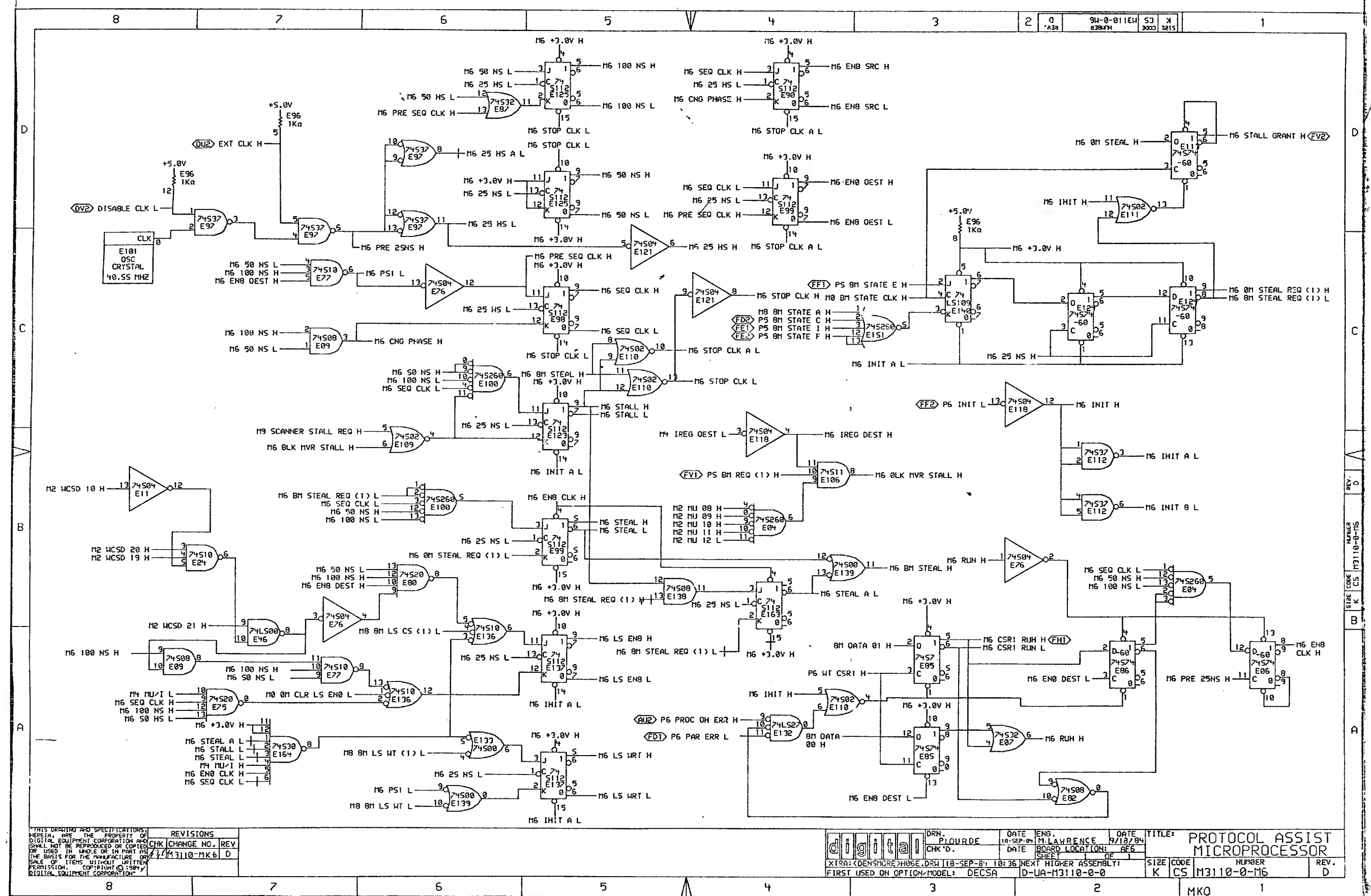




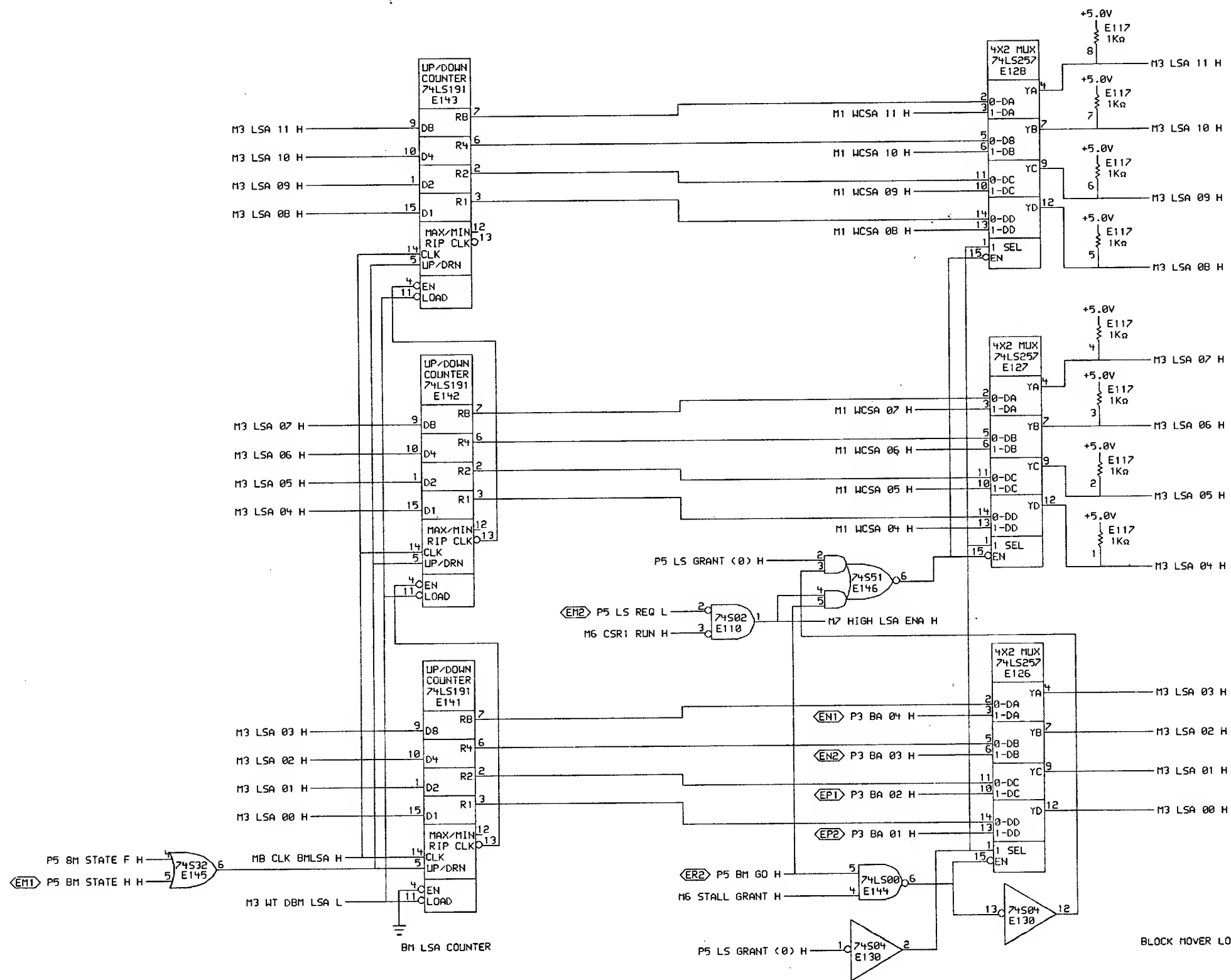








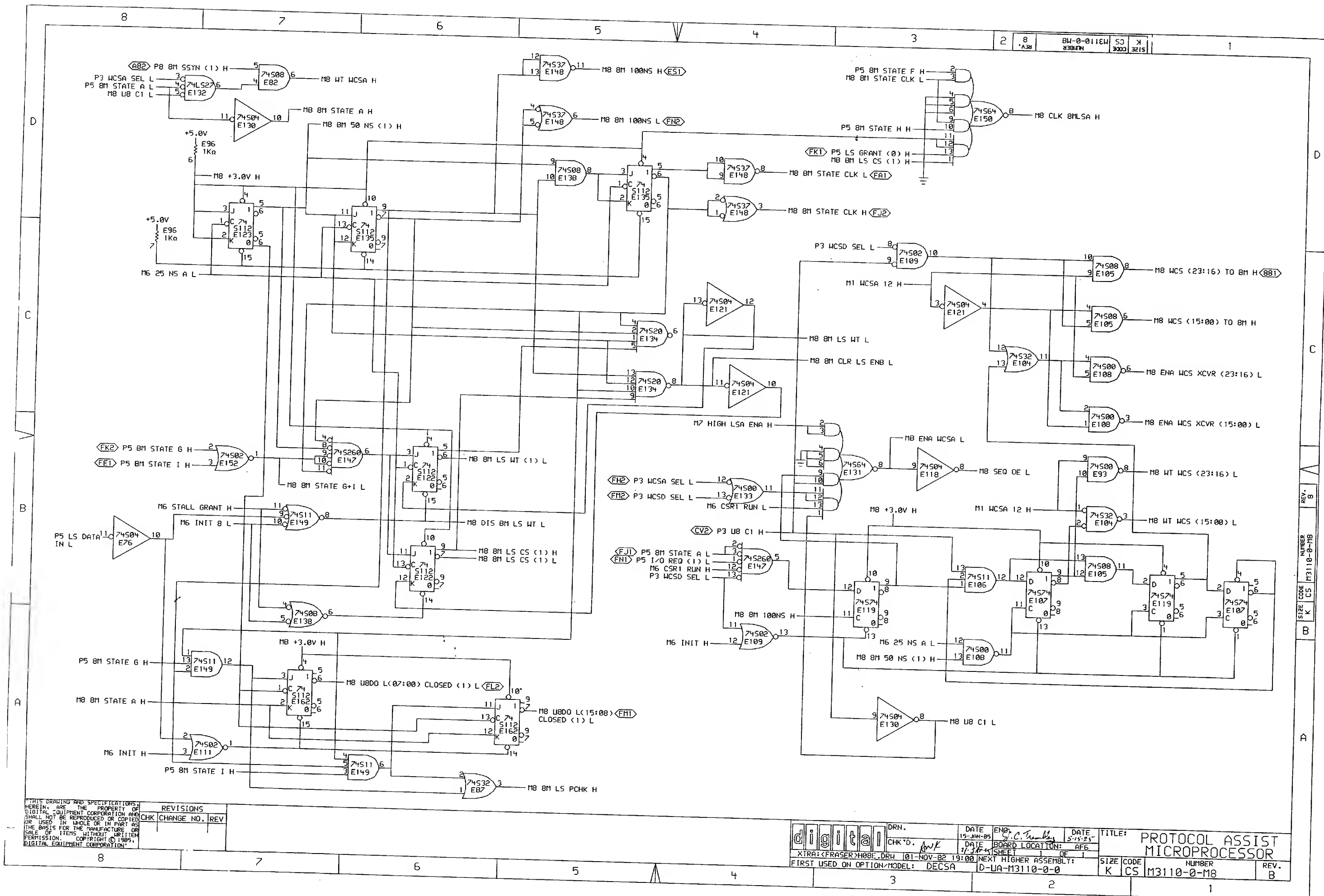




BLOCK MOVER LSA  
WD CNT, & DMA CNTR

DRN.	DATE	ENG.	DATE	TITLE:	PROTOCOL ASSIST MICROPROCESSOR			
CHK'D.	DATE	BOARD LOCATION:	AF6	SHEET	1	OF	1	
DSKD: <COOKE> M7.DRW	02-NOV-82	09:34	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER	REV.	
FIRST USED ON OPTION MODEL:	DECSA	D-UA-M3110-0-0		K	CS	M3110-0-M7	A	

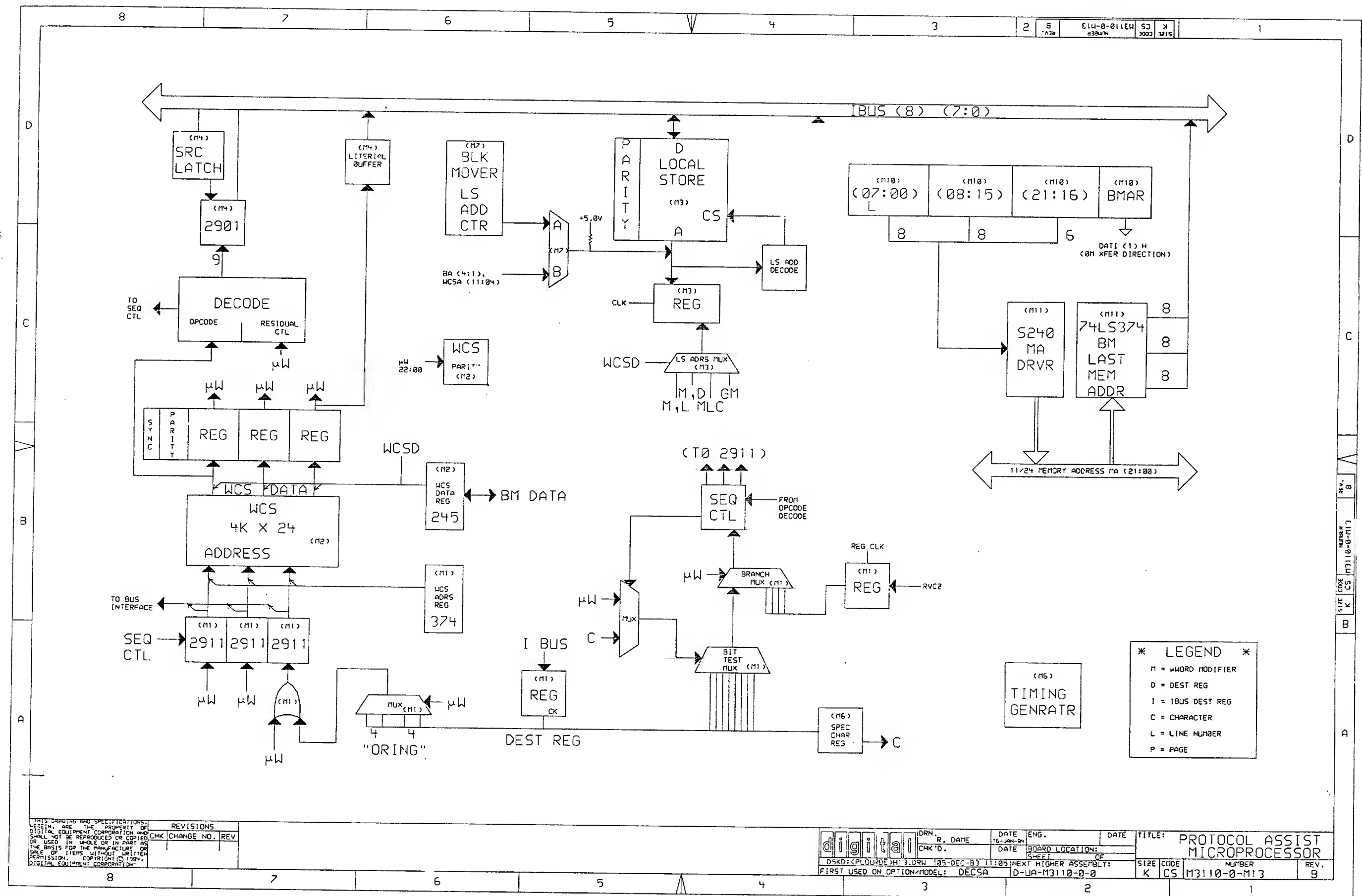


















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## DRAWING DIRECTORY

ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS										
1			M3111-00					C1	D1	D2	E2	F2	F3	H3		
2	D-UA-M3111-0-0	2		PROTOCOL ASSIST BM SCANNER		A	B	C	C	D	E	F	H	J		
3	K-PL-M3111-0-0	2		PROTOCOL ASSIST BM SCANNER		A	B	B	C	D	E	F	H	J		
4	D-EC-5014979-0-0	2		ETCH CUT DRAWING		A	B	C	C	D	E	F	H	J		
5	D-MD-5014979-0-0	5		DRILL & ETCH DRAWING		A	A	A	A	B	B	B	C	C		
6			5014979-00	ETCHED BOARD		C	C	C	C	D1	D1	D1	E1	E1		
7	K-CS-M3111-0-P1	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	B	B		
8	K-CS-M3111-0-P2	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	B	B		
9	K-CS-M3111-0-P3	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	B	C	C	
10	K-CS-M3111-0-P4	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	B	B	C	C		
11	K-CS-M3111-0-P5	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	B	B		
12	K-CS-M3111-0-P6	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	B	B		
13	K-CS-M3111-0-P7	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	B	B	B	C	C		
14	K-CS-M3111-0-P8	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	A	A		
15	K-CS-M3111-0-P9	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	B	C		
16	K-CS-M3111-0-P10	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	A	A		
17	K-CS-M3111-0-P11	1		PROTOCOL ASSIST BM SCANNER		A	A	B	B	B	B	B	C	D		
18	K-CS-M3111-0-P12	1		PROTOCOL ASSIST BM SCANNER		A	A	B	B	C	C	D	D	E		
19	K-CS-M3111-0-P13	1		PROTOCOL ASSIST BM SCANNER		A	B	B	B	B	B	B	C	C		
20	K-CS-M3111-0-P14	1		PROTOCOL ASSIST BM SCANNER		A	A	B	B	B	B	B	B	B		
21	K-CS-M3111-0-P15	1		PROTOCOL ASSIST BM SCANNER		A	A	B	B	B	B	B	B	B		
22	K-CS-M3111-0-P16	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	A	A		
23	K-CS-M3111-0-P17	1		PROTOCOL ASSIST BM SCANNER		A	A	B	B	B	B	B	B	B		
24	K-CS-M3111-0-P18	1		PROTOCOL ASSIST BM SCANNER		A	A	B	B	B	B	B	B	B		
25	K-CS-M3111-0-P19	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	A	A		
NOTES:					DD REV	A	B	C	D	E	F	H	J	K		
REVISION HISTORY			CONT. REVISION HISTORY			MADE BY: T. F. WALSH DATE: 19 OCT 82			d i g i t a l							
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	CHECKED BY: T. F. WALSH DATE: 22 OCT 82 16-JULY-85			TITLE DRAWING DIRECTORY							
JF	INITIAL	A				DESIGN ENGINEER: J. BUZYNSKI DATE: 22 OCT 82 J.C. Tremblay 16-JULY-85			PROTOCOL ASSIST BM SCANNER							
ML	M3111-TW001	B				RESPONSIBLE ENG: J. BUZYNSKI DATE: 22 OCT 82 J.C. Tremblay 16-JULY-85										
ML	M3111-TW002	C				PRODUCTION ENG: W. S. ASHTON DATE: 22 OCT 82			SIZE	CODE	DOCUMENT NUMBER	REV.				
ML	M3111-TW003	D							K	DD	M3111-0-0	K				
ML	M3111-TW004	E				SHEET 1 OF 4 MKO										



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DRAWING DIRECTORY

ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS											
26	K-CS-M3111-0-P20	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	A	A			
27	K-CS-M3111-0-P21	1		PROTOCOL ASSIST BM SCANNER		A	A	A	A	A	A	A	A	A			
28	K-CS-M3111-0-P22	1		PROTOCOL ASSIST BM SCANNER		A	A	B	B	B	B	B	B	B			
29	K-PC-M3111-0-DBI	-		PC DATA BASE		A	A	A	A	B	B	B	C	C			
30	K-CS-M3111-0-DBS	-		CS DATA BASE		A	B	C	C	D	E	F	H	J			
31	K-CS-M3111-0-P23	1		PROTOCOL ASSIST BM SCANNER		-	-	-	-	A	A	A	B	B			

NOTES:

DD  
REV

A

B

C

D

E

F

H

J

K

REVISION HISTORY

CONT. REVISION HISTORY

MADE BY:  
DATE:

CHECKED BY:  
DATE:

DESIGN ENGINEER:  
DATE:

RESPONSIBLE ENG:  
DATE:

PRODUCTION ENG:  
DATE:

d i g i t a l

TITLE DRAWING DIRECTORY

PROTOCOL ASSIST BM SCANNER

SIZE | CODE | DOCUMENT NUMBER | REV.  
K | DD | M3111-0-0 | K



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DRAWING DIRECTORY

ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS											
						E1	F1	H1									
1			M3111-00														
2	D-UA-M3111-0-0	2		PROTOCOL ASSIST BM SCANNER		C1	C2	C3									
3	K-PL-M3111-0-0	2		PROTOCOL ASSIST BM SCANNER		C1	C2	C3									
4	D-EC-5014979-0-0	3		ETCH CUT DRAWING		C1	C2	C3									
5	D-MD-5014979-0-0	5		DRILL & ETCH DRAWING		A	A	A									
6			5014979-00	ETCHED BOARD		C	C	C									

NOTES:

SHEET 3 FOR ETCHED BOARD REV C, "SPECIAL REVS"

DD REV

H

J

K

REVISION HISTORY			CONT. REVISION HISTORY			MADE BY:		DIGITAL							
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	DATE:									
						CHECKED BY:		TITLE DRAWING DIRECTORY							
						DATE:		PROTOCOL ASSIST BM SCANNER							
						DESIGN ENGINEER:									
						DATE:									
						RESPONSIBLE ENG:									
						DATE:									
						PRODUCTION ENG:									
						DATE:									

SIZE

CODE

DOCUMENT NUMBER

REV.

K

DD

M3111-0-0

K

SHEET 3 OF 4 MKO

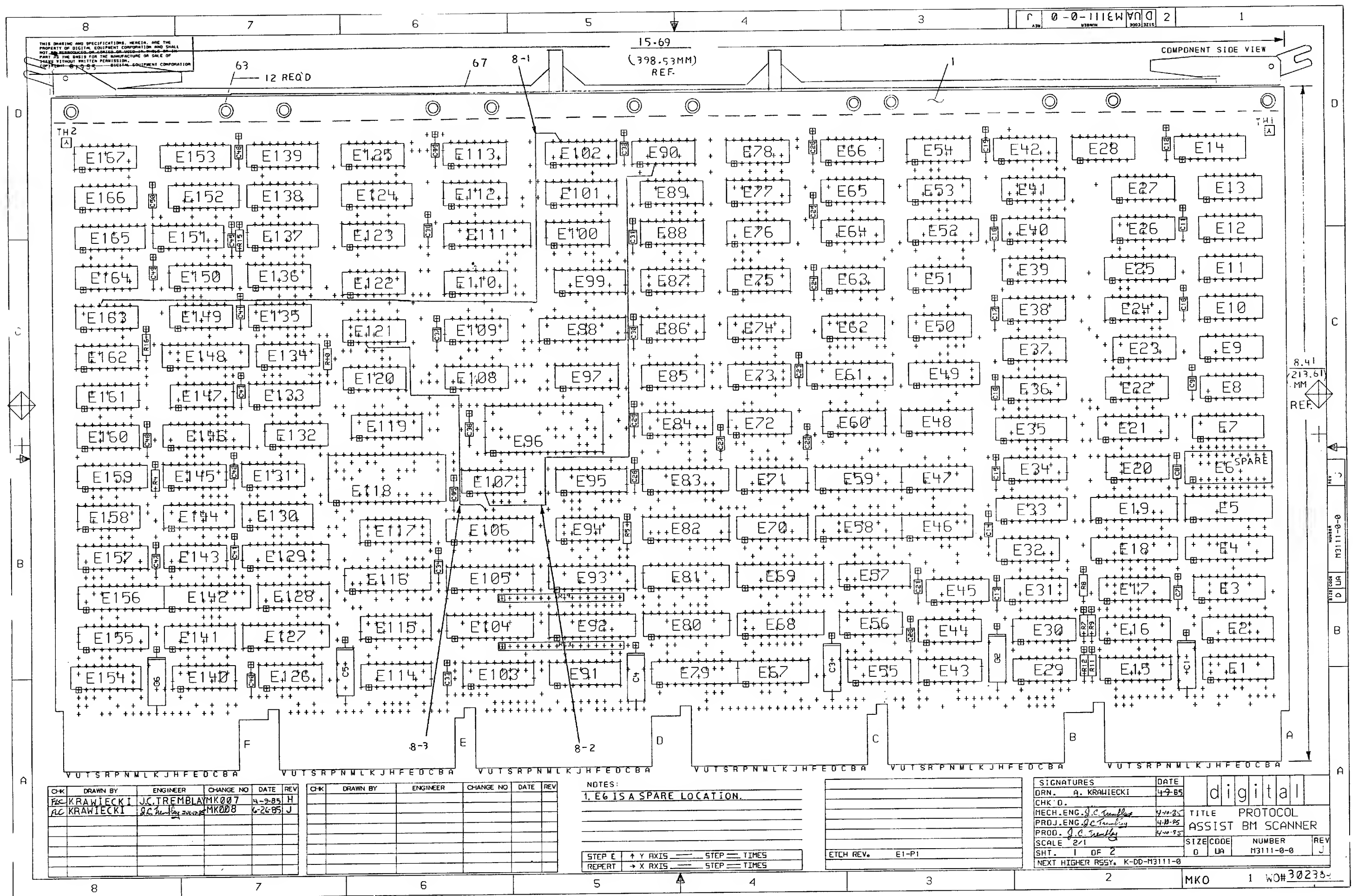


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## DRAWING DIRECTORY

ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS											
1			M3111-00			H2											
2	D-UA-M3111-0-0	2		PROTOCOL ASSIST BM SCANNER		F1											
3	K-PL-M3111-0-0	2		PROTOCOL ASSIST BM SCANNER		F1											
4	D-EC-5014979-0-0	3		ETCH CUT DRAWING		F1											
5	D-MD-5014979-0-0	5		DRILL & ETCH DRAWING		B											
6			5014979-00	ETCHED BOARD		D1											
NOTES:																	
SHEET 4 FOR ETCHED BOARD REV D1, "SPECIAL REVS"					DD REV	K											
REVISION HISTORY			CONT. REVISION HISTORY			MADE BY:		d i g i t a l									
DATE:			DATE:			DATE:											
CHECKED BY:			CHECKED BY:			DATE:		TITLE DRAWING DIRECTORY									
DESIGN ENGINEER:			DESIGN ENGINEER:			DATE:		PROTOCOL ASSIST BM SCANNER									
RESPONSIBLE ENG:			RESPONSIBLE ENG:			DATE:											
PRODUCTION ENG:			PRODUCTION ENG:			DATE:											
DATE:			DATE:			DATE:											
SIZE			CODE			DOCUMENT NUMBER			REV.								
K			DD			M3111-0-0			K								
SHEET 4 OF 4 MKO																	







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REWORK INSTRUCTIONS M3111-MK008:

WIRE ADDS SIDE 1 (P/N 9105740 -55).

- 8-1 FROM E163-12 TO E102-15.
- 8-2 FROM E107-3 TO E90-3.
- 8-3 FROM PTH BELOW E107-3 TO E121-3.

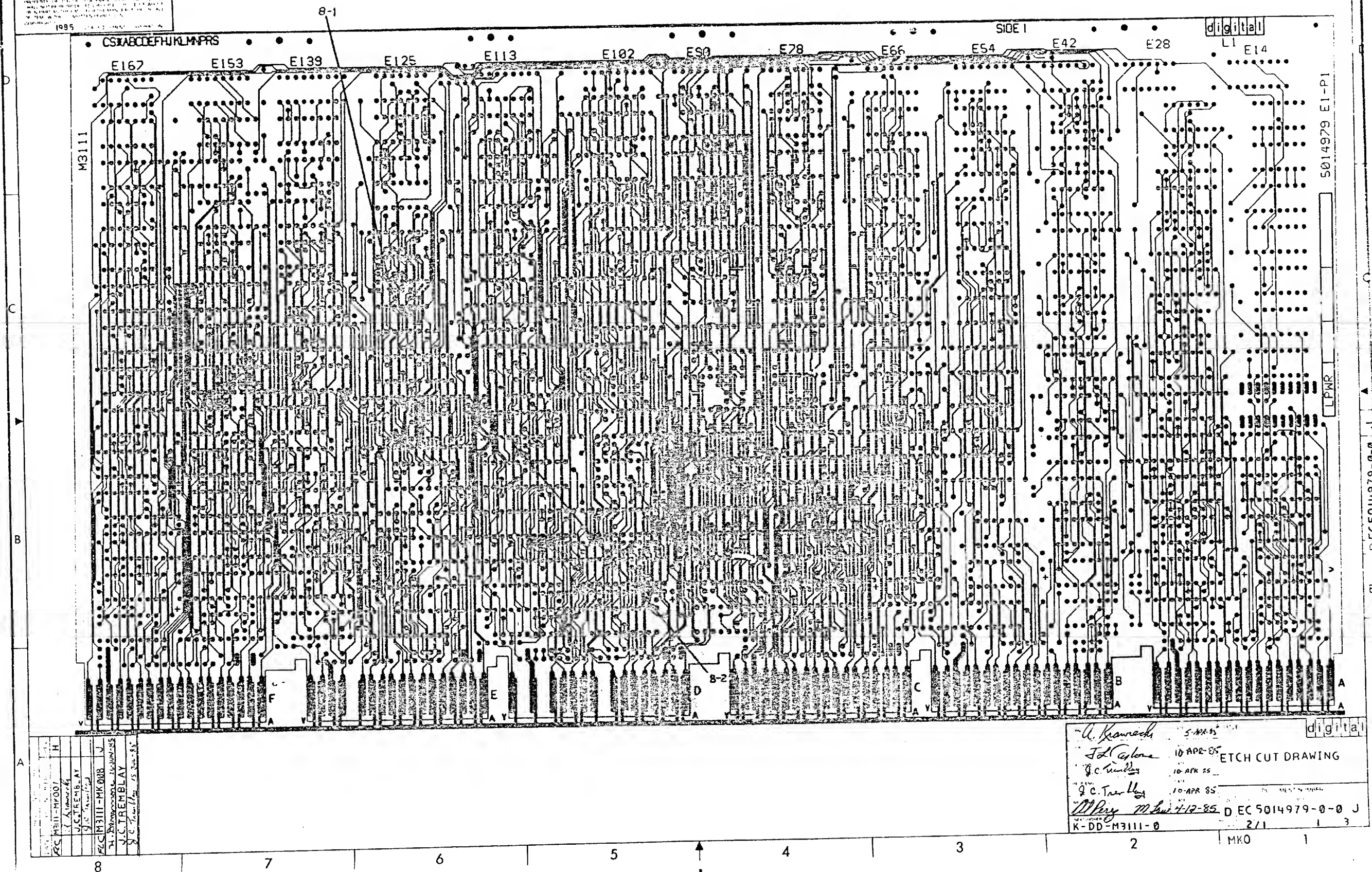
REVISION HISTORY		
REV	ECO NUMBER	DATE

TITLE  
PROTOCOL ASSIST  
B.M. SCANNER

DOCUMENT NUMBER			REV
SIZE	CODE	NUMBER	
D	UA	M3111-0-0	J
SCALE NONE			SHEET 2 OF 2



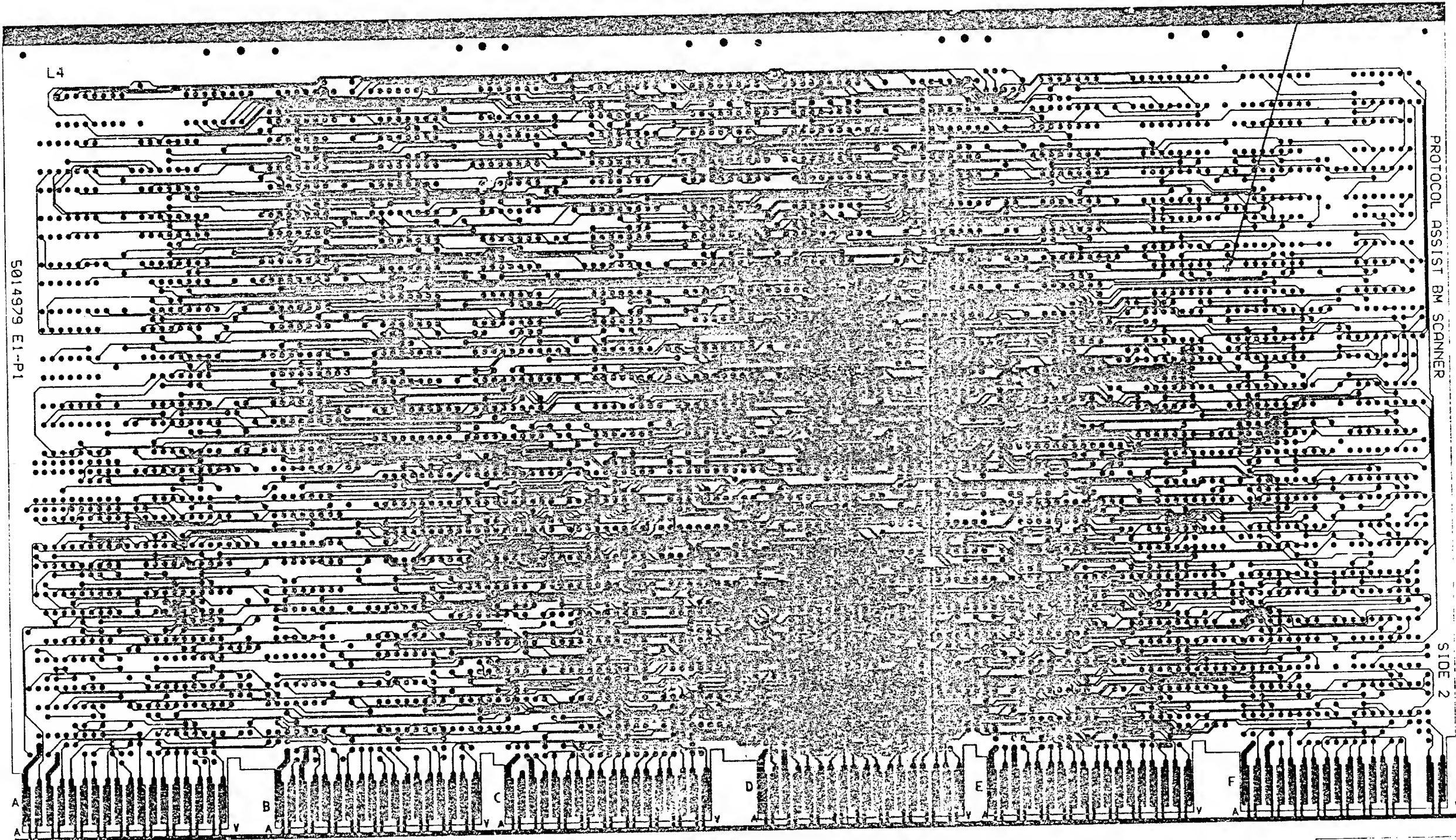
1995





1985

DEC5014979-0-0 J



PROTOCOL ASSIST BM SCANNER

SIDE 2

5014979 E1-P1

TITLE		ETCH CUT DRAWING		DEC5014979-0-0 J	
Mk0		271		2	



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REV 0-0-6764105 DEC 5014979-0-0

# REWORK INSTRUCTIONS M3111-MK008: (ETCH REV. E1-P1).

## ETCH CUTS SIDE 1:

8-1 CUT ETCH BELOW E121-3 AS SHOWN. (ONE PLACE ONLY)

8-2 CUT ETCH AT E107-3.

## ETCH CUTS SIDE 2:

8-3 CUT ETCH CONNECTING TO E163-12 BETWEEN E135 AND E149.

REVISION HISTORY		
REV	ECO NUMBER	DATE

DRD 138C  
EN-01285-16-REV(A)(20)

DOCUMENT NUMBER		
SIZE CODE	NUMBER	REV.
DEC	5014979-0-0	J
SCALE	NONE	SHEET 3 OF 3

MK0

1



LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
				VARIATION REVISION LEVEL:	00		
					A1		
1	1	5014979-00		DRILL AND ETCH PROTOCGL ASSIST	1		
2	65	1012784-00		.047 MFD 50V +80-20% CER	44		C7-C50
3	66	1017472-00		10 MFD 35V +75-10% AL EL	6		C1-C6
4	67	1216988-02		HANDLE,MODULE,HEX TWO EJECTORS	1		
5	61	1300309-00		390.0 .25 W 5.0 % CF	2		R8,R11
6	59	1300365-00		1.0 K .25 W 5.0 % CF	6		R4,R5,R9,R10,R15,R16
7	60	1301322-00		180.0 .25 W 5.0 % CF	2		R7,R12
8	68	1319037-00		R NETWORK 10-110 2.0 % 16PIN	2		R13,R14
9	4	1909705-00		DEC 8891 NAND GATE-QUAD 2IN O	1		E31
10	22	1910532-00		74500 NAND GATE-QUAD 2IN	7		E62,E66,E109,E125,E149,E90,E113
11	8	1910534-00		74S04 INVERTER GATE-HEX 11	4		E38,E94,E110,E145
12	20	1910536-00		74S10 NAND GATE-TRIPLE 3IN	2		E37,E54
13	21	1910537-00		74S11 AND GATE-TRIPLE 3IMP	2		E50,E77
14	16	1910542-00		74S64 A-D-I GATE 4-2-3-2	2		E108,E27
15	6	1910544-00		74S74 FF-D DUAL,EDGE TRIGG	2		E60,E167
16	71	1910544-01		74S74-60GG-D DUAL,EDGE TRIG	3		E41,E78,E134
17	31	1910550-00		74S174 FF-D HEX	2		E95,E119
18	39	1910957-00		74S175 FF-D QUAD COMMON CLO	2		E14,E84
19	18	1911469-00		DEC 8640 RECEIVER,BUS,QUAD,U	6		E29,E30,E43,E44,E45,E55
20	2	1911579-00		/REPLACED BY 19-14987-00	4		E1,E2,E3,E17
21	17	1911675-00		74S138 DECODER/DEMUX 3-8 LI	5		E101,E102,E124,E138,E139
22	33	1912096-00		DEC 74S86 XOR GATE,QUAD 2IN	3		E34,E158,E166
23	35	1912125-00		74S135 XOR/NOR GATE-QUAD 2I	1		E33
24	30	1912388-00		74S02 NOR GATE-QUAD 2IN,PO	5		E85,E107,E120,E147,E161
25	29	1912389-00		74S08 AND GATE-QUAD 2IN,PO	2		E72,E112
26	52	1912646-00		LS253 MUX 1 OF 4 (DUAL)	8		E46-E49,E57,E67,E70,E71
27	43	1912661-00		74S189 MEMORY READ/RITE	2		E144,E152
28	54	1912697-00		LS174 FF-D HEX W/CLEAR	1		E115
29	40	1912731-00		DEC 8646A TRANSCEIVER,4BIT BUS	2		E92,E93
30	55	1912746-00		DEC 74S37 NAND GATE-QUAD 2IN	1		E11

REVISION HISTORY			BASIC PART NO: M3111		DRN: D.SIREEN		DATE: 04-FEB-82		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D: <i>E.T. GERRY</i>		DATE: 15-JUL-85		TITLE PARTS LIST	
---	INITIAL	X01	SECTION VARIATION INDEX		CHK'D: <i>E.T. GERRY</i>		DATE: 04-FEB-82		PROTOCOL ASSIST B.M. SCANNER (PLUTO)	
---	INITIAL	A	[A] 00		DES.ENG: <i>J.C. Tremblay</i>		DATE: 15-JUL-85		DOCUMENT NUMBER	
JB	M3111-TW001	B	[B]		DES.ENG: J.BUZYNSKI		DATE: 8-FEB-82		SIZE CODE NUMBER REV	
JB	M3111-TW003	C	[C]		RESP.ENG.: <i>J.C. Tremblay</i>		DATE: 15-JUL-85		K PL M3111-0-0	
JB	INACTIVATE	D	[D]		RESP.ENG.: J.C.TREMBLAY		DATE: 26-JUN-85		RELEASE DATE: 26-JUN-85	
JB	M3111-MK005A	E	[E]		MFG.ENG.: W.BROOKE		DATE: 26-JUN-85		FILE NAME: MK1186.PLS	
ML	M3111-MK006	F	[F]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		EDIT #	
JCT	M3111-MK007	H	[H]		ID-UA-M3111-0-0		MK-DD-M3111-0		33	
JCT	M3111-MK008	J	[J]							
			[K]							
			[L]							
			[M]							
			[N]							

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LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
				VARIATION REVISION LEVEL:	00 A1	
31	14	1912799-00		LS00 NAND-GATE-QUAD 2IN,P	4	E8,E40,E88,E89
32	13	1912801-00		LS02 NOR-GATE-QUAD 2IN	4	E24,E53,E63,E73
33	19	1912803-00		LS04 INVERTER GATE,HEX	2	E22,E131
34	12	1912805-00		LS08 AND GATE-QUAD 2IN,PO	7	E20,E36,E42,E64,E87,E97,E150
35	58	1912807-00		LS10 NAND GATE-TRIPLE 3IN	1	E136
36	10	1912808-00		LS11 AND GATE-TRIPLE 3IN	2	E13,E135
37	46	1912811-00		LS21 AND GATE-DUAL 4IN,PO	1	E126
38	9	1912813-00		LS27 NOR GATE-TRIPLE 3IN	3	E10,E75,E123
39	38	1912816-00		LS32 OR GATE-QUAD 2IN,POS	2	E74,E100
40	25	1912820-00		LS51 A-O-I GATE 2-WIDE 2I	3	E121,E122,E132
41	7	1912824-00		LS74 CF-D DUAL,EDGE TRIGG	6	E23,E26,E39,E65,E76,E163
42	5	1912833-00		LS109 FF-JK DUAL,POS EDGE	6	E21,E25,E52,E99,E133,E137
43	43	1912847-00		LS157 MUX 1 OF 2(QUAD)	3	E114,E157,E165
44	15	1912849-00		LS161 COUNTER,SYNCHR,4BIT	1	E151
45	3	1912851-00		LS169 COUNTER,SYNCH. UP/DG	1	E7
46	42	1912853-00		LS175 FF-D QUAD	3	E35,E153,E159
47	53	1912863-00		LS273 FF-D OCTAL W/CLEAR	2	E58,E59
48	36	1912867-00		LS298 MUX 1 OF 4,2IN W/S	1	E117
49	48	1913340-00		74S32 OR GATE-QUAD 2IN	4	E51,E86,E160,E28
50	47	1913493-00		74S241 OCTAL BUFFER,TRI-STA	1	E106
51	24	1913671-00		74S374 FF-D,OCTAL,TRI STATE	4	E79,E80,E82,E129
52	70	1913671-50		74S374 FF-D,OCTAL,TRI STATE	3	E61,E111,E148
53	34	1913788-00		26S10 TRANSCEIVER,BUS,QUA	2	E91,E103
54	51	1913939-00		LS191 COUNTER,SYNCHR. UP/D	1	E141
55	27	1914085-00		74S260 NOR GATE-DUAL,POS	4	E56,E140,E164,E143
56	23	1914086-00		74S30 NAND GATE-POS 8IN	2	E32,E162
57	41	1914214-00		LS374 FF-D OCTAL EDGE TRIG	4	E81,E83,E105,E142
58	11	1914438-00	DC	013 UNIBUS INTERRUPT-BIP	2	E15,E16
59	56	1914451-00		LS393 COUNTER,BINARY,4BIT	2	E9,E12
60	44	1914768-00	C	67401J MEMORY FIFO,SERIAL	4	E128,E130,E154,E155
61	49	1915193-00		LS244 DRIVER,LINE,OCTAL,TR	3	E104,E127,E156
62	32	1915218-00		LS245 TRANSCEIVER,BUS,OCTA	2	E68,E69
63	26	1915219-00		LS373 FF-D OCTAL-TRANSPARE	4	E4,E5,E18,E19
64	57	23031K4-00	K4-01		1	E146
65	69	23042K4-00	K4-01		1	E98
66	72	23079C6-00	C6-01		1	E118
67	73	23080C6-00	C6-01		1	E96
68	28	23085B1-00	B1-01		1	E116
69	63	9009000-00		EYELET,ROLLED 0.1210DX0.156	12	
70	74	9105740-55		WIRE(WRAP) 30AWG KYNAR UL14	20	

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							PROTOCOL ASSIST B.M. SCANNER (PLUTO)				M3111-0-0	J

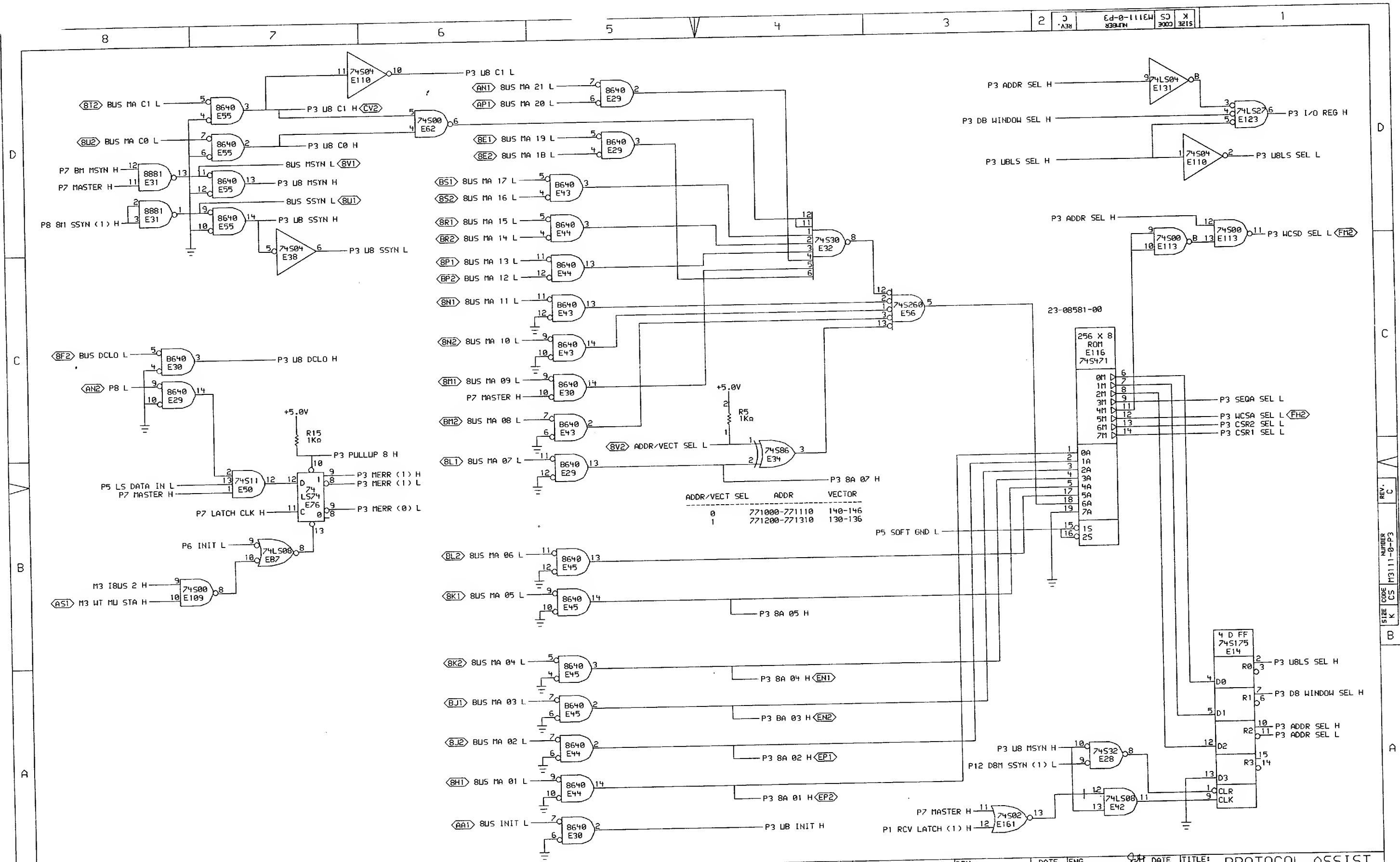












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REVISIONS		
CHK	CHANGE NO.	REV
RC	1	C

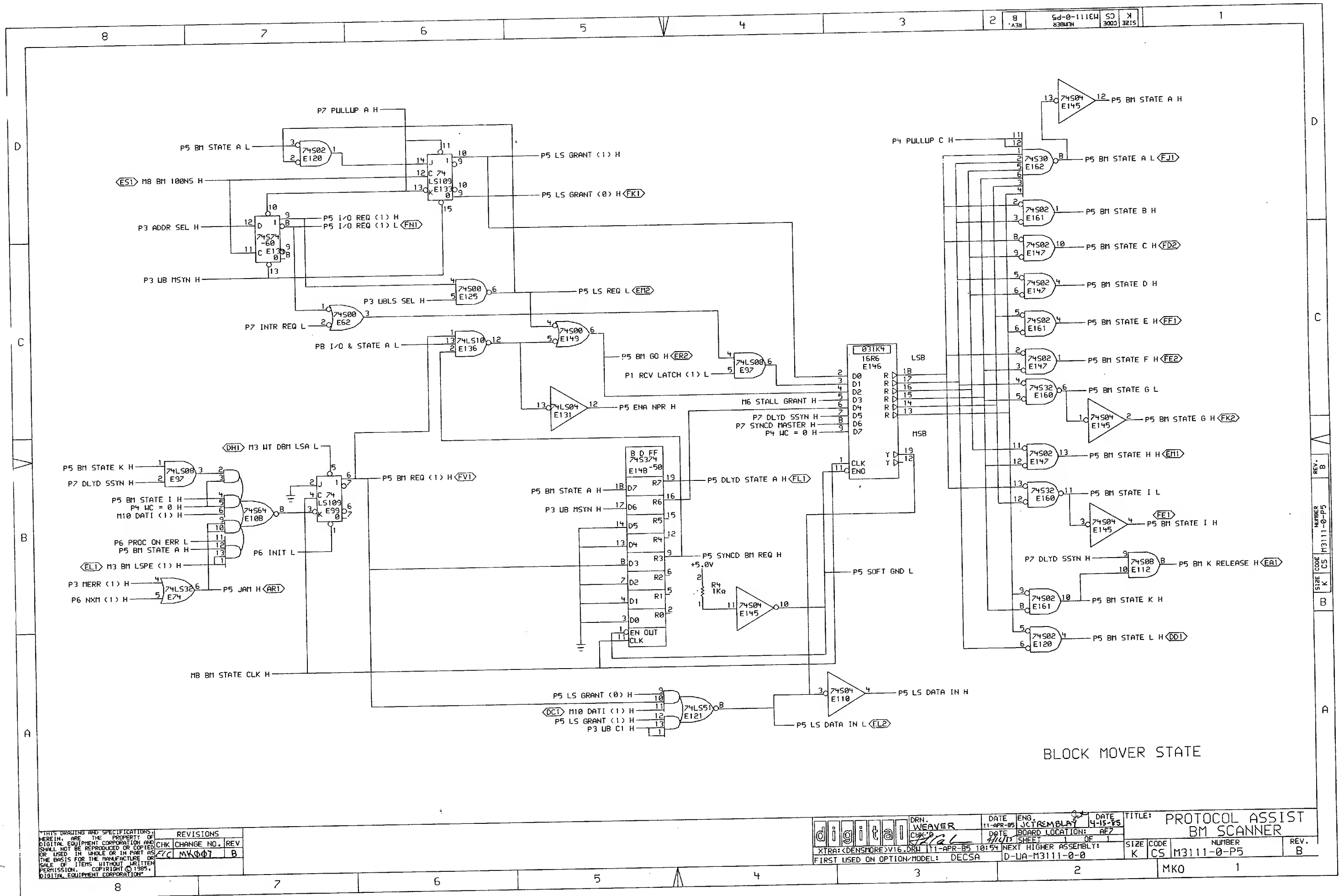
digital	DRN. WEAVER	DATE 11-APR-85	ENG JCTREMBLAY	DATE 4-15-85	TITLE: PROTOCOL ASSIST BM SCANNER
	CHK-D	DATE 11-APR-85	DATE 11-APR-85	DATE 11-APR-85	DATE 11-APR-85
XTRA: (DENSEMORE) V14.DRW 11-APR-85 10:52 NEXT HIGHER ASSEMBLY: D-UA-M3111-0-0					
FIRST USED ON OPTION/MODEL: DECSA					
SIZE	CODE	NUMBER	REV.		
K	CS	M3111-0-P3	C		
MK0			1		



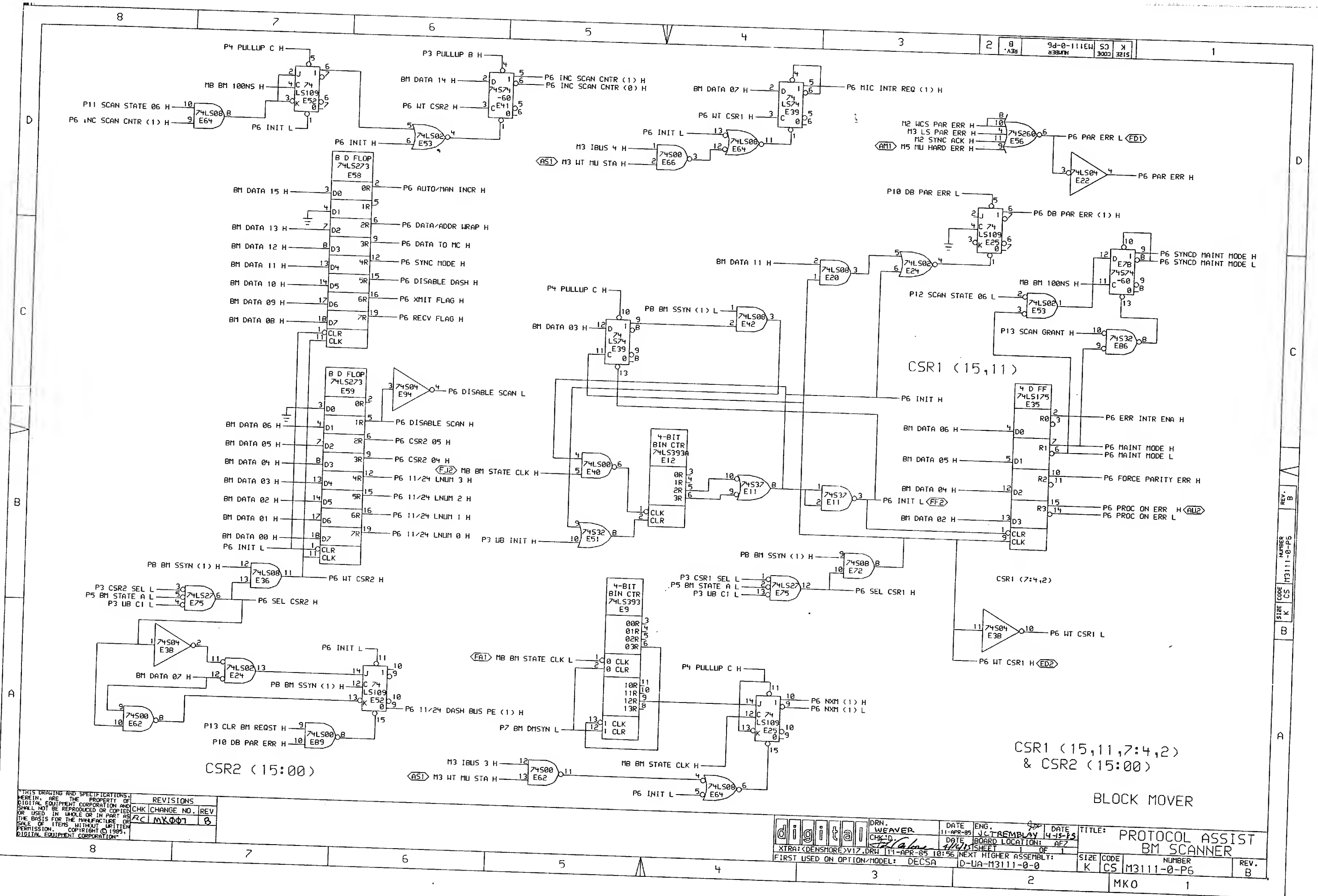


TITLE: PROTOCOL ASSIST BM SCANNER			
SIZE	CODE	NUMBER	REV.
K	CS	M3111-0-P4	C

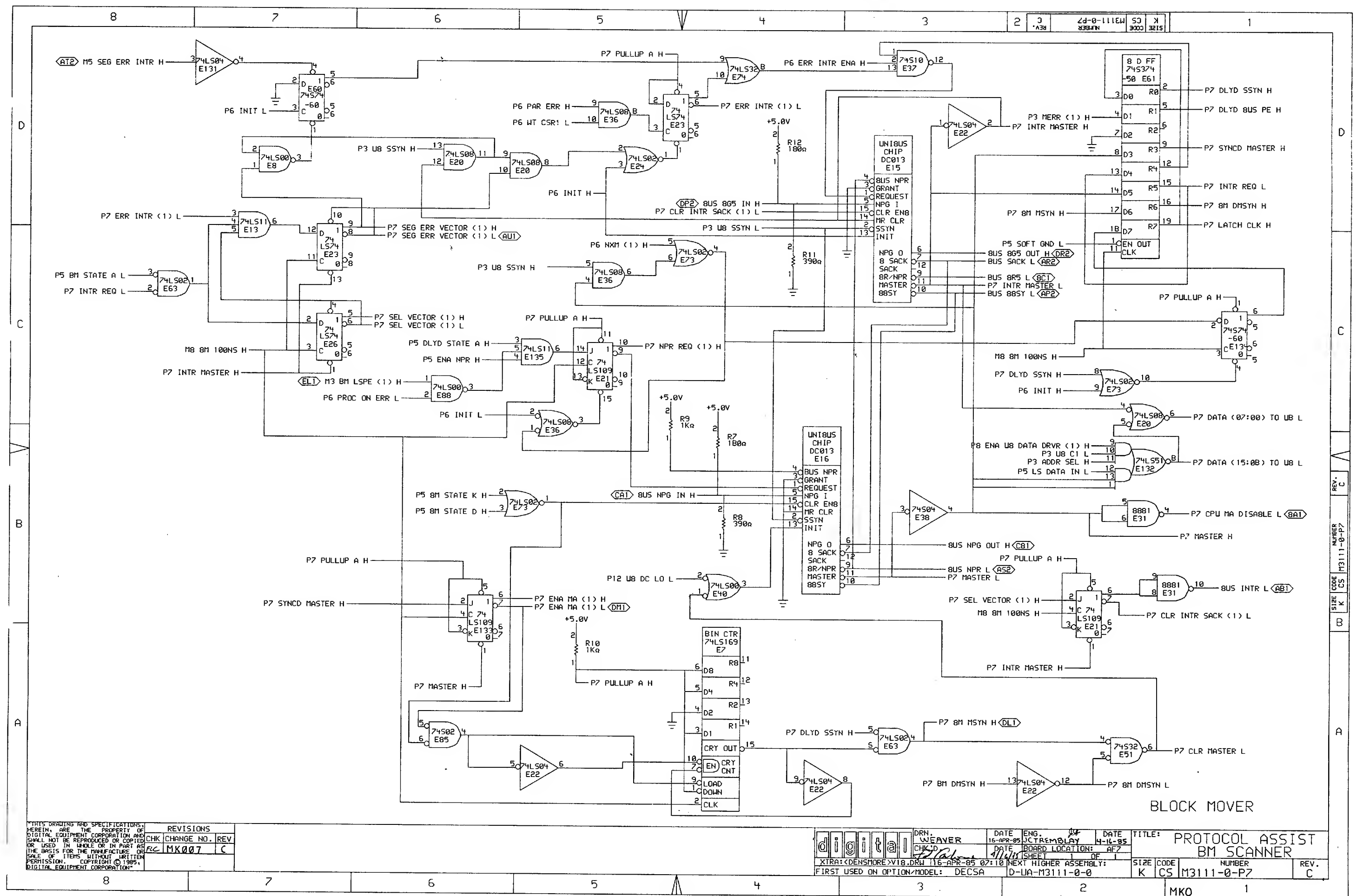




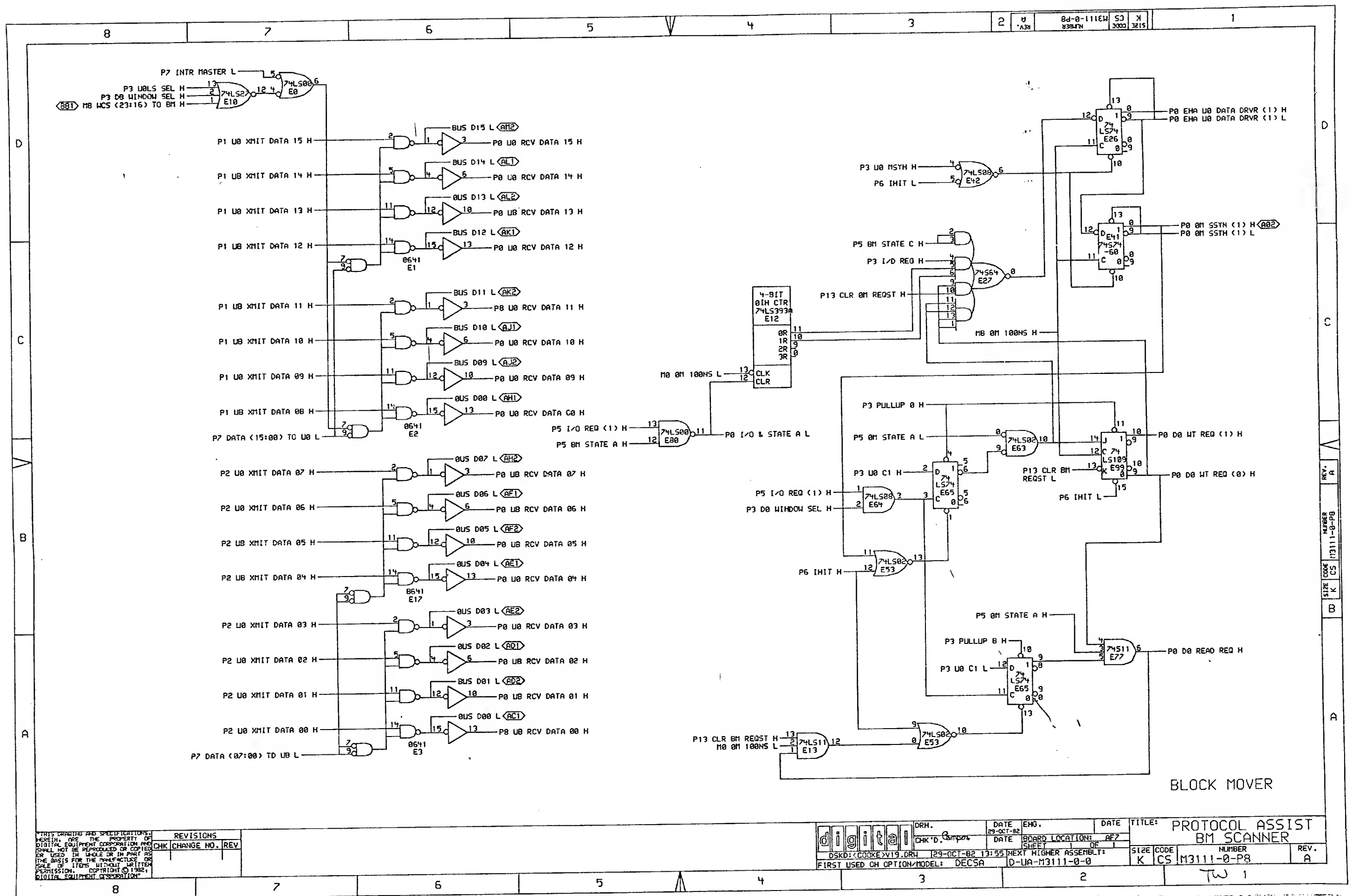




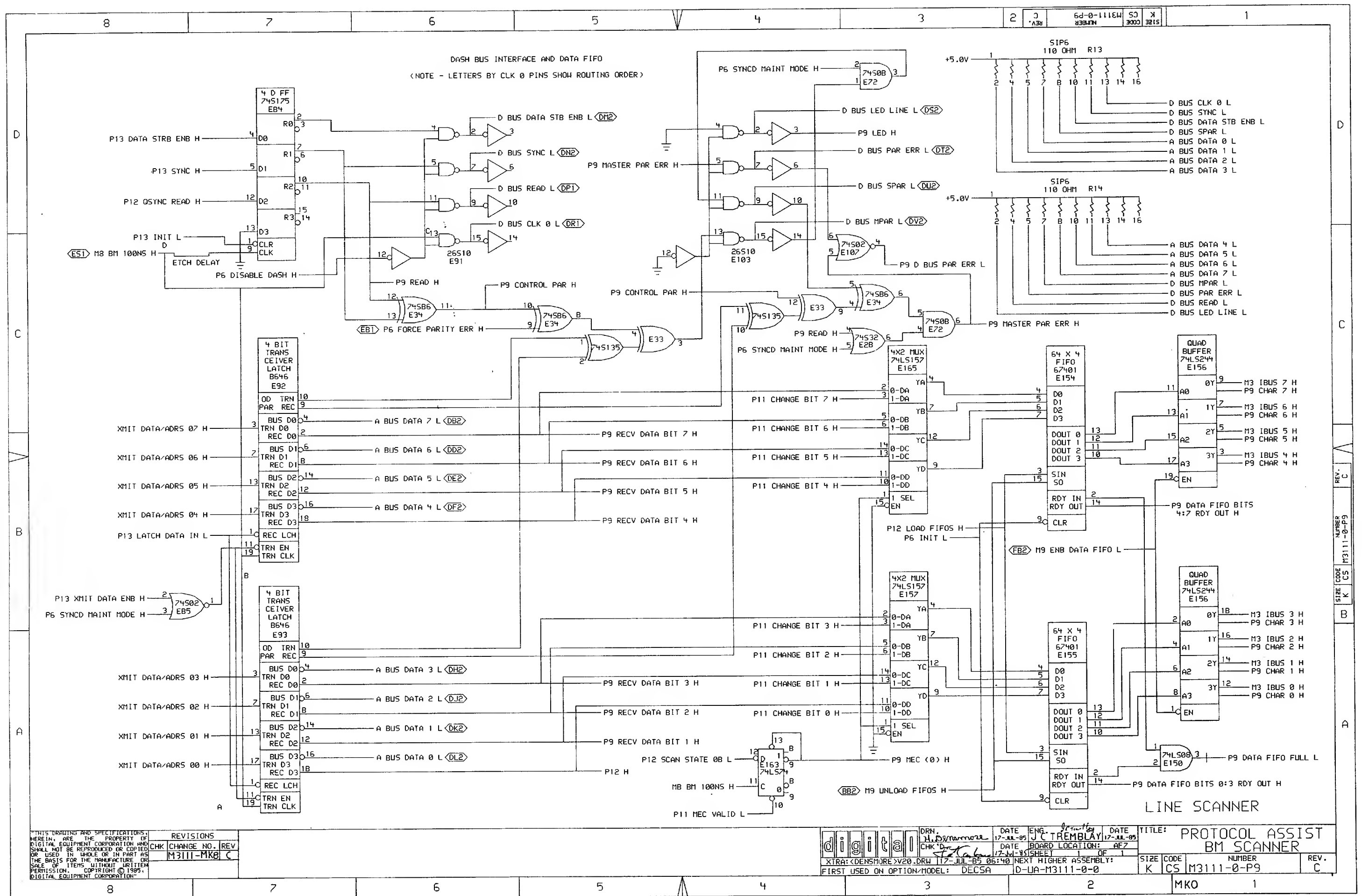




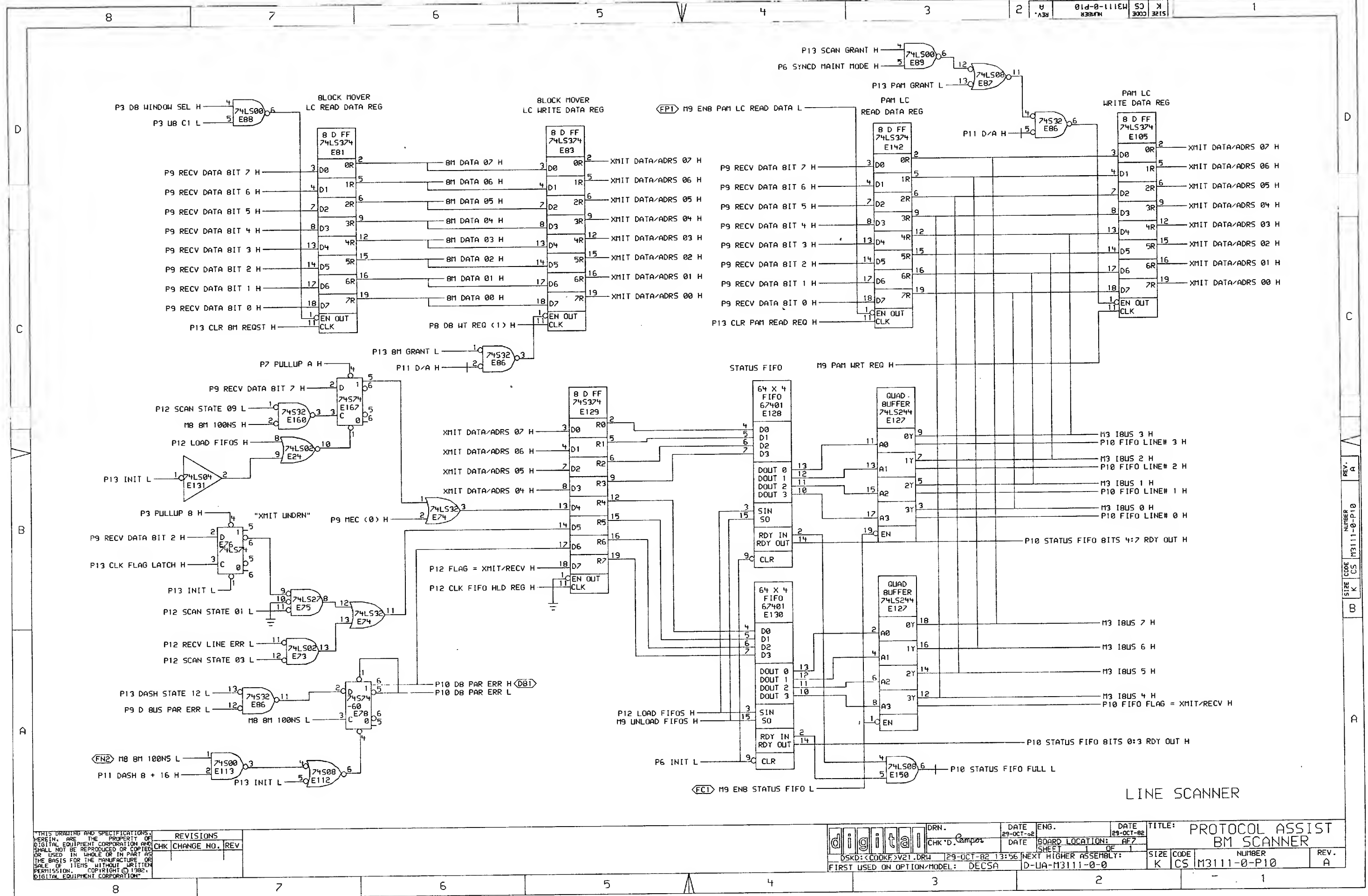




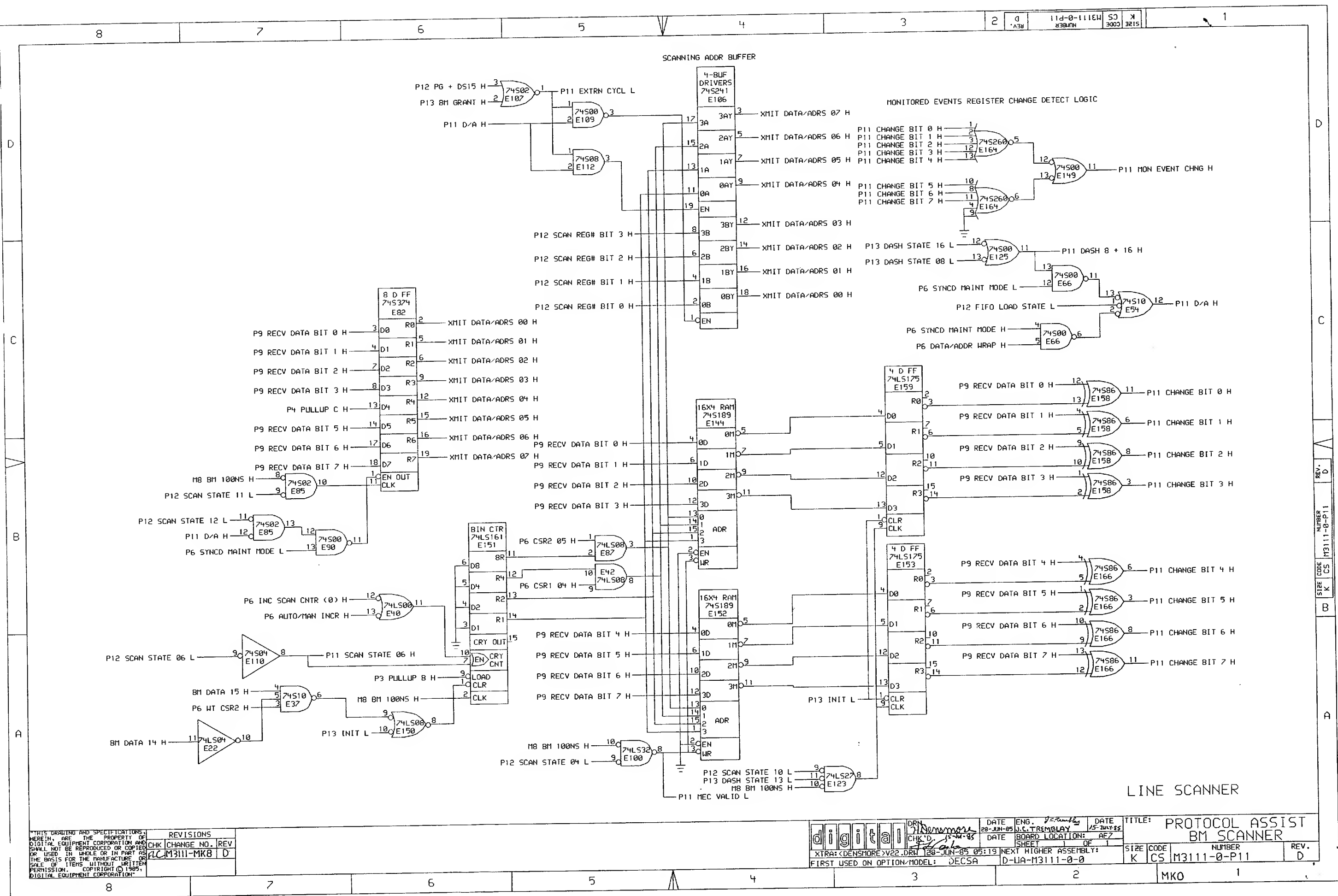




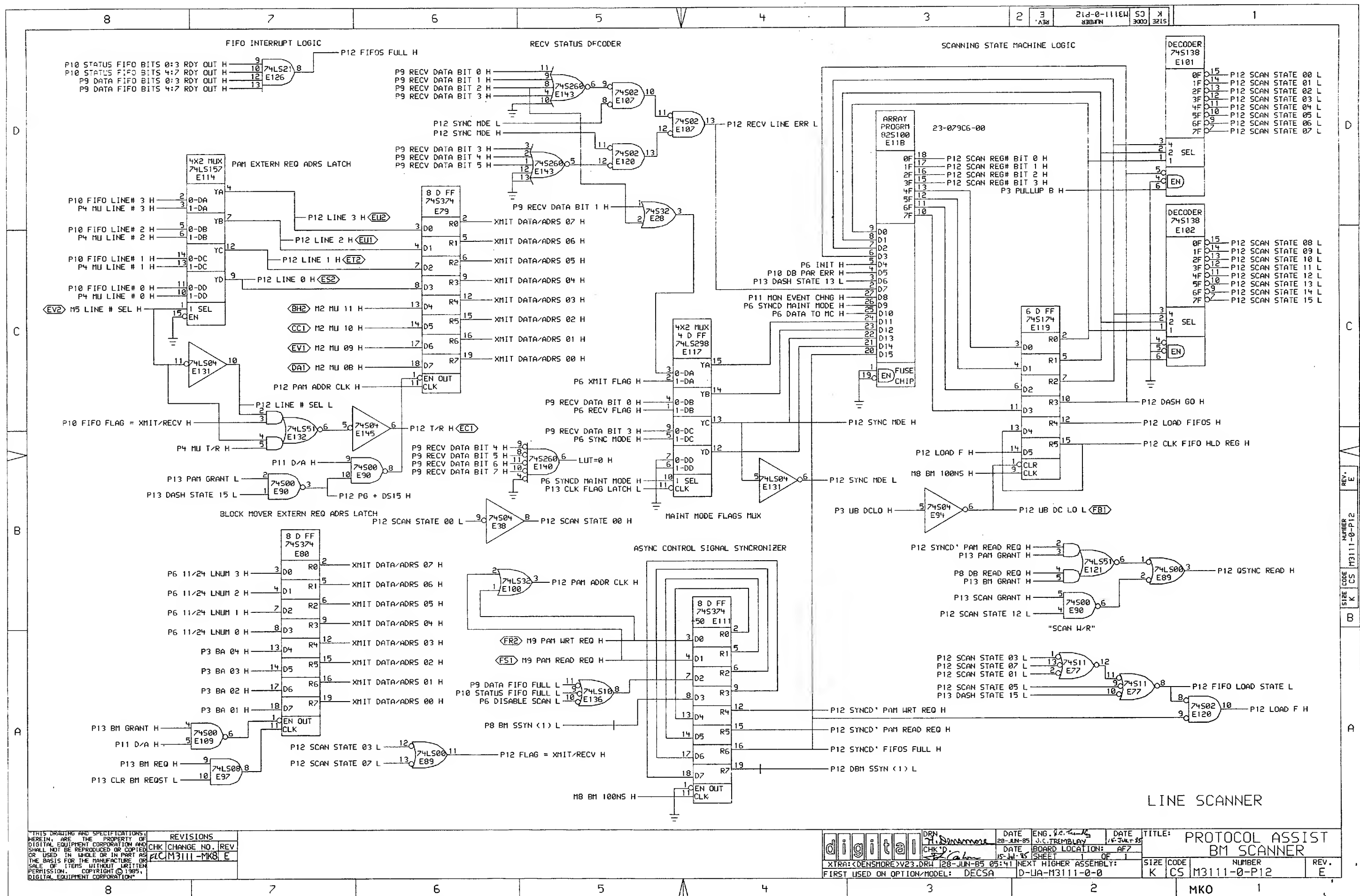




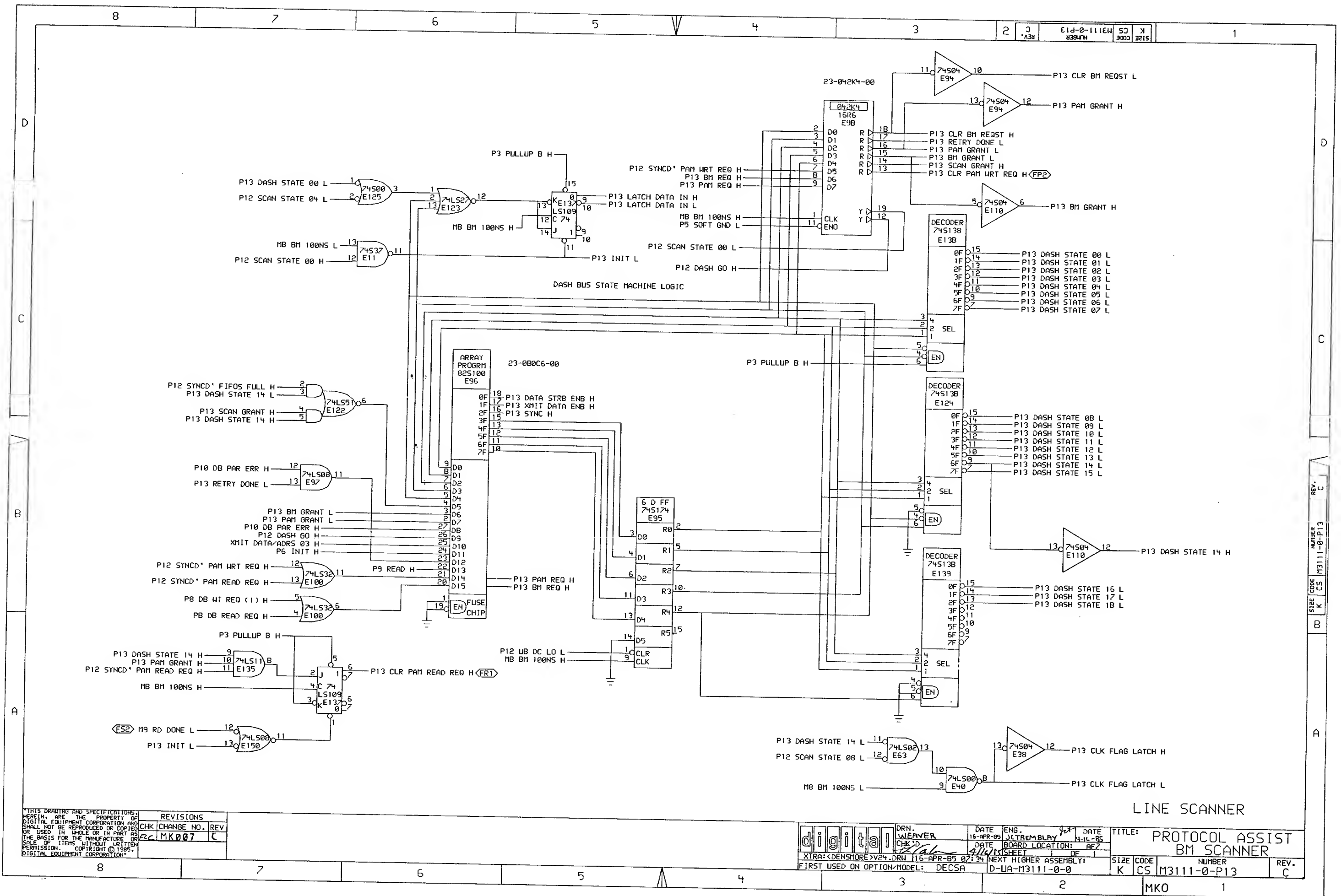




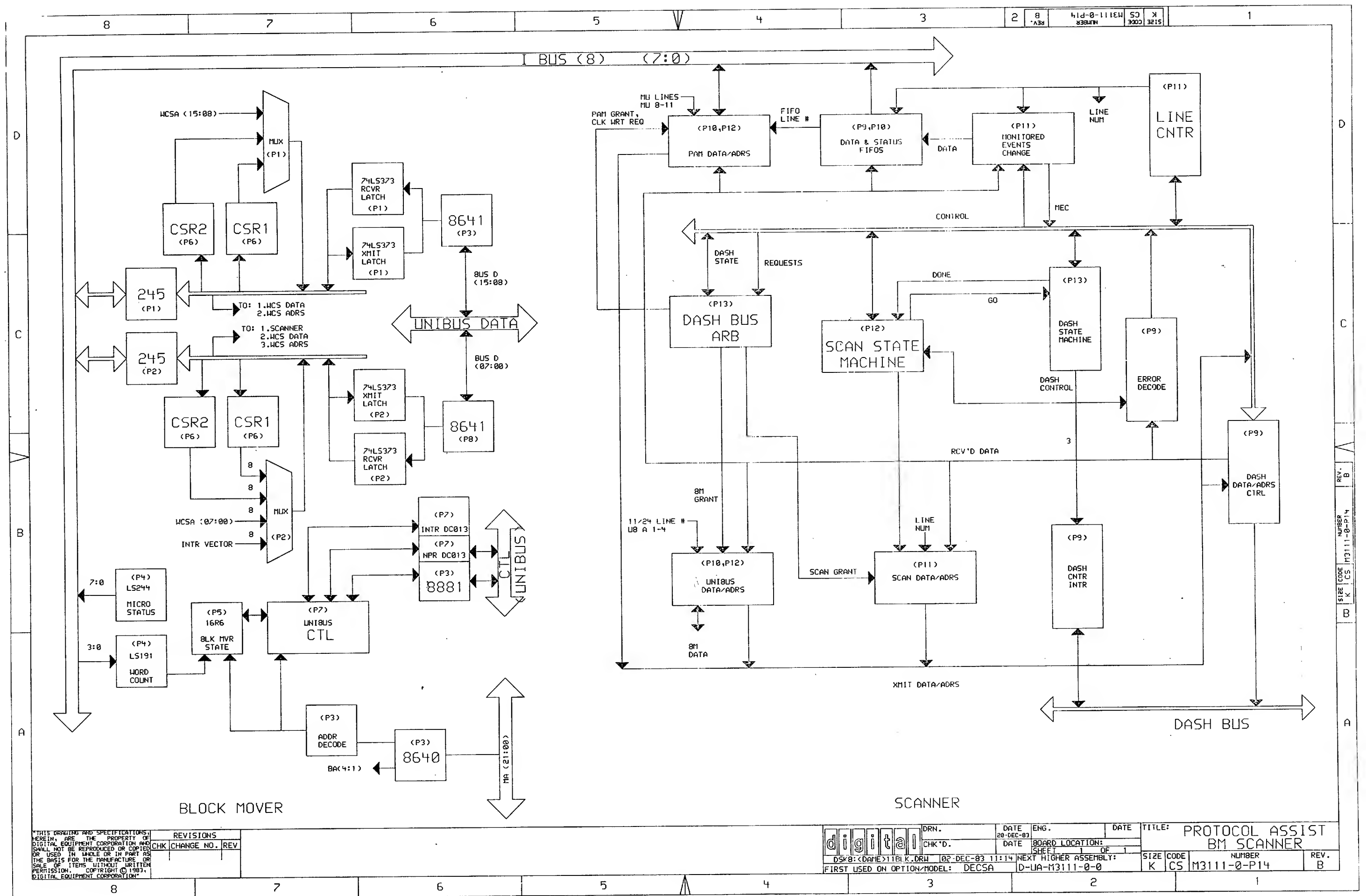












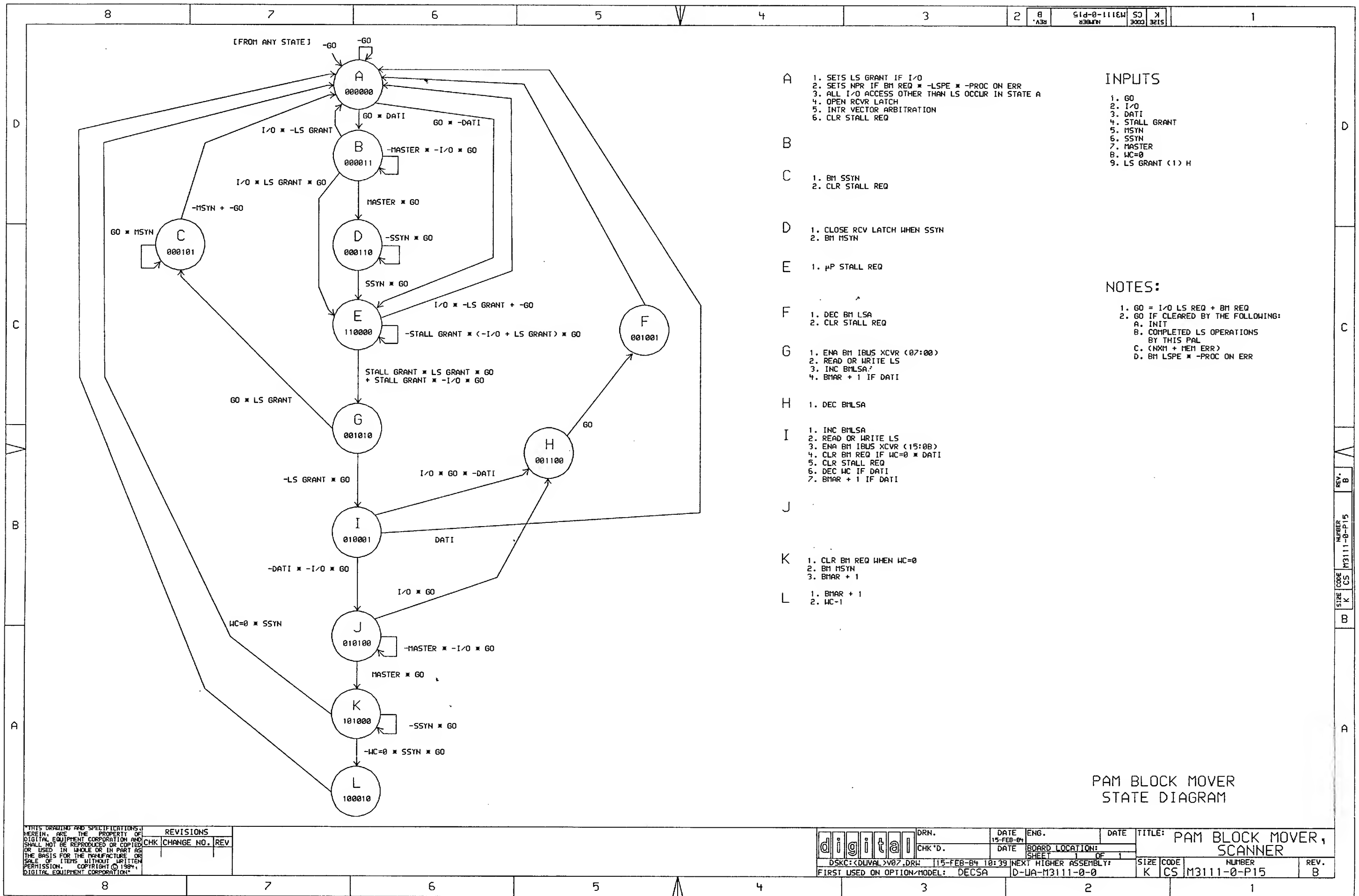
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REVISIONS  
CHK CHANGE NO. REV.

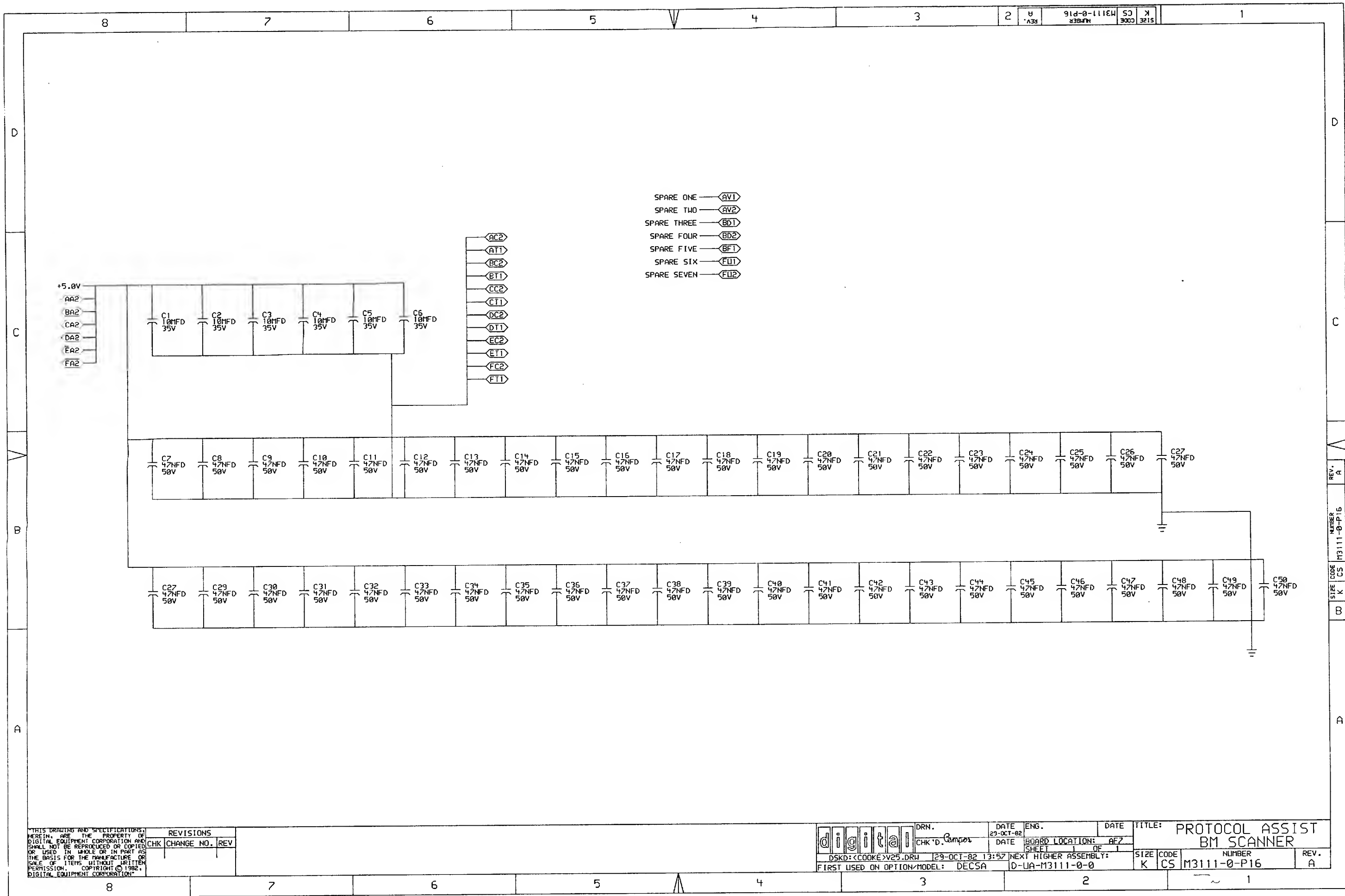
digital DRN. DATE ENG. DATE TITLE: PROTOCOL ASSIST BM SCANNER  
CHK'D. DATE BOARD LOCATION: SHEET 1 OF 1  
DSK8: <DAME>11BLK.DRW 02-DEC-83 11:14 NEXT HIGHER ASSEMBLY: D-UA-M3111-0-0  
FIRST USED ON OPTION/MODEL: DECSA

SIZE CODE NUMBER REV.  
K CS M3111-0-P14 B











COMMENTS:

STATE 0: INIT STATE  
THIS STATE DOES THE INIT  
FOR THE SCANNER LOGIC

STATE 8: READ STATUS (#1001)  
THIS STATE READS THE LINE  
STATUS TO CHECK IF SERVICE  
NEEDED.

STATE 9: READ RECV STATUS (#0001)  
READS THE RECV STATUS REG  
TO CHECK FOR RECV CHAR  
ERRORS

STATE 3: LOAD RECV ERR BITS  
LOADS THE FIFO WITH THE CONTENTS  
OF THE RECV STATUS REG. IF THERE  
IS AN ERROR.

STATE 11: READ COMMAND REG. (#0011)  
READ FOR A ROW OF THE LINE'S  
COMMAND REG.

STATE 12: WRITE COMMAND REG. (#0011)  
WRITES COMMAND REG. (NO PAR ERR)  
SETTING BIT 5 (RESET ERROR)

STATE 13: READ RECV CHAR (#0000)  
READ THE RECV CHAR

STATE 7: LOAD RECV CHAR  
LOADS THE RECV CHAR IN THE DATA  
DATA FIFO AND THE LINE NUMBER IN  
THE STATUS FIFO

STATE 1: LOAD XMIT FLAG  
LOADS LINE NUMBER OF XMIT FLAG IN  
STATUS FIFO

STATE 2: NOT USED

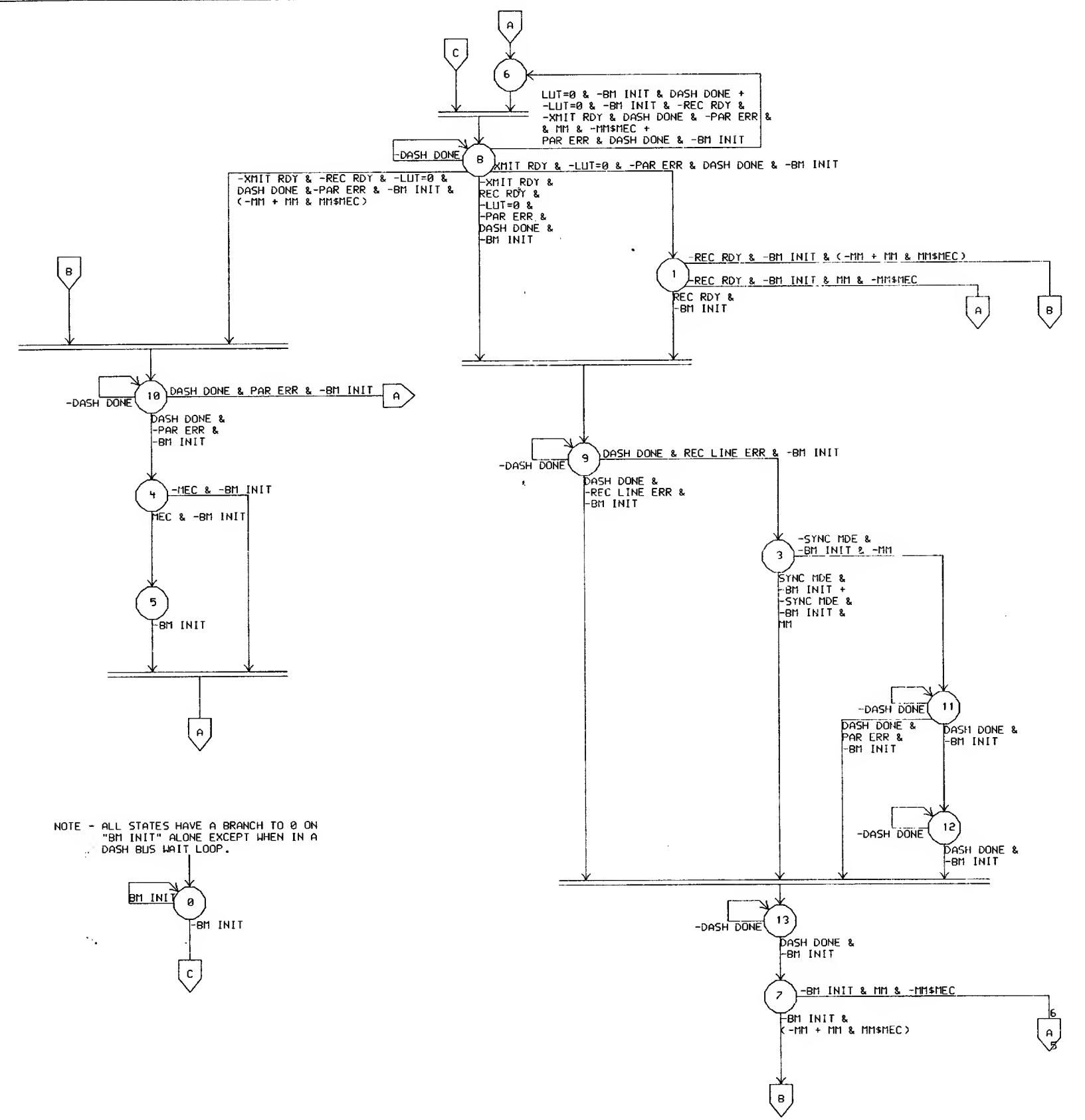
STATE 10: READ MON. EVENTS. (#1011)  
READS THE MON. EVENTS REG ON THE  
LINE ALSO READS OLD VALUE FROM RAM.

STATE 4: CHECK MEC  
CHECKS OLD VALUE AGAINST NEW FOR  
CHANGES ALSO WRITES RAM WITH NEW VALUE.

STATE 5: LOAD CHANGE DATA  
LOADS FIFO WITH CHANGE DATA IE CHANGE

STATE 6: INCR CNTR  
INCR. THE SCANNING CNTR TO POINT TO THE  
NEXT LINE

REVISION HISTORY:  
\*\*\*\*\*  
V.6 - SCH 4/1/82  
V.7 - SCH 4/20/82



REVISIONS		
CHK	CHANGE NO.	REV

digital		DRN.	DATE	ENG.	DATE	TITLE:	
CHK'D.		DATE	BOARD LOCATION:	OF	1	SCANNER PLA STATE DIAGRAM	
DSKB: <NAME> SCANST.DRW		102-DEC-83 11:24	NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: DECSA		D-UA-M3111-0-0			K	CS	M3111-0-P17
							REV. B



8

7

6

5

4

3

2

B

B1d-0-1111E4

CS

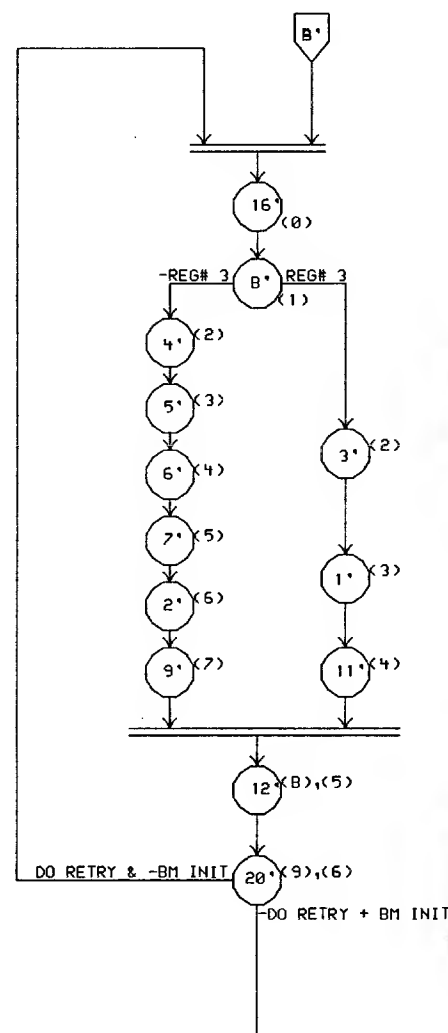
K

321

1

NOTES: -IN MAINT MODE ALL STATES ARE THE SAME  
D/A IS ASSERTED OR NEGATED DEPENDING  
THE ASSERTION OR NEGATION OF THE  
DATA/ADRS WRAP BIT RESPECTIVELY.  
FOR A DATA WRAP THE DATA FROM THE LAST  
DASH BUS EXTERNAL WRITE IS WRAPED  
AROUND. FOR AN ADDRESS WRAP THE ADDRESS  
OF THE READ IS WRAPED AROUND.

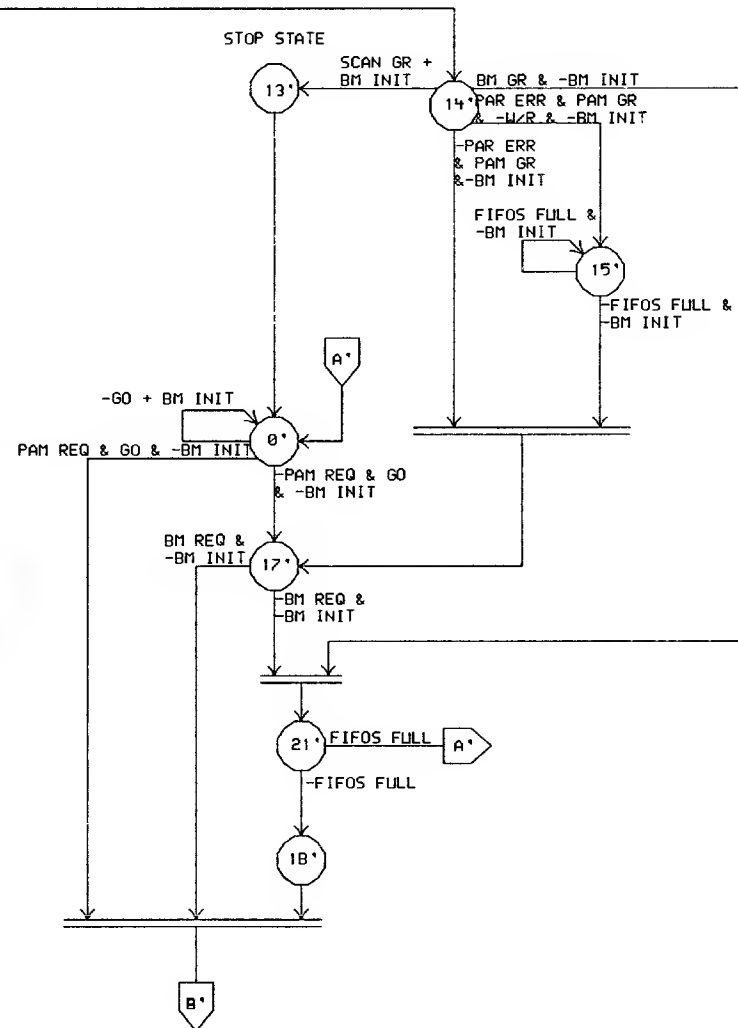
### DASH BUS CYCLE CONTROL



THESE STATES ARE THE DASH BUS CYCLE  
AND CANNOT BE INTERRUPTED BY BM INIT.  
THE NUMBERS IN PARENS CORRESPOND TO  
CLOCK CYCLES ON THE DASH TIMING DIAGRAMS  
THE LEFT SIDE ARE THE LONG CYCLES THE  
RIGHT SIDE ARE THE SHORT CYCLES

STATES 15', 17', & 18'  
BRANCH TO INIT (0')  
IF "BM INIT" IS ASSERTED

### DASH BUS ARBITRATION



### COMMENTS

STATE 0: INIT/IDLE STATE  
LOOPS IN THIS STATE WHILE INIT OR DASH  
BUS CYCLE NOT REQUESTED (NOT GO)

STATE 13: STOP STATE  
STOP STATE IS ALWAYS PASSED THROUGH  
WHEN CYCLE(S) ARE DONE TO INFORM THE SCAN  
PLA THAT THE REQUEST FINISHED

STATE 14: LAST GRANT CHECK  
CHECKS TO SEE WHO GOT SERVICED LAST  
(I.E. PAM, BLOCK MOVER OR SCANNER)  
AND GOES TO THE APPROPRIATE STATE  
TO ARB. TO DETERMINE THE NEXT REQUESTOR  
TO GET SERVICE

STATE 15: PAR ERR LINE# LOAD  
IN THE EVENT OF A PAM WRITE ACCESS PAR  
ERROR THIS STATE LOADS THE LINE NUMBER IN THE  
STATUS FIFO TO INFORM THE PAM OF THAT

STATE B: DASH CYCLE LENGTH CHECK  
THIS STATE BRANCHES TO DO A LONG OR SHORT  
DASH CYCLE DEPENDING ON THE REGISTER NUMBER  
MSB. IT IS ALSO CYCLE #1 IN ALL DASH CYCLES.  
(SEE NOTE AT LEFT FOR EXPLANATION)

STATE 17: BLOCK MOVER ARB.  
THIS STATE ARBITRATES FOR THE BLOCK MOVER. IT  
IS ONLY ENTERED IF THE PAM DOES NOT WANT SERVICE  
OR HAS ALREADY HAD ONE DASH ACCESS THIS CYCLE.  
LEAVING THIS STATE SETS BLOCK MOVER GRANT IF THE  
BLOCK MOVER REQUESTED SERVICE.

STATE 18: SCANNER ARB.  
AFTER PAM AND BLOCK MOVER HAVE HAD SERVICE,  
OR NEITHER HAVE REQUESTED SERVICE THIS STATE  
GRANTS THE SCANNER THE SERVICE IT ORIGINALLY  
REQUESTED

STATE 20: CHECK PAR ERR  
LAST CYCLE IN DASH BUS ACCESS ALSO CHECKS IF  
A RETRY IS NEEDED AND HAS NOT BEEN DONE ONCE  
ALREADY (I.E. DO RETRY ASSERTED). ALSO CHECK  
IF AN INIT OCCURED DURING THE DASH BUS CYCLE

### REVISION HISTORY:

\*\*\*\*\*  
V.14 - SCM 4/8/82  
V.15 - SCM 4/13/82

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REVISIONS  
CHK CHANGE NO. REV

digital

DRN.

CHK'D.

DATE

DATE

ENG.

DATE

BOARD LOCATION:

SHEET

OF 1

TITLE:

DASH BUS PLA  
STATE DIAGRAM

SIZE

CODE

NUMBER

M3111-0-P18

REV.

B

8

7

6

5

4

3

2

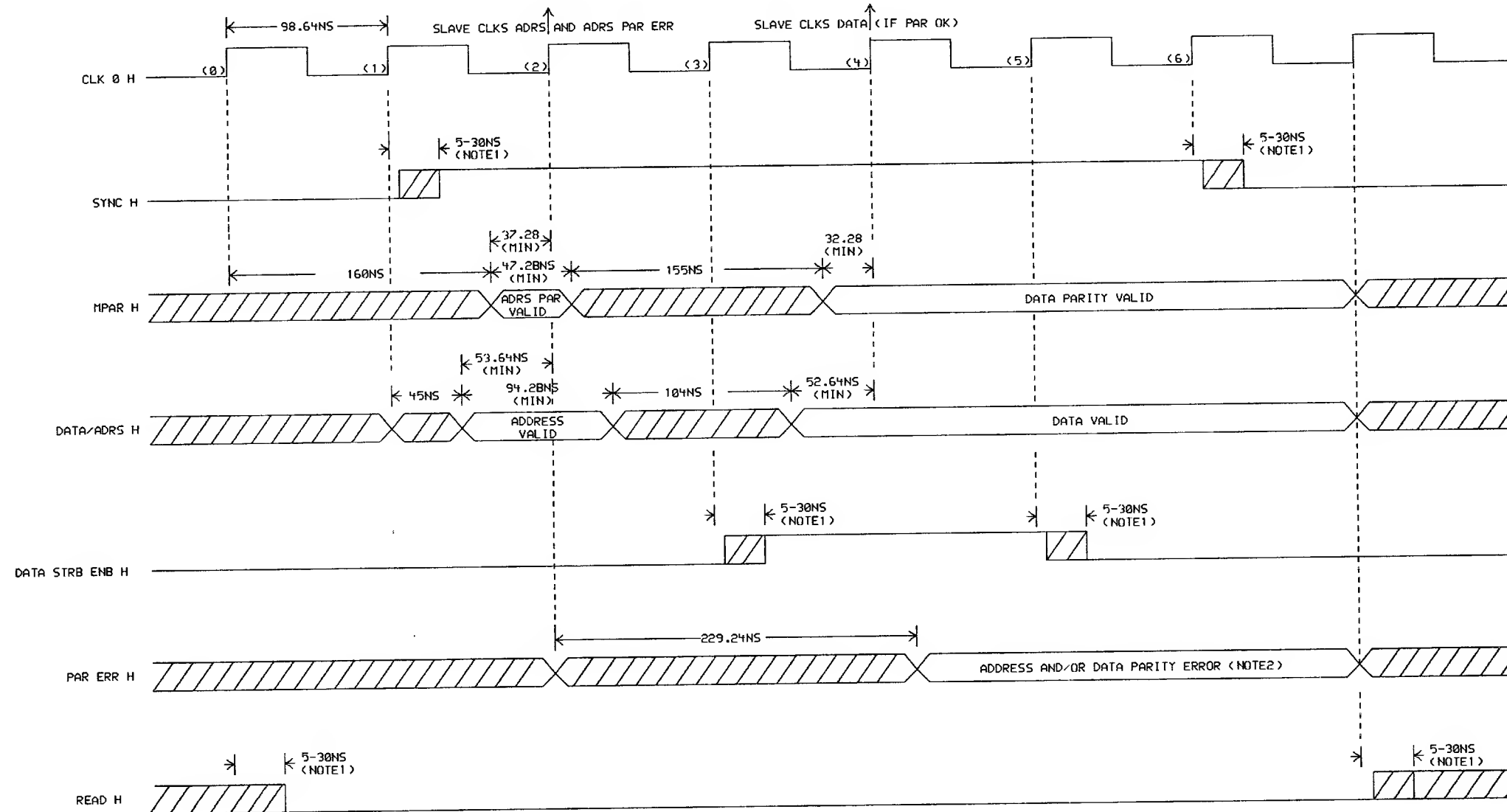
1



# DASH BUS HARDWARE REGISTER WRITE CYCLE

(NOTE - ALL SIGNALS GENERATED AT MASTER ARE SHOWN AT THE OUTPUT OF THE SLAVES RECEIVERS. ALL SIGNALS GENERATED BY THE SLAVE ARE SHOWN AT THE INPUT TO THE SLAVES DRIVERS)

(NOTE - ALL TIMES ARE MAX TIMES UNLESS OTHERWISE SPECIFIED)



(NOTE1 - SYNC, DATA STRB ENB, READ, AND CLK 0 ARE IN THE SAME XCEIVER CHIP, KEEP EQUAL LOADING ON THE RECEIVER OUTPUT TO INSURE THERE WILL BE NO SKEW BETWEEN THESE SIGNALS SEE ELECTRICAL REQ. SEC. OF DASH BUS FUNC. SPEC.)

(NOTE2 - DATA OR ADDRESS PARITY ERROR SHOWN AT SLAVE AT LATEST TIME ALLOWED TO REPORT TO MASTER.)

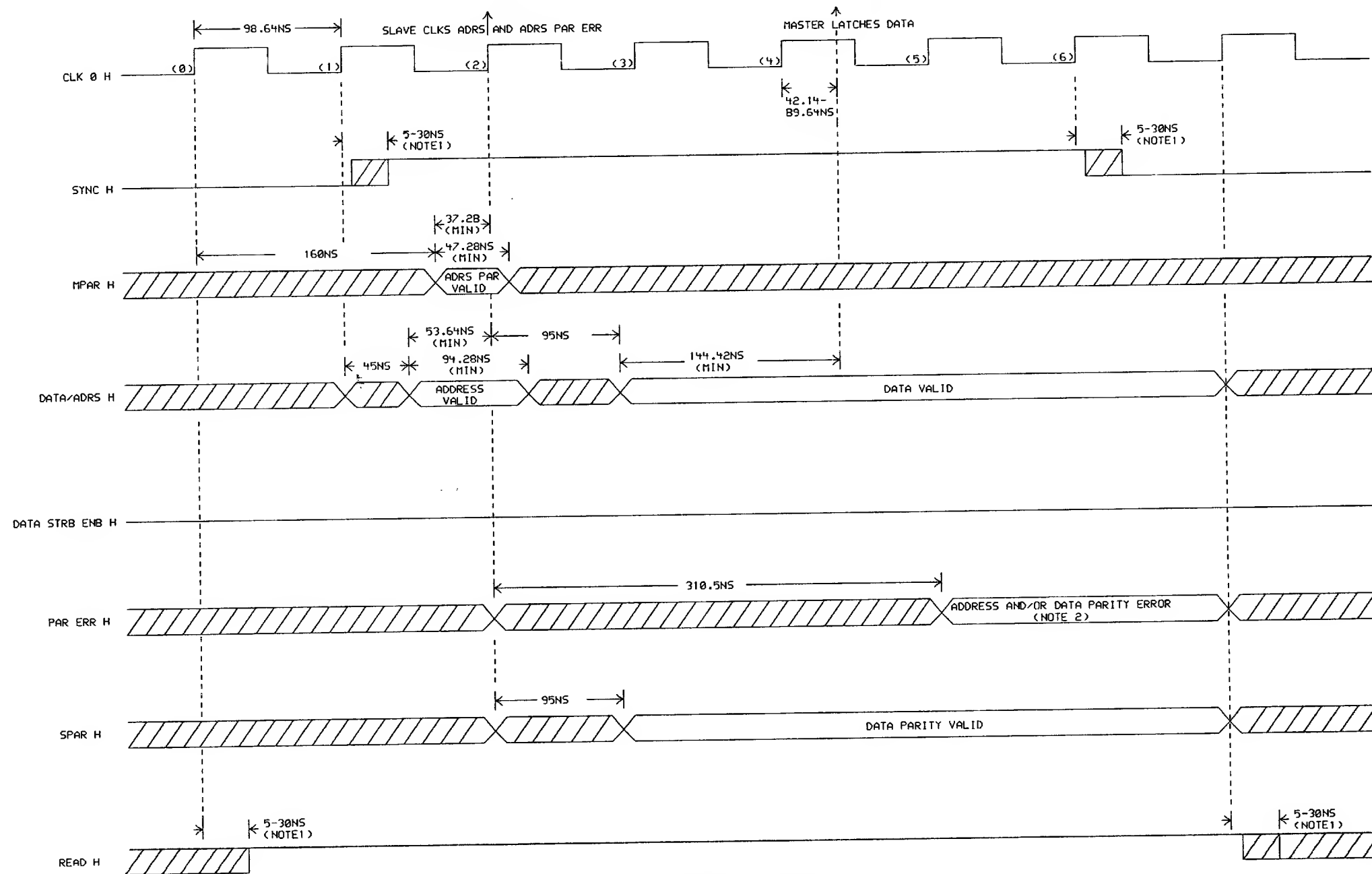
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	82-NOV-82	BOARD LOCATION:		PROTOCOL ASSIST
DSKD: <MAYHEW>LDSH.DRW	102-NOV-82 10:25	SHEET 1 OF 1		BM SCANNER
FIRST USED ON OPTION/MODEL:	DECSA	D-UA-M3111-0-0	SIZE	CODE
			K	CS
			NUMBER	REV.
			M3111-0-P19	A



# DASH BUS HARDWARE REGISTER READ CYCLE

(NOTE - ALL SIGNALS GENERATED AT MASTER ARE SHOWN AT THE OUTPUT OF THE SLAVES RECEIVERS. ALL SIGNALS GENERATED BY THE SLAVE ARE SHOWN AT THE INPUT TO THE SLAVES DRIVERS)

(NOTE - ALL TIMES ARE MAX TIMES UNLESS OTHERWISE SPECIFIED)



(NOTE 1 - SYNC, DATA STRB ENB, READ, AND CLK 0 ARE IN THE SAME XCEIVER CHIP, KEEP EQUAL LOADING ON THE RECEIVER OUTPUT TO INSURE THERE WILL BE NO SKEW BETWEEN THESE SIGNALS SEE ELECTRICAL REQ. SEC. OF DASH BUS FUNC. SPEC.)

(NOTE 2 - DATA PARITY ERROR SHOWN AT SLAVE AS REPORTED BY MASTER.)

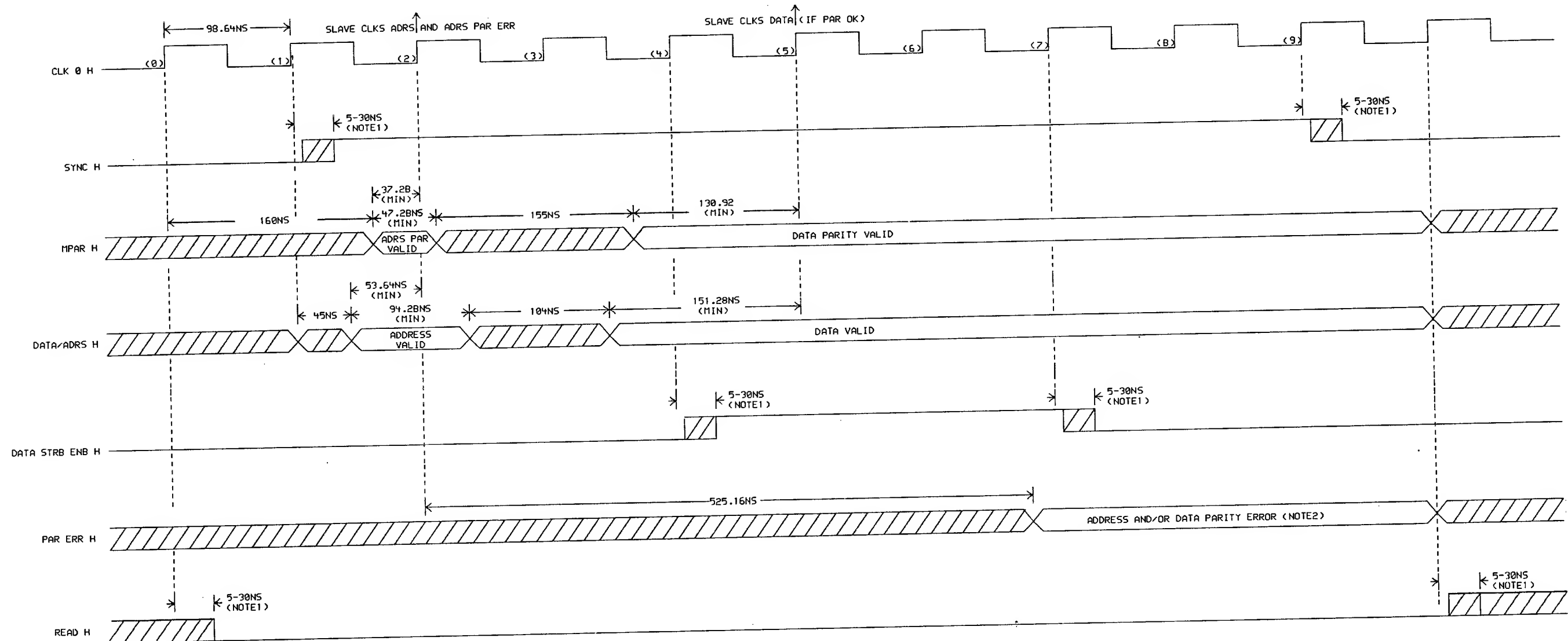
DRN.	DATE	ENG.	DATE	TITLE: PROTOCOL ASSIST
CHK'D.	82-NOV-82	BOARD LOCATION:		BM SCANNER
DSKD: (MAYHEW) LCSR.DRW	10:24	NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL: DECSA	D-LA-M3111-0-0	SIZE	CODE	NUMBER
		K	CS	M3111-0-P20
				REV. A



# DASH BUS LSI REGISTER WRITE CYCLE

(NOTE - ALL SIGNALS GENERATED AT MASTER ARE SHOWN AT THE OUTPUT OF THE SLAVE'S RECEIVERS. ALL SIGNALS GENERATED BY THE SLAVE ARE SHOWN AT THE INPUT TO THE SLAVE'S DRIVERS)

(NOTE - ALL TIMES ARE MAX TIMES UNLESS OTHERWISE SPECIFIED)



(NOTE1 - SYNC, DATA STRB ENB, READ, AND CLK 0 ARE IN THE SAME XCEIVER CHIP, KEEP EQUAL LOADING ON THE RECEIVER OUTPUT TO INSURE THERE WILL BE NO SKEW BETWEEN THESE SIGNALS SEE ELECTRICAL REQ. SEC. OF DASH BUS FUNC. SPEC.)

(NOTE2 - DATA OR ADDRESS PARITY ERROR SHOWN AT SLAVE AT LATEST TIME ALLOWED TO REPORT TO MASTER.)

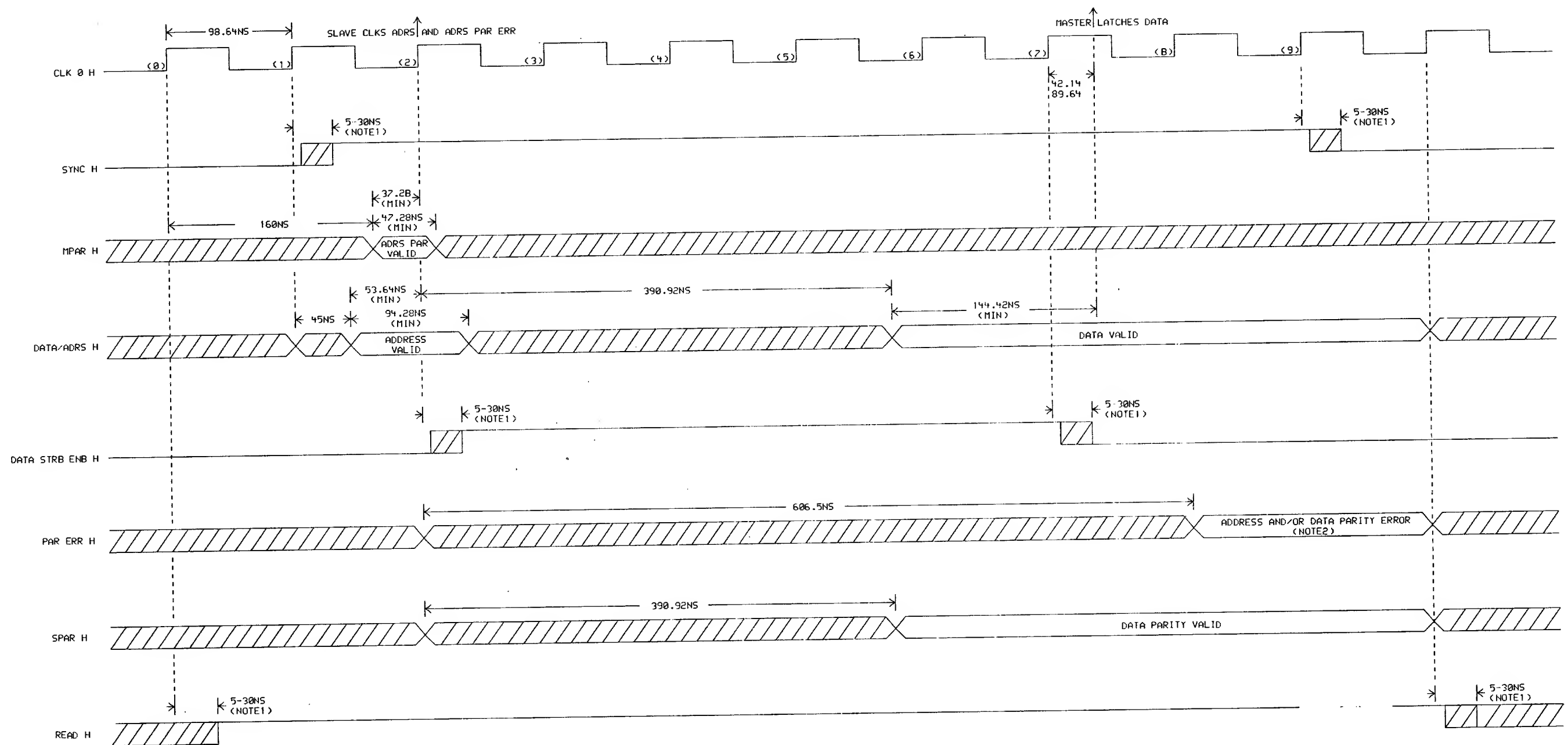
DRN.	DATE	ENG.	DATE	TITLE: PROTOCOL ASSIST
CHK'D.	DATE	BOARD LOCATION:		BM SCANNER
DSKD: <MAYHEW> LCOLW.DRW		02-NOV-82 10:23	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL: DECSA		D-LA-M3111-0-0		K CS M3111-0-P21
				REV. A



# DASH BUS LSI REGISTER READ CYCLE

(NOTE - ALL SIGNALS GENERATED AT MASTER ARE SHOWN AT THE OUTPUT OF THE SLAVE RECEIVERS. ALL SIGNALS GENERATED BY THE SLAVE ARE SHOWN AT THE INPUT TO THE SLAVE DRIVERS)

(NOTE - ALL TIMES ARE MAX TIMES UNLESS OTHERWISE SPECIFIED)



(NOTE1 - SYNC, DATA STRB ENB, READ, AND CLK 0 ARE IN THE SAME XCEIVER CHIP, KEEP EQUAL LOADING ON THE RECEIVER OUTPUT TO INSURE THERE WILL BE NO SKEW BETWEEN THESE SIGNALS SEE ELECTRICAL REQ. SEC. OF DASH BUS FUNC. SPEC.)

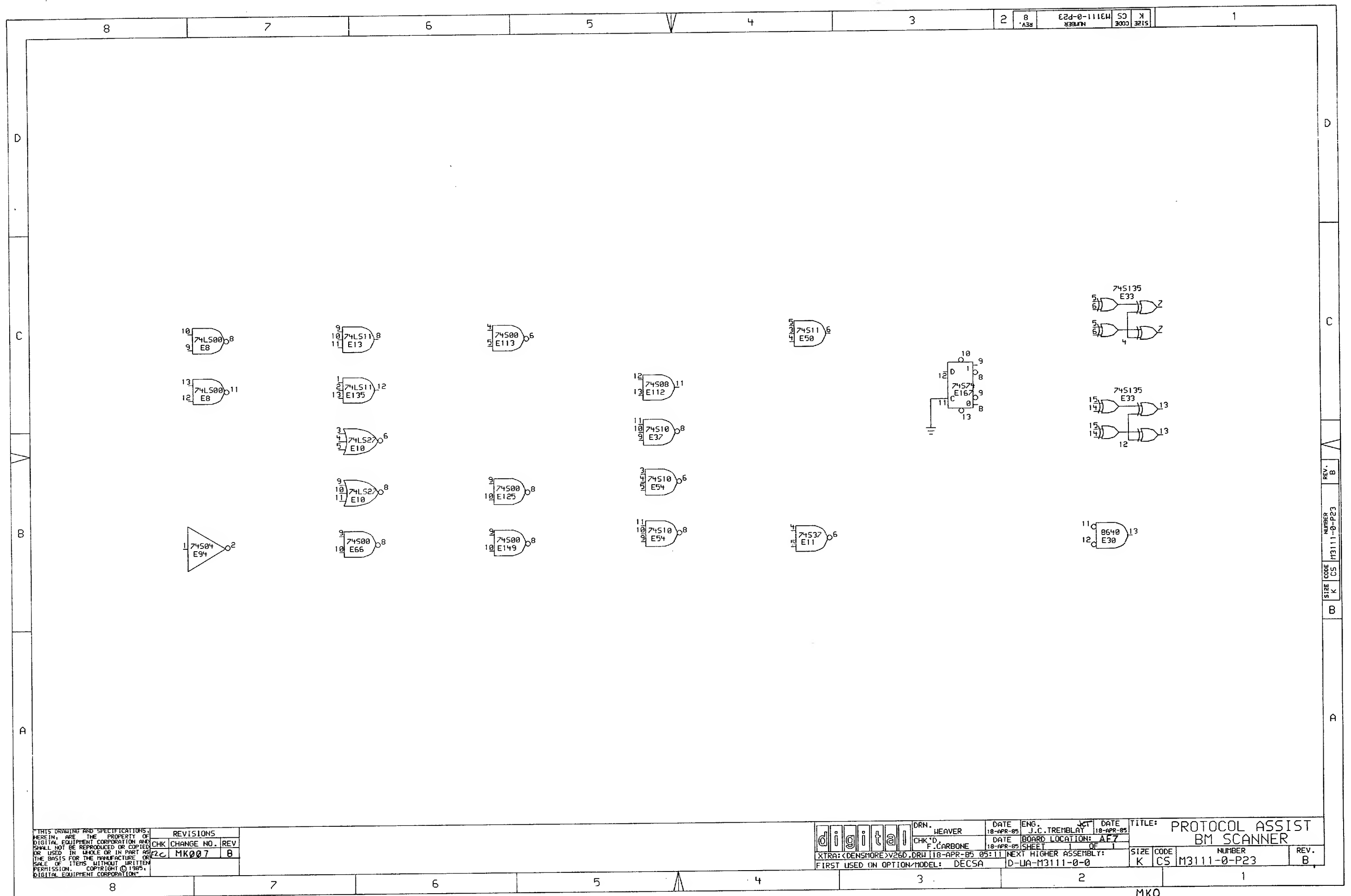
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REVISIONS  
CHK CHANGE NO. REV

digital DRN. DATE 20-DEC-83 ENG. DATE TITLE: PROTOCOL ASSIST BM SCANNER  
CHK'D. DATE BOARD LOCATION: SHEET 1 OF 1  
DSKU: (DAHE) LDI.R.DRW 02-DEC-83 11:38 NEXT HIGHER ASSEMBLY: SIZE CODE NUMBER REV.  
FIRST USED ON OPTION/MODEL: DECSA D-UA-M3111-0-0 K CS M3111-0-P22 B







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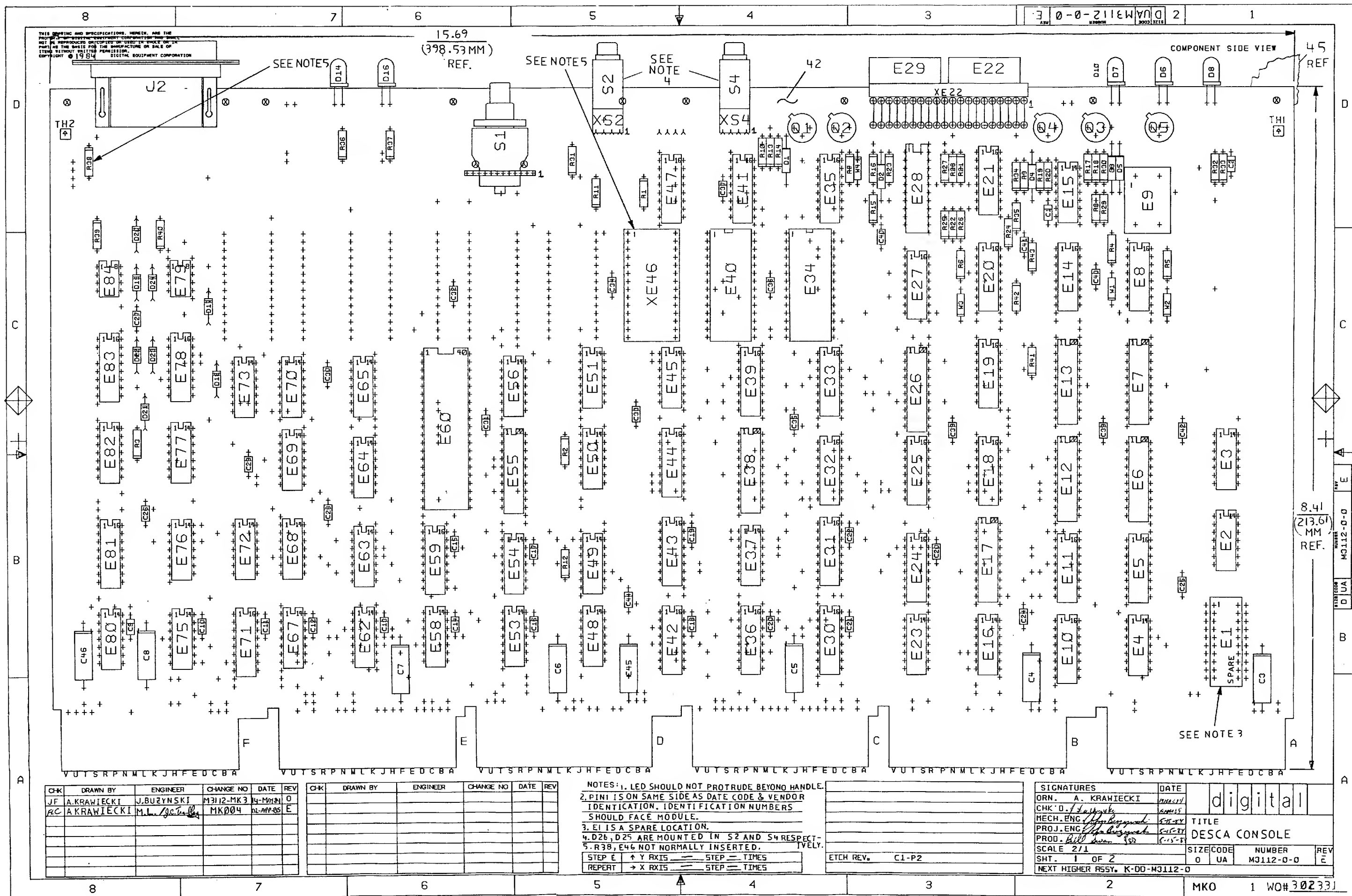
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1			M3112-00	MODULE REV													
2	D-UA-M3112-0-0	3		DECSA CONSOLE		A	B	C	D	E	E						
3	K-PL-M3112-0-0	3		DECSA CONSOLE		A	B	C	D	E	F						
4	D-EC-5014994-0-0	2		ETCH CUT DRAWING		A	A	B	C	C	C						
5	D-MD-5014994-0-0	3		DRILL & ETCH DRAWING		A	A	A	B	B	B						
6			5014994	ETCHED BOARD		B	B	B	C1	C1	C1						
7	K-CS-M3112-0-K1	1		CONSOLE, COOT TERM.		A	A	A	A	A	A						
8	K-CS-M3112-0-K2	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
9	K-CS-M3112-0-K3	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
10	K-CS-M3112-0-K4	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
11	K-CS-M3112-0-K5	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
12	K-CS-M3112-0-K6	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
13	K-CS-M3112-0-K7	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
14	K-CS-M3112-0-K8	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
15	K-CS-M3112-0-K9	1		CONSOLE, BOOT TERM.		A	B	B	C	C	C						
16	K-CS-M3112-0-K10	1		CONSOLE, BOOT TERM.		A	A	A	B	B	B						
17	K-CS-M3112-0-K11	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
18	K-CS-M3112-0-K12	1		CONSOLE, BOOT TERM.		A	A	A	A	A	A						
19	K-PC-M3112-0-DBI			PC DATA BASE		A	A	B	B	B	C						
20	K-CS-M3112-0-DBS			CS DATA BASE		B	B	B	C	C	C						
21	K-CS-M3112-0-K13	1		CONSOLE, BOOT TERM.		A	B	C	D	D	E						
						-	-	-	A	A	B						

NOTES:

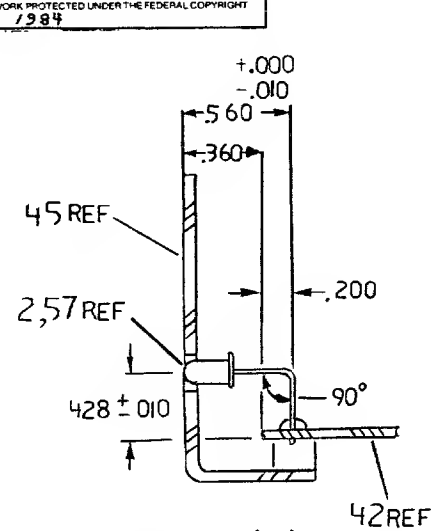
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ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	CHECKED BY: T. WALSH DATE: 01 NOV 82		TITLE DRAWING DIRECTORY									
ML	M3112-TW001	B				DESIGN ENGINEER: M. LAWRENCE DATE: 01 APR 85		DECSA CONSOLE									
ML	M3112-TW002	C				RESPONSIBLE ENG: M. LAWRENCE DATE: 01 APR 85											
	M3112-TW003	D				PRODUCTION ENG: W. BROOKE DATE: 01 APR 85											
	M3112-MK004	E															
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						SHEET 1 OF 1 M10											



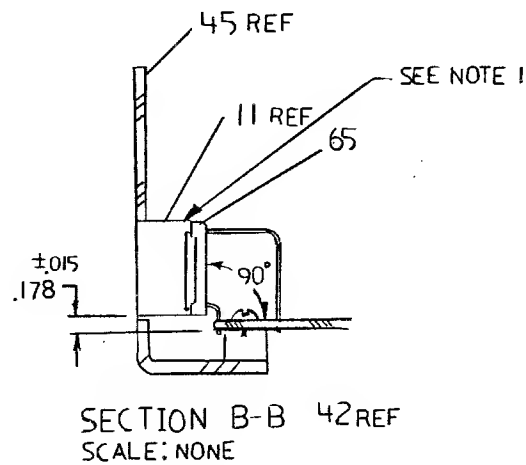




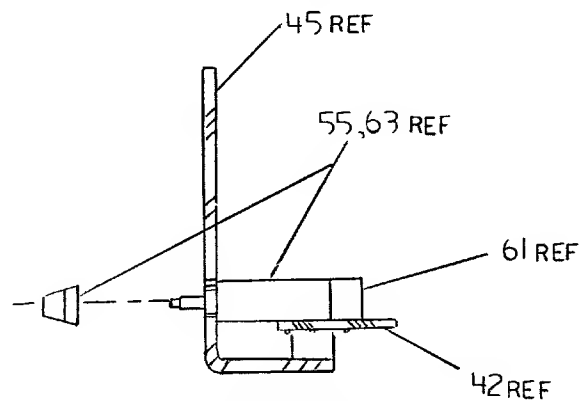
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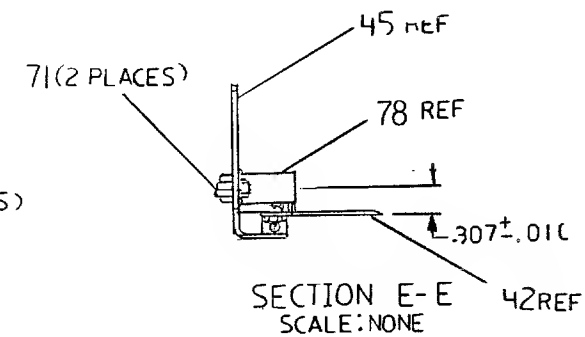
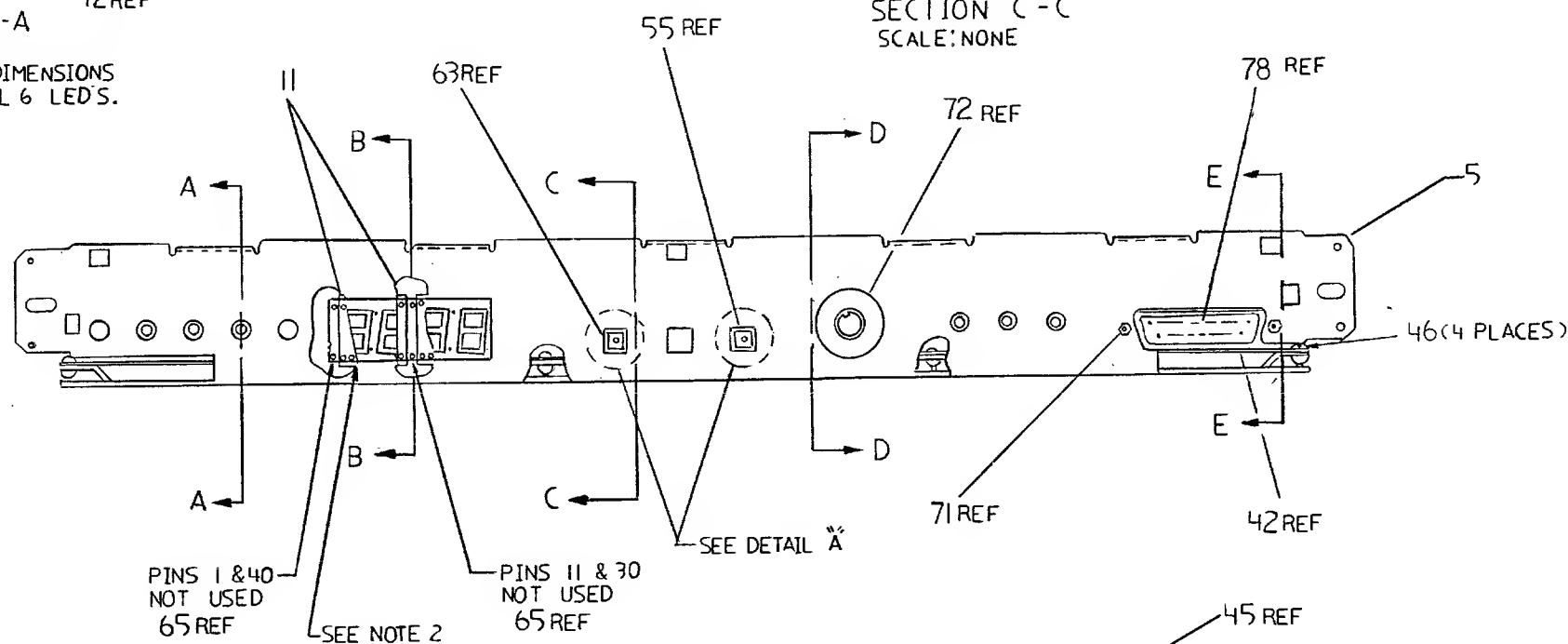
SECTION A-A  
SCALE: NONE  
NOTE: SAME DIMENSIONS FOR ALL 6 LED'S.



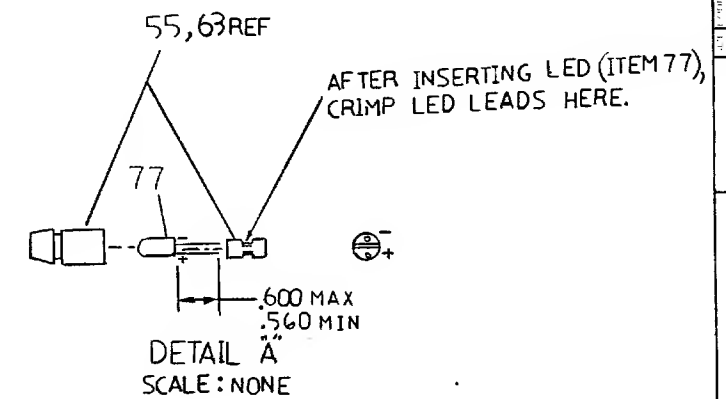
SECTION B-B 42 REF  
SCALE: NONE



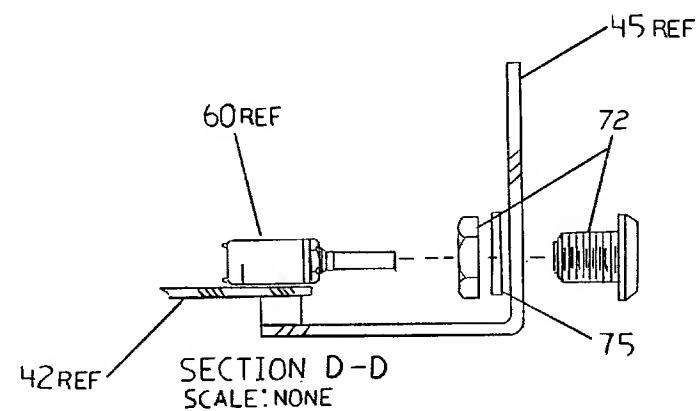
SECTION C-C  
SCALE: NONE



SECTION E-E  
SCALE: NONE



DETAIL A  
SCALE: NONE



SECTION D-D  
SCALE: NONE

DATE	ECO NUMBER	REV

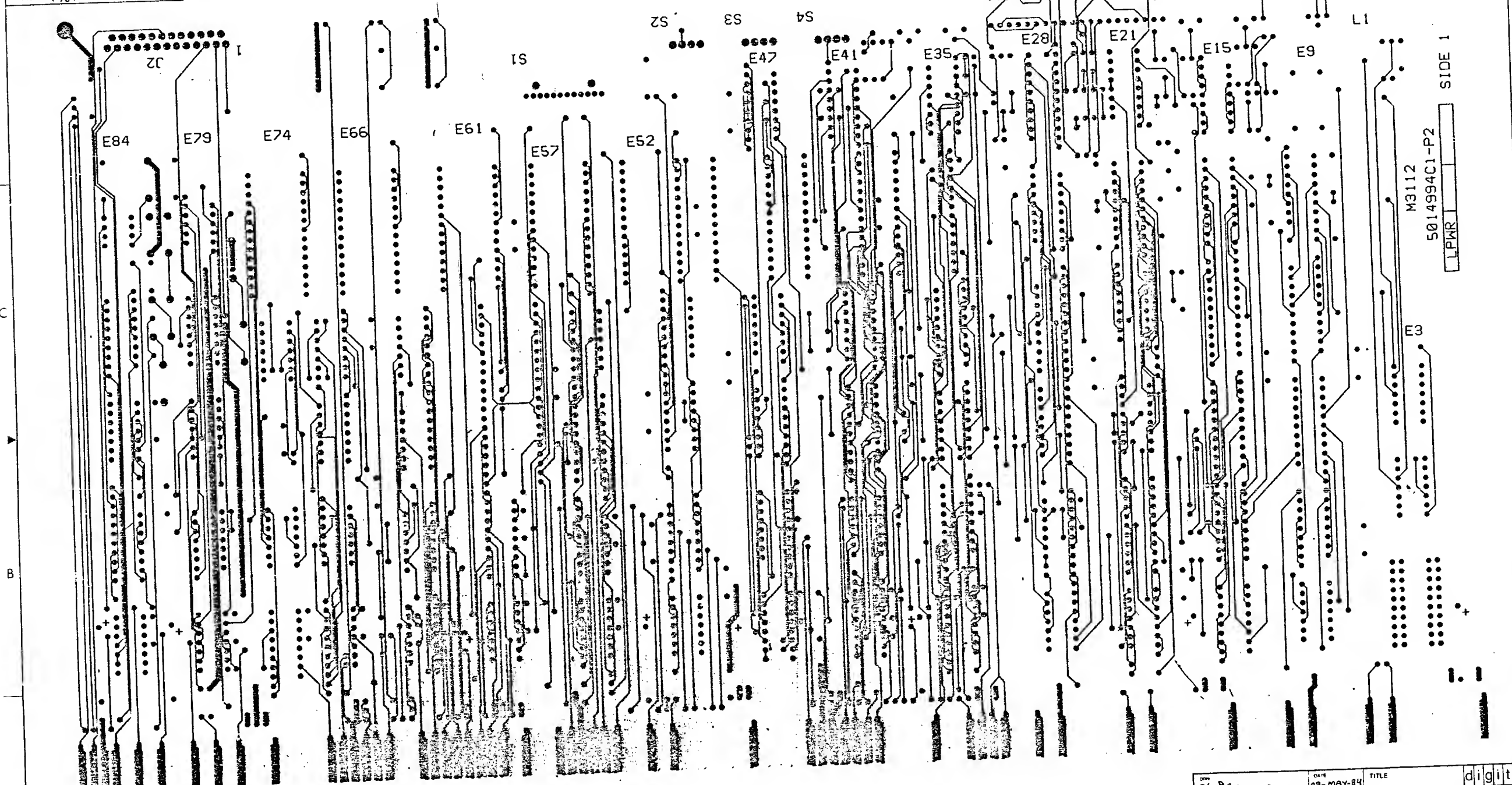
TITLE  
DECSA CONSOLE

SIZE	CODE	NUMBER	REV
DUA	M3112-0-0		E
SCALE	1:1	SHEET 2 OF 2	

MKO



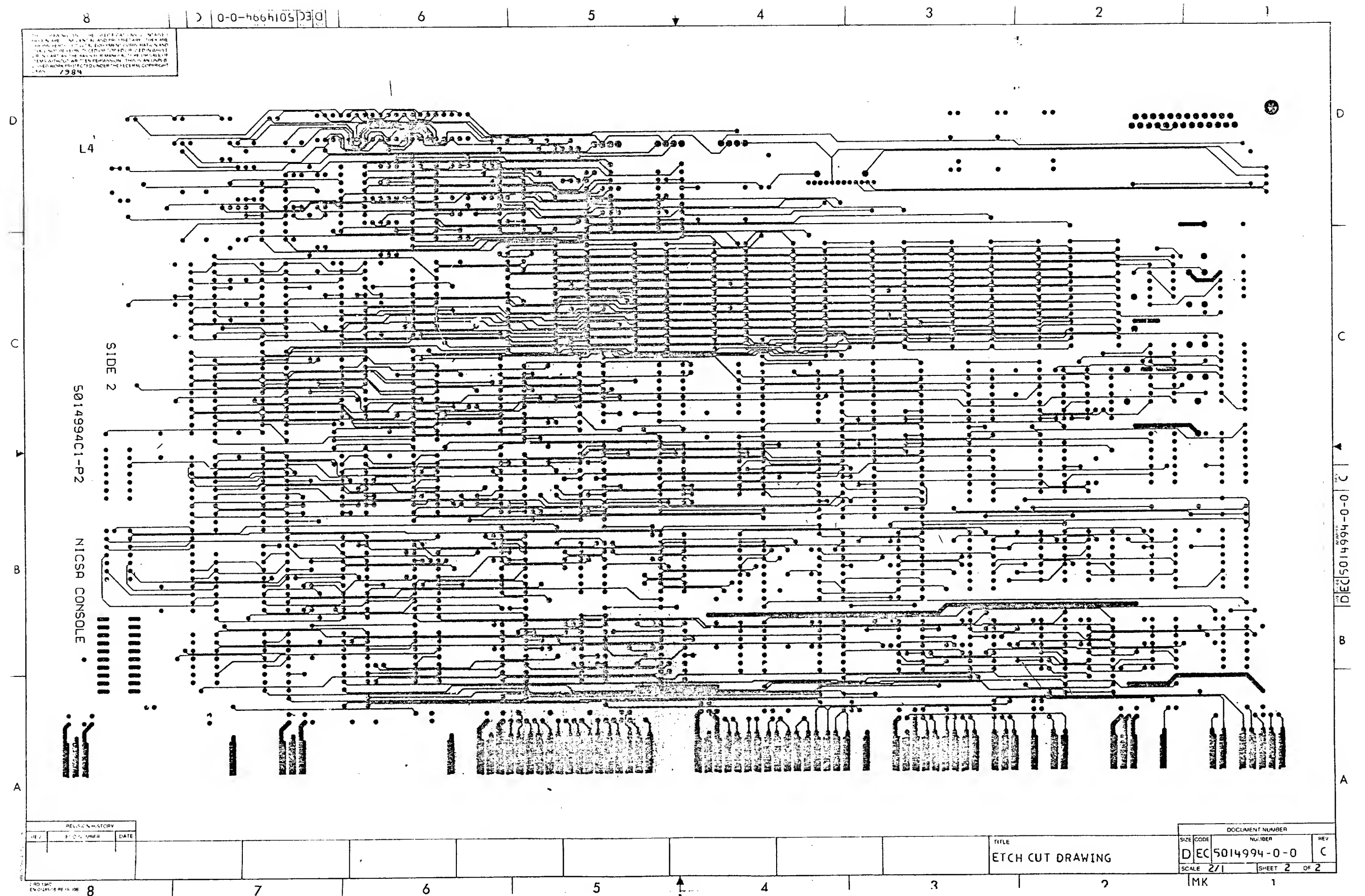
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REV	ECO NUMBER	DATE	DESCRIPTION
1	1	15 MAY 84	INITIAL DESIGN
2	2	15 MAY 84	REVISION 1
3	3	15 MAY 84	REVISION 2
4	4	15 MAY 84	REVISION 3
5	5	15 MAY 84	REVISION 4
6	6	15 MAY 84	REVISION 5
7	7	15 MAY 84	REVISION 6
8	8	15 MAY 84	REVISION 7

DESIGNED BY H. Denamore	DATE 09-MAY-84	TITLE digital
CHIEF ENGINEER J. Talar	DATE 15-MAY-84	ETCH CUT DRAWING
DESIGNER J. Talar	DATE 15-MAY-84	
REVIEWER J. Talar	DATE 15-MAY-84	
APPROVED BY J. Talar	DATE 15-MAY-84	
FOR DOC. NO. D-UA-M3142-0-0		DOCUMENT NUMBER D EC 5014994-0-0 C
		SCALE 2/1
		MARK 1











AUTOMATED BY VAXKPL (V1.1)

## P A R T S   L I S T

SHEET A2   OF A3

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV 00 02	REFERENCE DESIGNATORS
33	6	19-12799-00		LS00 NAND-GATE-QUAD 2IN,P	2	E4,E15
34	4	19-12803-00		LS04 INVERTER GATE,HEX	3	E2,E16,E56
35	27	19-12805-00		LS08 AND GATE-QUAD 2IN,PO	2	E65,E73
36	12	19-12807-00		LS10 NAND GATE-TRIPLE 3IN	2	E23,E51
37	48	19-12808-00		LS11 AND GATE-TRIPLE 3IN	1	E63
38	26	19-12810-00		LS20 NAND GATE-DUAL 4IN	1	E64
39	28	19-12811-00		LS21 AND GATE-DUAL 4IN,PO	3	E68,E72,E77
40	47	19-12813-00		LS27 NOR GATE-TRIPLE 3IN	1	E76
41	20	19-12816-00		LS32 OR GATE-QUAD 2IN,POS	4	E45,E48,E69,E70
42	33	19-12824-00		LS74 FF-D DUAL,EDGE TRIGG	1	E82
43	7	19-12843-00		LS139 DECODER, 2 OF 4(DUAL	2	E5,E41
44	14	19-12847-00		LS157 MUX 1 OF 2(QUAD)	3	E27,E32,E35
45	21	19-12851-00		LS169 COUNTER,SYNCH. UP/DO	1	E47
46	32	19-12853-00		LS175 FF-D QUAD	2	E81,E83
47	8	19-12863-00		LS273 FF-D OCTAL W/CLEAR	6	E6,E7,E12,E13,E17,E26
48	5	19-13414-00		LS14 INVERTER GATE-HEX SC	1	E3
49	11	19-13478-00	MAN	6710 SEGMENT DISPLAY LED,	2	E22,E29
50	15	19-13493-00	74S241	OCTAL BUFFER,TRI-STA	1	E28
51	19	19-13939-00	LS191	COUNTER,SYNCH. UP/D	1	E44
52	24	19-14214-00	LS374	FF-D OCTAL EDGE TRIG	1	E55
53	22	19-14451-00	LS393	COUNTER,BINARY,4BIT	1	E49
54	18	19-15193-00	LS244	DRIVER,LINE,OCTAL,TR	1	E38
55	34	19-15415-00	9636	DRIVER,DUAL,EIA RS-4	1	E84
56	30	19-19542-01	9639	RECEIVER,LINE,DUAL	1	E79
57	25	21-17312-00		UART DL-11 SOFTWARE	1	E60
58	29	21-19305-01	201	SWITCH,ANALOG,QUAD	1	E78
59	46	90-08301-01		SCREW,MACH PAN PHIL 4-	4	
60	45	70-19151-00		CBT HANDLE ASSY.	1	
61	44	#BLANK		*** THIS ITEM IS NOT USED ***	-	
62	43	90-09185-00		JUMPER, WIRE, INSULATED, BLACK B	4	W1-W4
63	55	12-18945-06		SW,PB,LT 1PST NO-MOMENTARY .25A	1	S2
64	56	#BLANK		*** THIS ITEM IS NOT USED ***	-	
65	62	12-20449-01		*** THIS ITEM IS NOT USED ***	-	
66	66	#BLANK		*** THIS ITEM IS NOT USED ***	-	
67	67	#BLANK		*** THIS ITEM IS NOT USED ***	-	
68	68	#BLANK		*** THIS ITEM IS NOT USED ***	-	
69	69	#BLANK		*** THIS ITEM IS NOT USED ***	-	
70	70	#BLANK		*** THIS ITEM IS NOT USED ***	-	
71	71	91-05740-55		*** THIS ITEM IS NOT USED ***	-	
72	72	91-07688-55		*** THIS ITEM IS NOT USED ***	-	
73	73	90-08451-01		SCREW LOCK,STANDOFF ONLY .060TH	2	
74	74	12-16178-06		LOCK,PLASTIC 3POS ASSY	1	
75	75	23-112E4-00		E4-01,E4-02,E4-03,E4-06,E4-07ROM	1	E34
76	76	23-113E4-00		E4-01,E4-02,E4-03,E4-06,E4-07ROM	1	E40
77	77	90-09750-00		O-RING, RUBBER, .94 OD X .75 ID	1	
78	78	91-07776-55		*** THIS ITEM IS NOT USED ***	-	
79	79	11-24447-01		LED 1.5MCD@20MA RED T1 .100CC	2	D25,D26
80	80	12-20398-07		CONN,D SUB 25PIN ASSY RT ANGLE	1	J2
81	81	23-421E4-00		E4-06,E4-11 U REF	-	
				*** THIS ITEM IS NOT USED ***	-	
				*** THIS ITEM IS NOT USED ***	-	

D	I	G	I	T	A	L	TITLE	SECTION A	OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							DECSA CONSOLE			K	PL	N3112-0-0	F



AUTOMATED BY VAXKPL (V1.1)

PARTS LIST

SHEET A3 OF A3

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV 00 D2	REFERENCE DESIGNATORS
-----------	--------------	-------------	------------	-------------	-----------------------------	-----------------------

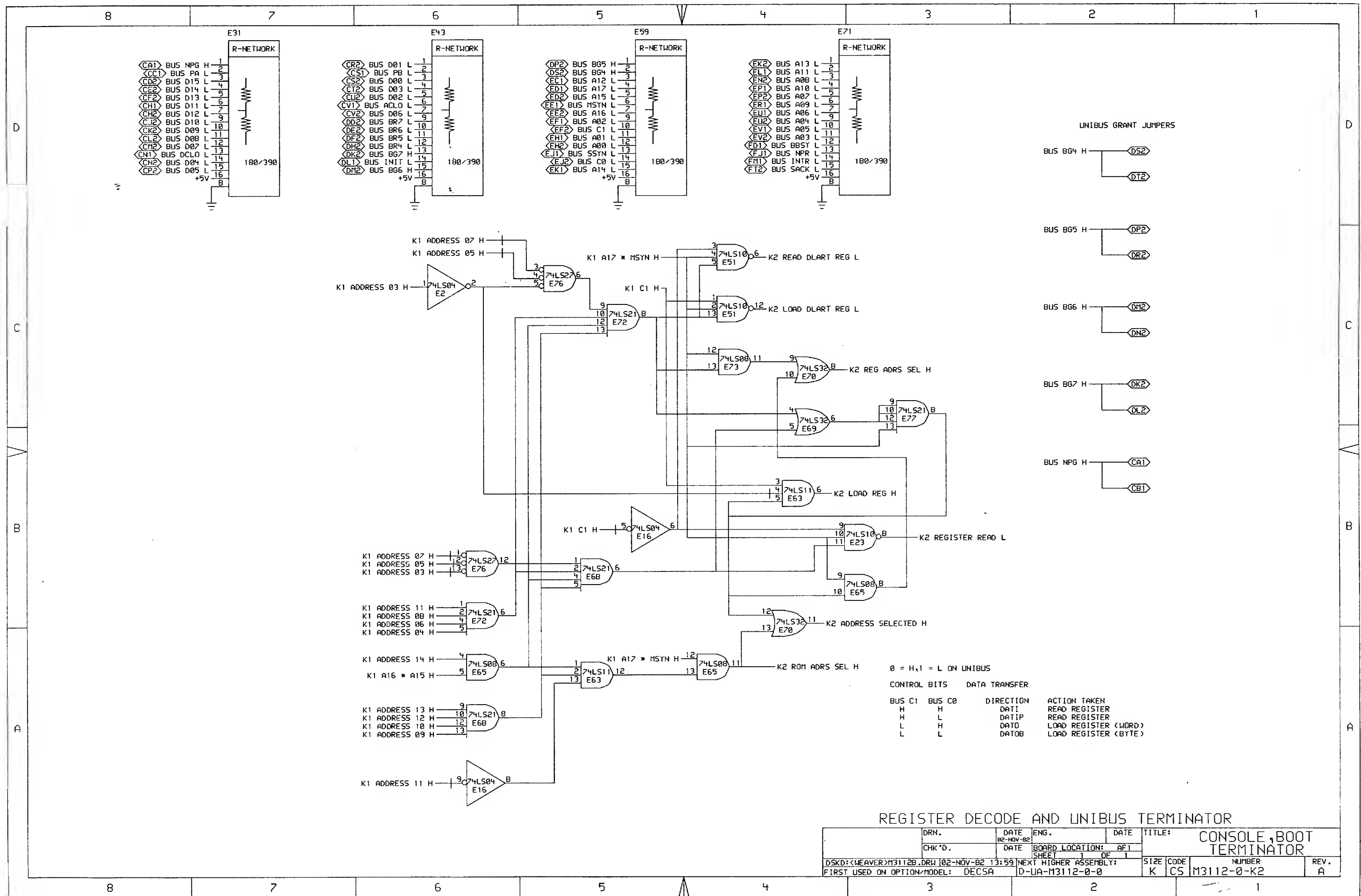
- 1 GEN: NOTE 1. R38 AND E46 ARE NOT NORMALLY INSERTED.  
2 GEN: NOTE 2. E1 IS A SPARE IC LOCATION.  
3 GEN: NOTE 3. LINE 79 E46 IS FOR MANUFACTURING USE ONLY.

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							DECSA CONSOLE		K	PL	M3112-0-0	F

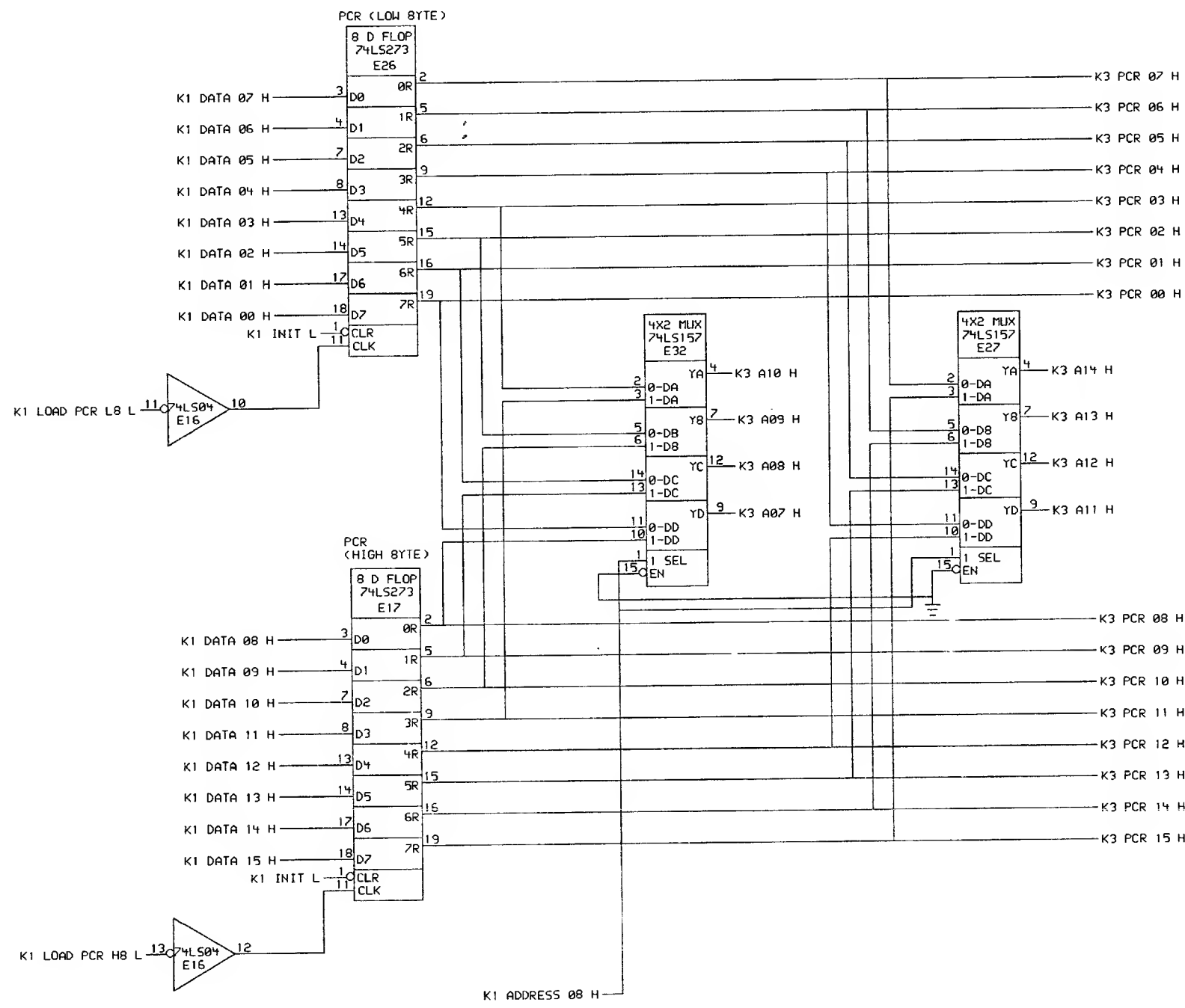








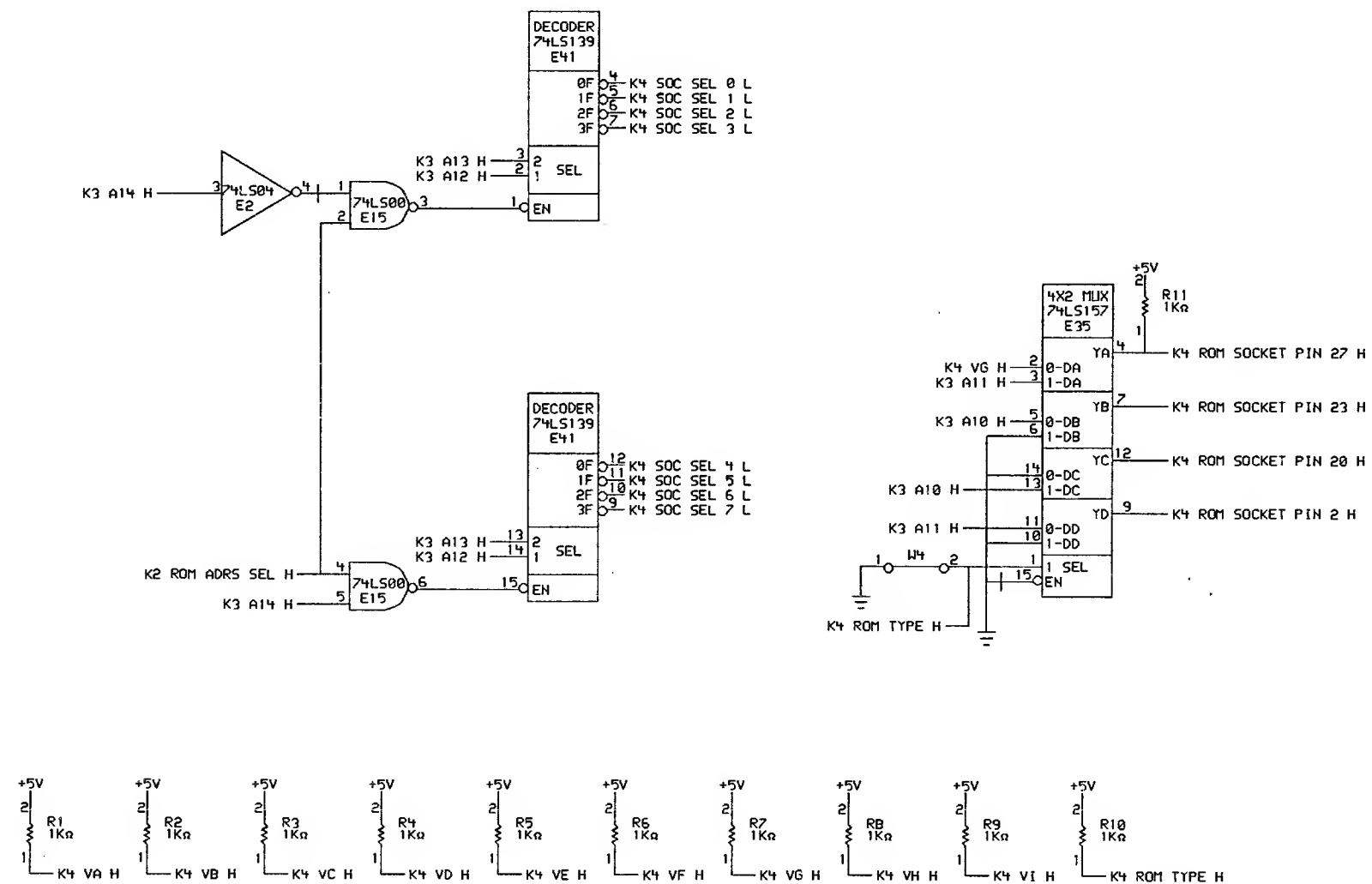




PAGE CONTROL REGISTER (PCR)

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	AF1	CONSOLE, BOOT
DSKD: <MEASUR> M3112C.DRW	02-NOV-82 13:59	NEXT HIGHER ASSEMBLY:		TERMINATOR
FIRST USED ON OPTION/MODEL:	DECSA	D-UA-M3112-0-0	SIZE CODE	NUMBER
			K CS	M3112-0-K3
				REV. A

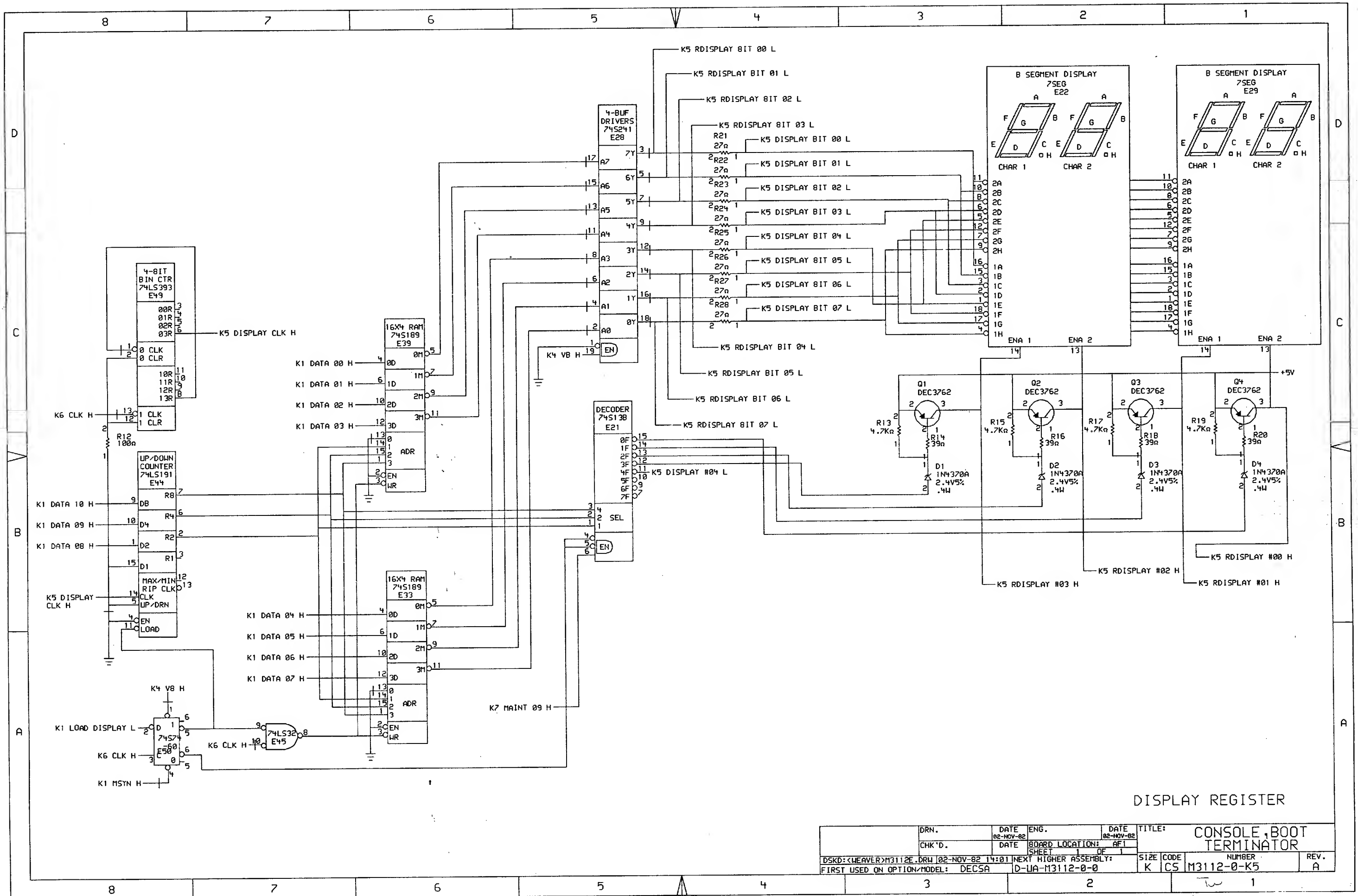




SOCKET SELECTION LOGIC

DRN.	DATE 02-NOV-82	ENG.	DATE 02-NOV-82	TITLE: CONSOLE, BOOT TERMINATOR
CHK'D.	DATE	BOARD LOCATION: AF1	SHEET 1 OF 1	SIZE CODE K CS
DSKD: C:\EAVR\M3112.DRW 102-NOV-82 14:00	NEXT HIGHER ASSEMBLY: DECSA	D-UA-M3112-0-0	NUMBER M3112-0-K4	REV. A
FIRST USED ON OPTION/MODEL:	DECSA	D-UA-M3112-0-0		







8 7 6 5 4 3 2 1

D

C

B

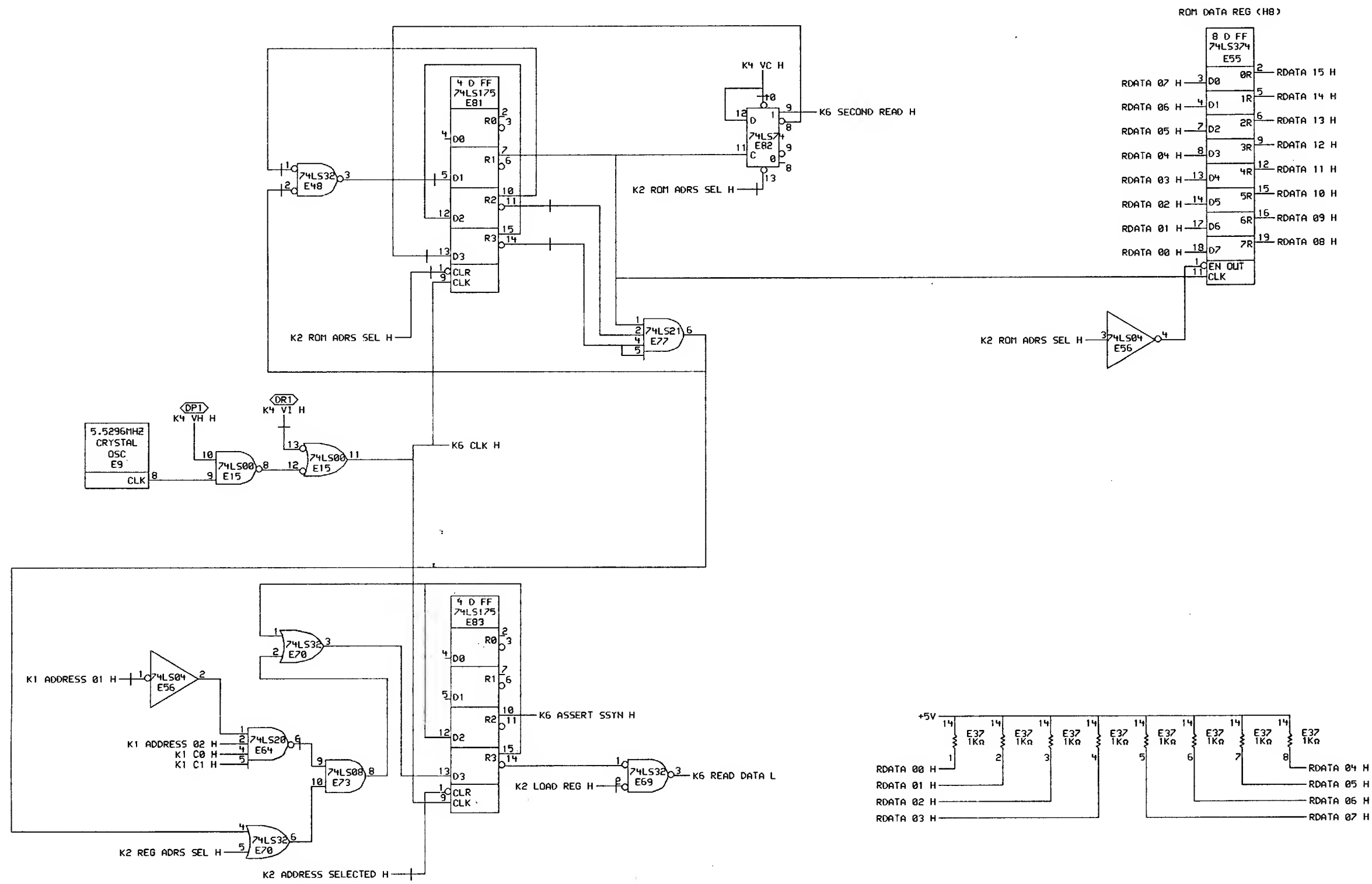
A

D

C

B

A

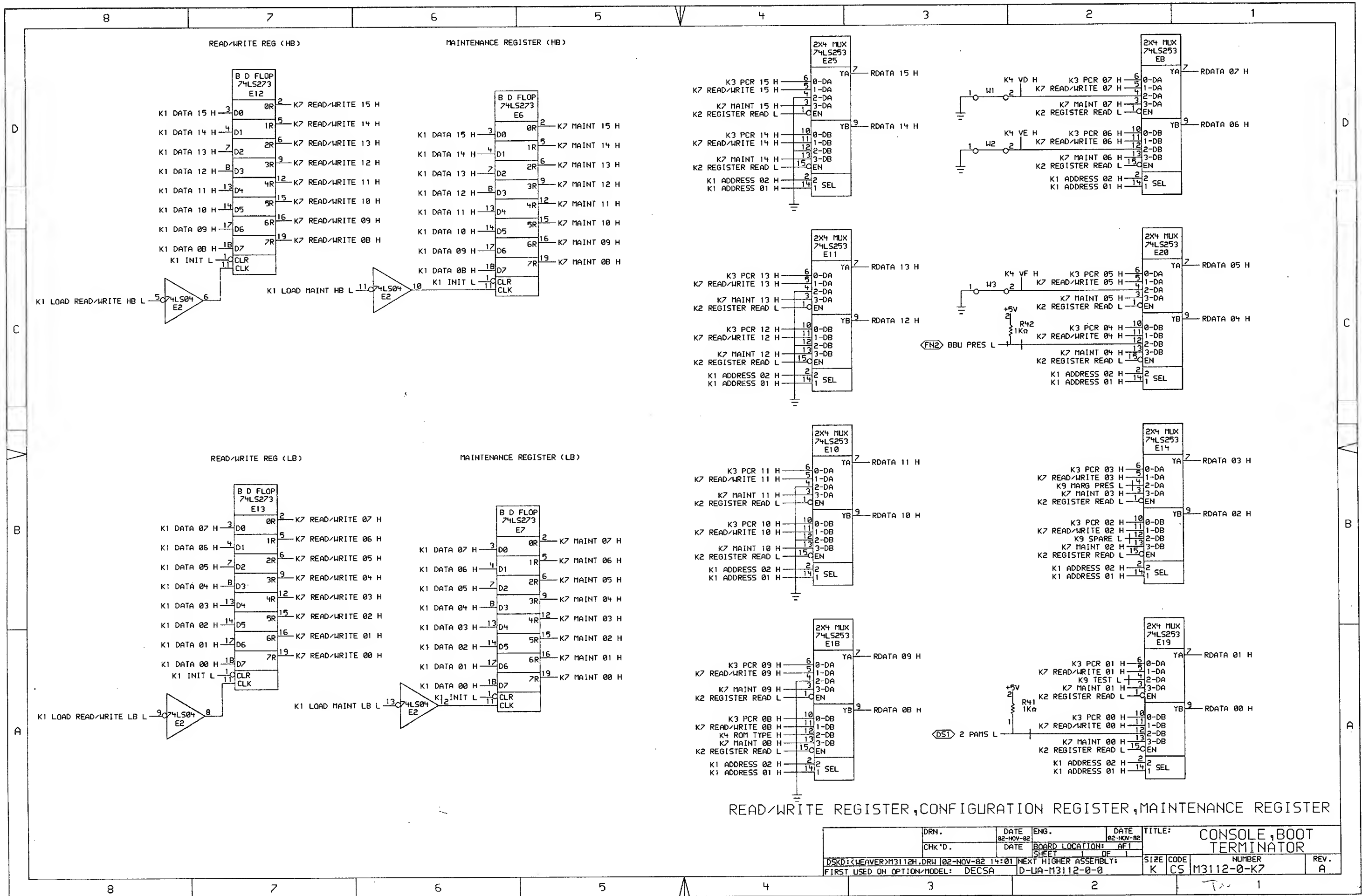


TIMING CIRCUIT AND ROM DATA REGISTER

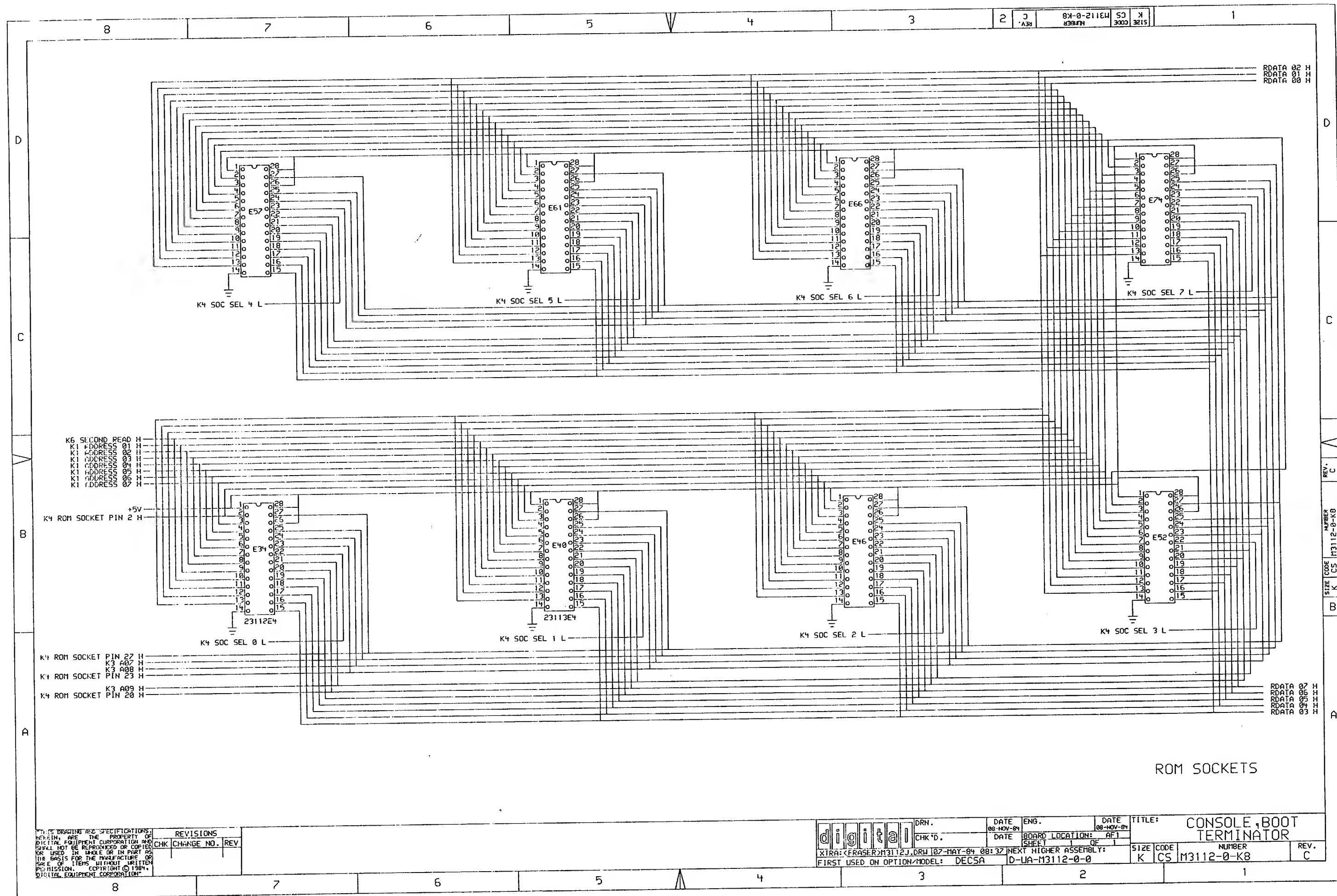
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	02-NOV-82	02-NOV-82	02-NOV-82	CONSOLE BOOT TERMINATOR
DSKD:CLEAVER>M3112F.DRW	02-NOV-82 14:01	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: DECSA	D-UA-M3112-0-0		K	CS
				NUMBER
				M3112-0-K6
				REV.
				A

8 7 6 5 4 3 2 1

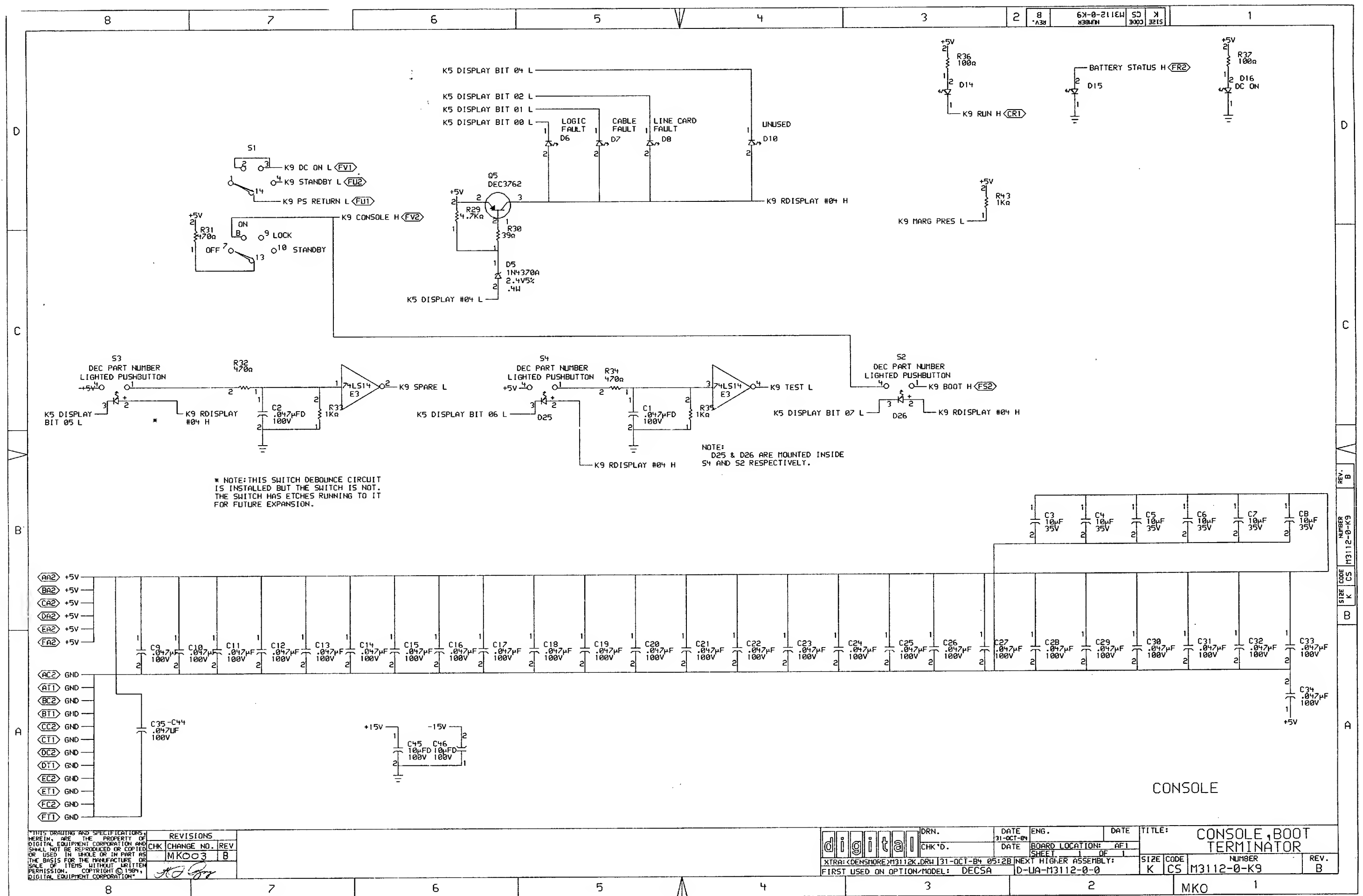




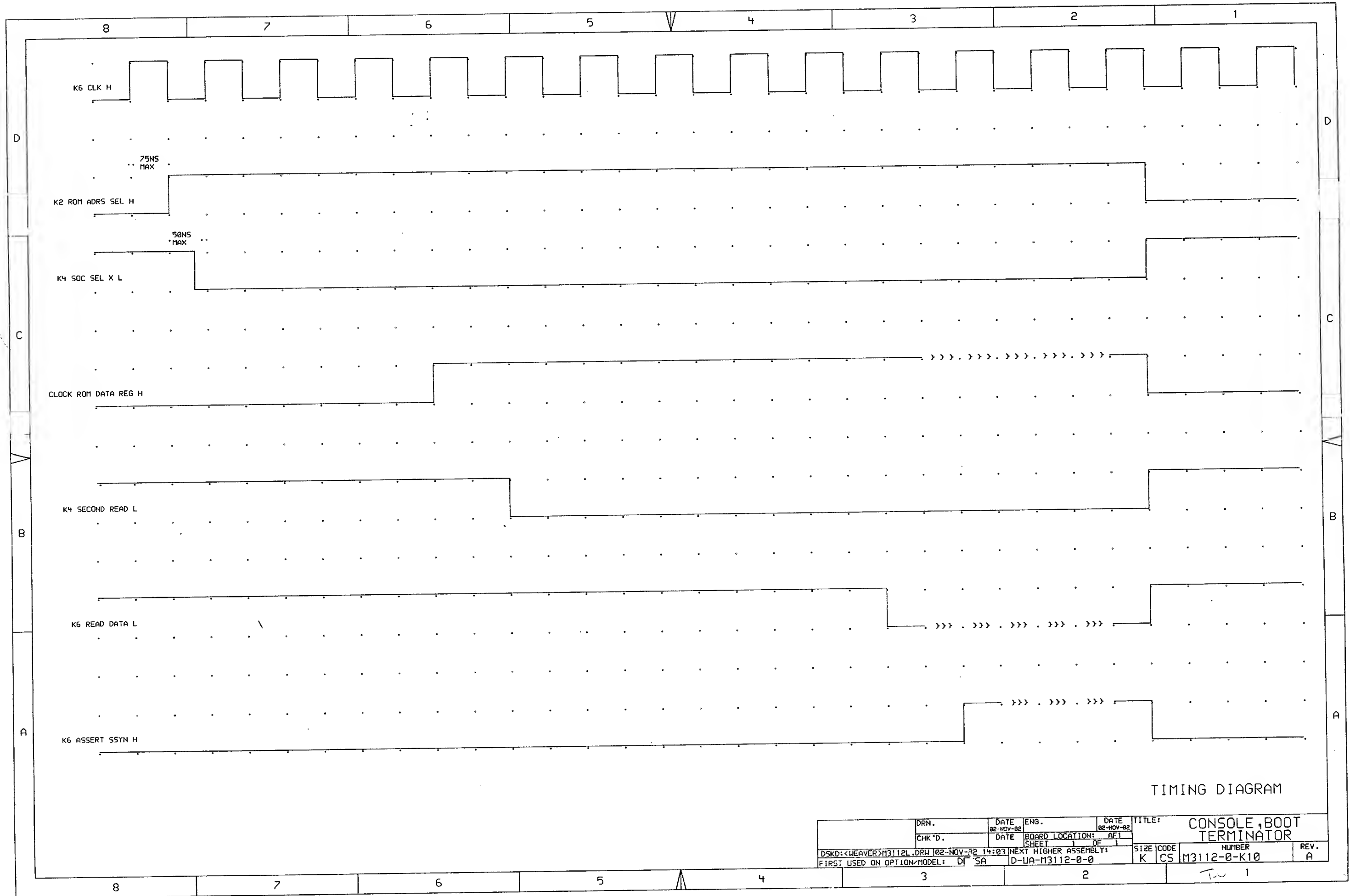






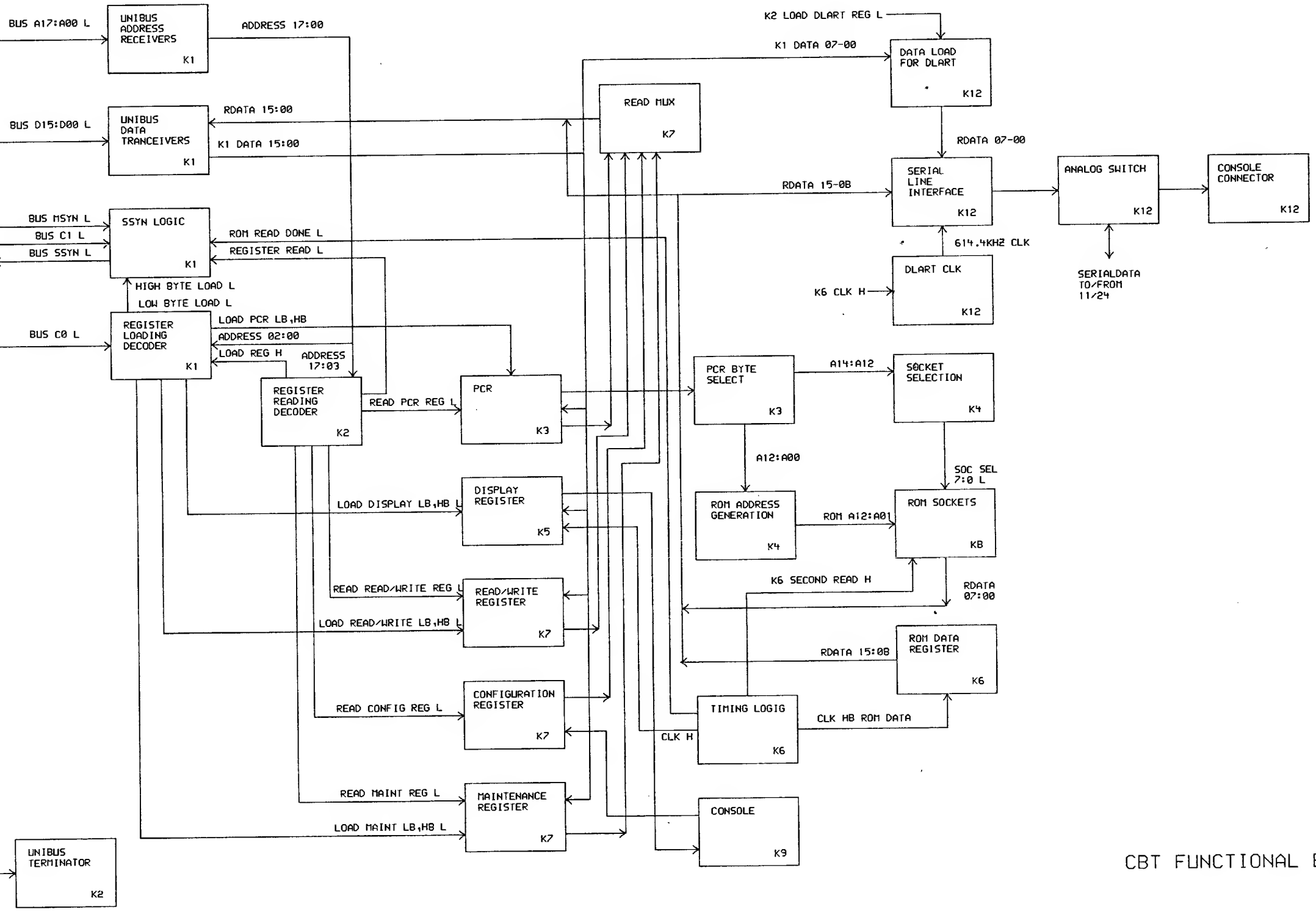








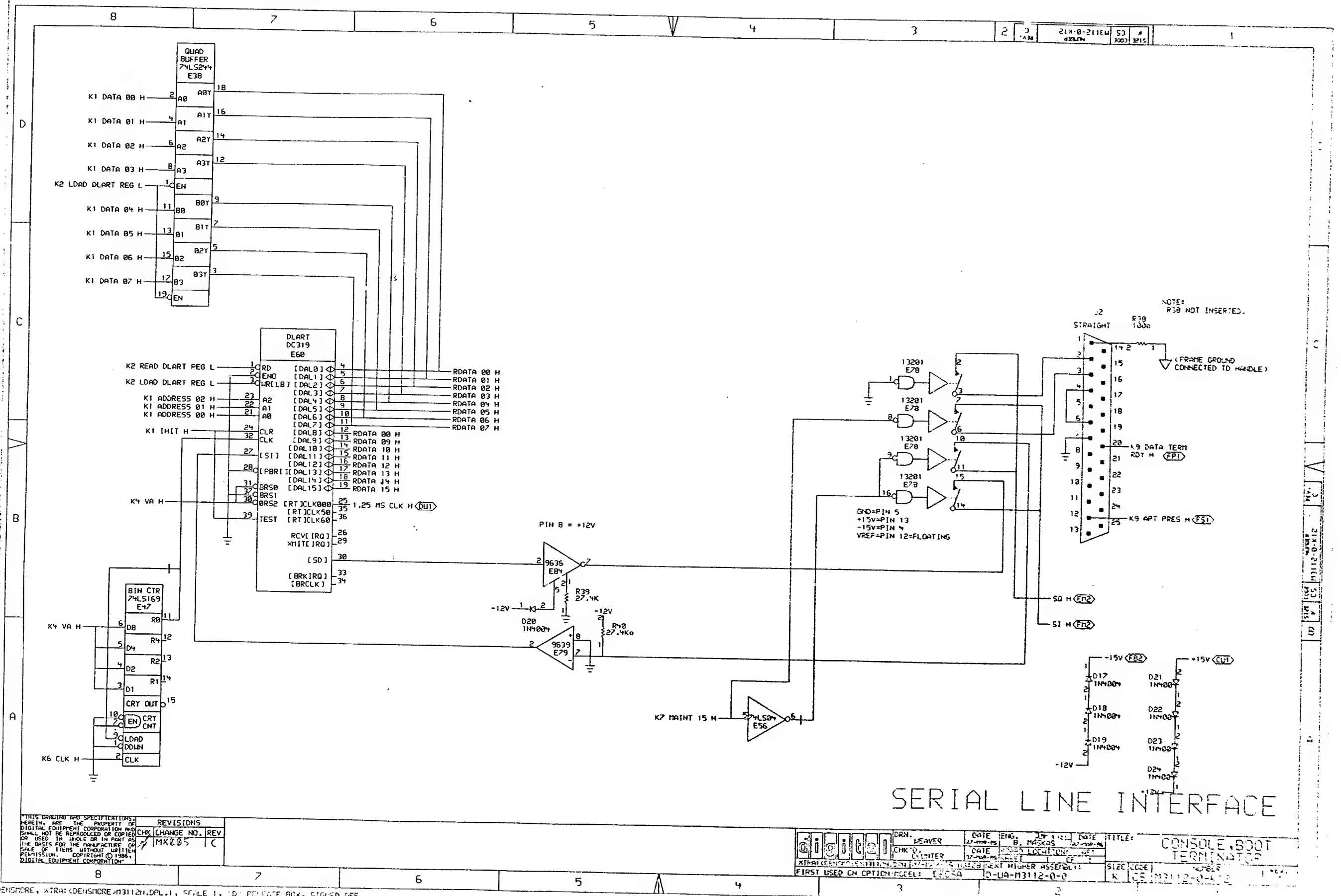
U  
N  
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S



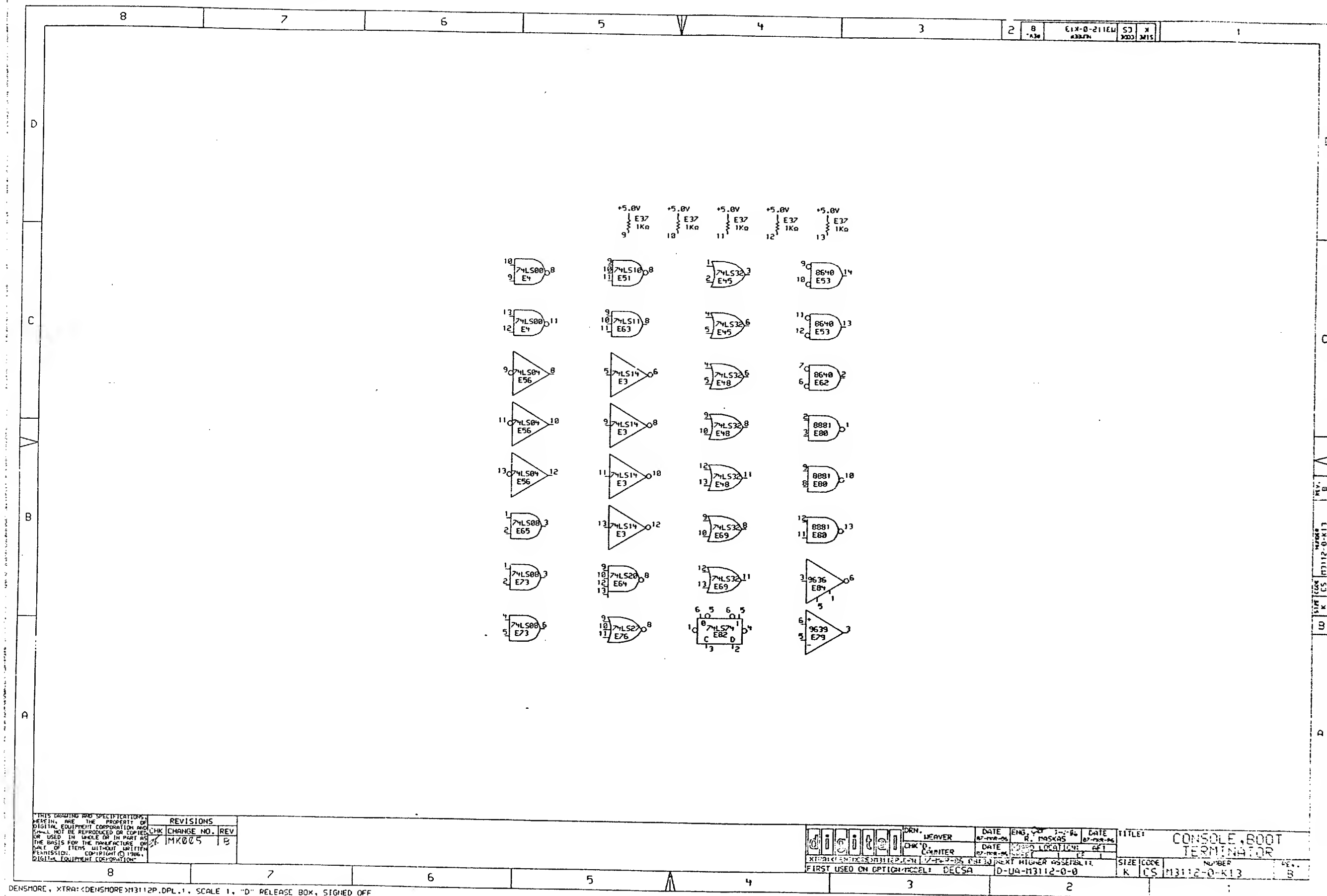
CBT FUNCTIONAL BLOCK DIAGRAM

DRN.	DATE	ENG.	DATE	TITLE
CHK'D.	02-NOV-82	AF1	02-NOV-82	CONSOLE, BOOT TERMINATOR
DSKD: CLEAVER M3112M.DRW	02-NOV-82 14:04	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: DECSA	D-UA-M3112-0-0		K CS	M3112-0-K11
				REV. A











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DRAWING DIRECTORY

ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS											
						A	B	C1	D1	C2	D2	D3					
1			M3100-00	SYNC/ASYNCR SERIAL LINE UNIT		A	B	C1	D1	C2	D2	D3					
2	D-UA-M3100-0-0	2		UNIT ASSEMBLY		A	B	C	D	E	E	F					
3	K-PL-M3100-0-DBP	2		PARTS LIST		A	B	C	D	C1	E	F					
4	K-PC-M3100-0-DBI	-		PC DATA BASE		D	D	D	E	D	E	E					
5	K-CS-M3100-0-DBS	-		SUDS DATA BASE		A	B	C	D	C	D	D					
6	K-CS-M3100-0-SLUI	1		SCHEMATIC		A	B	B	B	B	B	B					
7	K-CS-M3100-0-SLU2	1		SCHEMATIC		A	B	C	D	C	D	D					
8	K-CS-M3100-0-SLU3	1		SCHEMATIC		A	B	B	B	B	B	B					
9	K-CS-M3100-0-SLU4	1		SCHEMATIC		A	B	B	C	B	C	C					
10	K-CS-M3100-0-SLU5	1		SCHEMATIC		A	B	B	C	B	C	C					
11	K-CS-M3100-0-SLU6	1		SCHEMATIC		A	B	B	C	B	C	C					
12	K-CS-M3100-0-SLU7	1		SCHEMATIC		A	B	C	D	C	D	D					
13	K-CS-M3100-0-SLU8	1		SCHEMATIC		A	B	B	C	B	C	C					
14			5014426-00	ETCHED BOARD		C	D	D	E	D	E	E					
15	D-MD-5014426-0-0	6		DRILL AND ETCH DRAWING		A	B	B	C	B	C	C					
16	D-EC-5014426-0-0	3		ETCH CUT DRAWING		A	B	B	C	B	C	C					

NOTES:

DD  
REV

A

B

C

D

E

E

F

REVISION HISTORY

CONT. REVISION HISTORY

MADE BY: A. KRAWIECKI  
DATE: 10 SEP 84

CHECKED BY: B. FRASER  
DATE: 10 SEP 84

DESIGN ENGINEER: J. DOHERTY  
DATE: 10 SEP 84

RESPONSIBLE ENG: J. DOHERTY  
DATE: 10 SEP 84

PRODUCTION ENG: W. BROOKE  
DATE: 10 SEP 84

d i g i t a l

TITLE DRAWING DIRECTORY

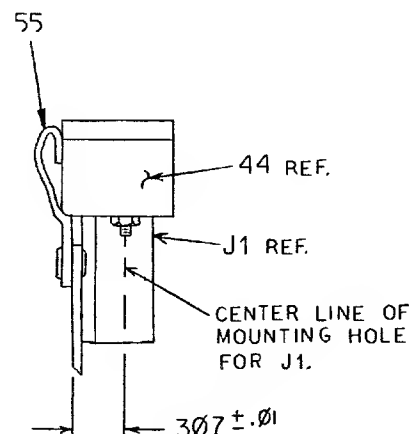
SYNC/ASYNCR SERIAL LINE UNIT

SIZE | CODE | DOCUMENT NUMBER | REV.  
K | DD | M3100-0 | F

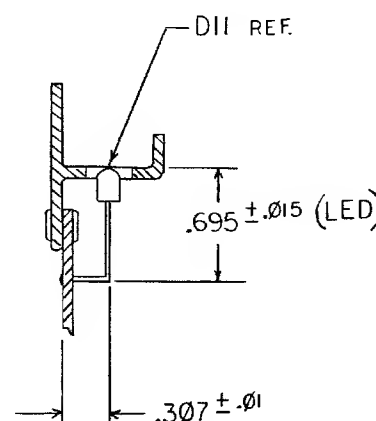
SHEET 1 OF 1 MKO



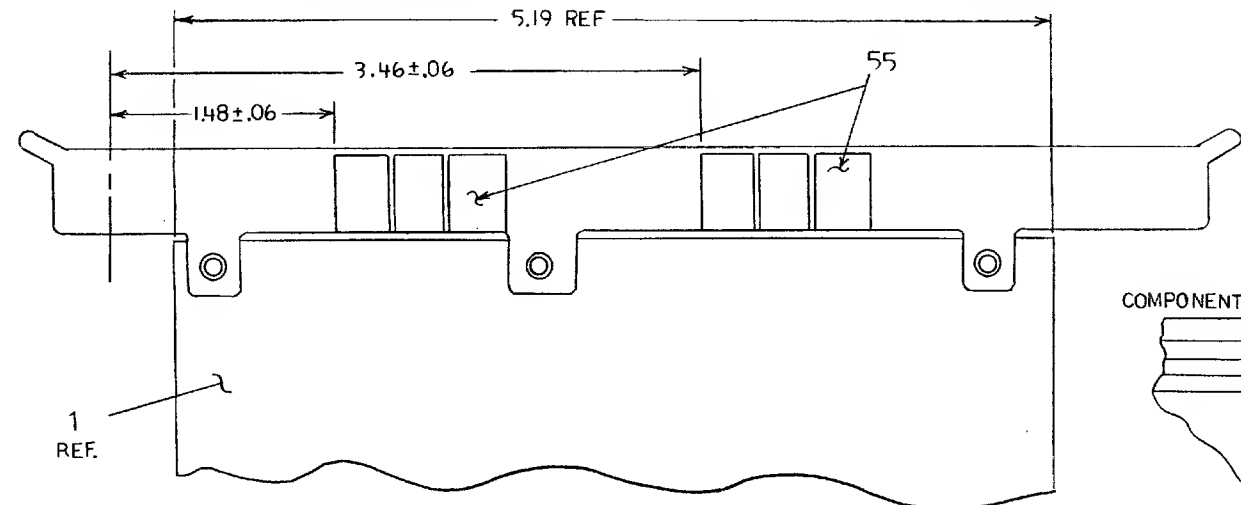
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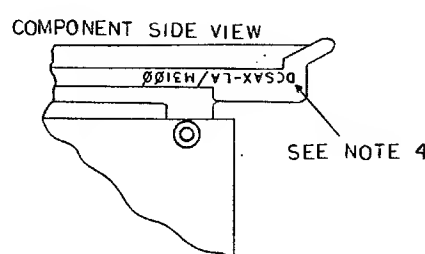
VIEW A-A  
(SCALE 2:1)



SECTION B-B  
(SCALE 2:1)



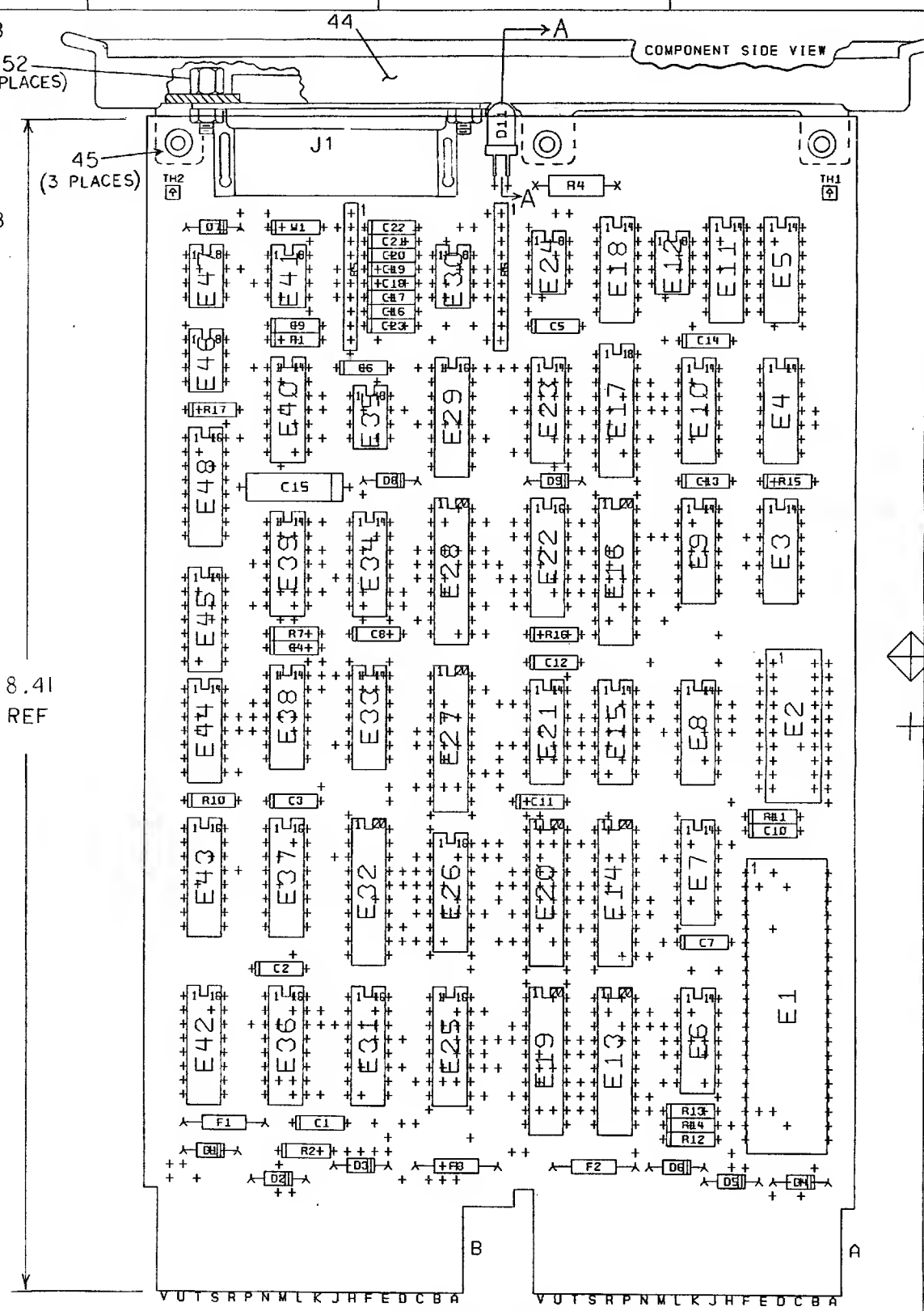
SIDE 2 VIEW  
(NO SCALE)



- NOTES:
1. UL RATING IS LPWR.
  2. R4, C16-C23 ARE NOT INSTALLED (OPT.)
  3. E2 IS A SPARE LOCATION.
  4. STAMP LOCATION / DIRECTION OF MODULE TYPE.

STEP E	Y AXIS 0.00	STEP 0	TIMES
REPEAT	X AXIS 5.19	STEP 1	TIMES

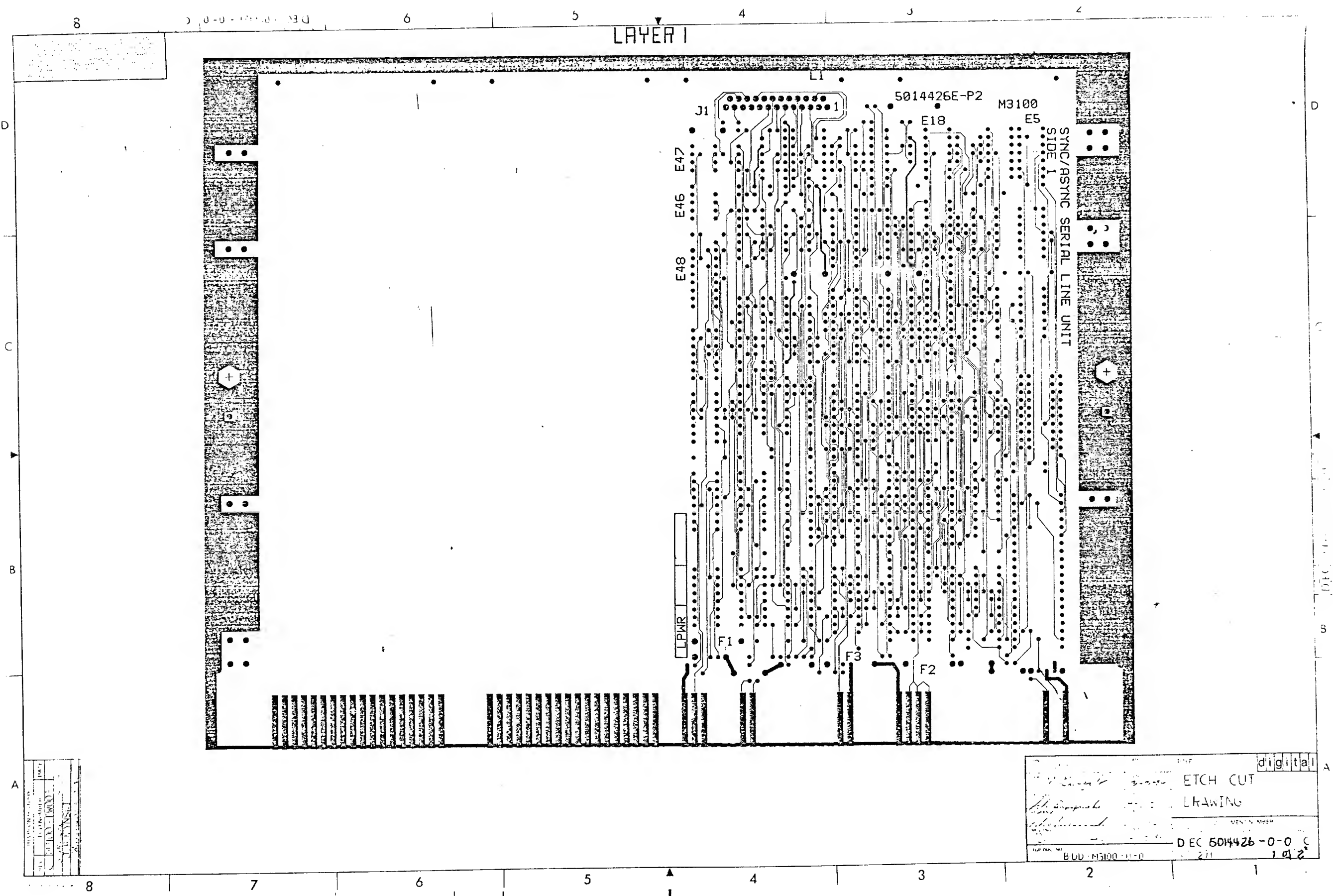
CHG	NO	REV	DATE	BY	CHK	APP
M3100-TW3	D		12/10/84	J. BLIZYNSKI		
M3100-TW3	E		12/10/84	J. BLIZYNSKI		
M3100-TW3	F		12/10/84	J. BLIZYNSKI		
M3100-TW3	G		12/10/84	J. BLIZYNSKI		
M3100-TW3	H		12/10/84	J. BLIZYNSKI		
M3100-TW3	I		12/10/84	J. BLIZYNSKI		
M3100-TW3	J		12/10/84	J. BLIZYNSKI		
M3100-TW3	K		12/10/84	J. BLIZYNSKI		
M3100-TW3	L		12/10/84	J. BLIZYNSKI		
M3100-TW3	M		12/10/84	J. BLIZYNSKI		
M3100-TW3	N		12/10/84	J. BLIZYNSKI		
M3100-TW3	O		12/10/84	J. BLIZYNSKI		
M3100-TW3	P		12/10/84	J. BLIZYNSKI		
M3100-TW3	Q		12/10/84	J. BLIZYNSKI		
M3100-TW3	R		12/10/84	J. BLIZYNSKI		
M3100-TW3	S		12/10/84	J. BLIZYNSKI		
M3100-TW3	T		12/10/84	J. BLIZYNSKI		
M3100-TW3	U		12/10/84	J. BLIZYNSKI		
M3100-TW3	V		12/10/84	J. BLIZYNSKI		
M3100-TW3	W		12/10/84	J. BLIZYNSKI		
M3100-TW3	X		12/10/84	J. BLIZYNSKI		
M3100-TW3	Y		12/10/84	J. BLIZYNSKI		
M3100-TW3	Z		12/10/84	J. BLIZYNSKI		



ETCH REV.	E-P2
-----------	------

SIGNATURES		DATE	digital			
DRN BUD DEMPSKI		3-6-84				
CHK'D. <i>J. Blizynski</i>		3-7-84				
MECH. ENG. <i>J. Blizynski</i>		4-22-84	TITLE SYNC/ASYNC SERIAL LINE UNIT			
PROJ. ENG. <i>J. Blizynski</i>		4-22-84				
PROD. <i>J. Blizynski</i>		4-27-84				
SCALE 2:1		SIZE CODE				
SHT. 1 OF 1		D UA	M3100-0-0	F		
NEXT HIGHER ASSY: K-00-M3100-0						





REV. 1.0  
DATE: 12/1/80  
BY: J. J. YNSEL  
APP: J. J. YNSEL

ETCH CUT  
DRAWING  
DEC 5014426-0-0 C  
1 of 2



REV A34AJ

5014426E-P2

L4

SIDE 2

ETCH CUT DRAWING

DEC 5014426-0-0

2 of 2



AUTOMATED BY PRTLST.40(50)

## P A R T S L I S T

SHEET A1 OF A2

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
				VARIATION REVISION LEVEL:	00 D3	
1	38	D-MD-5014426-0-0	5014426-00	MERC SYNC/ASYN LINE UNIT	1	
2	40		1012784-00	.047 MFD 50V +80-20% CER	13	C1-C9,C11-C14
3	1		1105796-00	PIV= 400 IO= 1.00A IN4004 DO-41	9	D1,D2,D3,D4,D5,D6,D7,D8,D9
4	2		1110324-01	LED 1MCD@10MA PIV=3.750 MIN. LE	1	D11
5	42		1205747-00	FUSE, SUB-MINI, 5.000A, 125V, A	1	F1
6	48		1210929-02	FUSE, SUB-MINI 1.000A, 125V, A	2	F2,F3
7	43	BLANK	1220449-01	*** THIS ITEM IS NOT USED ***	-	
8	35		1300271-00	220.0 .25 W 5.0 % CF	2	R1,R11
9	36		1300295-00	330.0 .25 W 5.0 % CF	2	R2,R7
10	8		1909712-00	DEC 8242 COMPARATOR-4BIT N,	1	E6
11	28		1910534-00	74804 INVERTER GATE-HEX 1I	1	E33
12	31		1910544-00	74874 FF-D DUAL,EDGE TRIGG	2	E38,E8
13	22		1910545-00	748112 FF-JK DUAL,EDGE TRIG	1	E22
14	32		1912388-00	74302 NOR GATE-QUAD 2IN,PO	1	E39
15	29		1912389-00	74808 AND GATE-QUAD 2IN,PO	1	E34
16	16		1912801-00	LS02 NOR-GATE-QUAD 2IN	1	E15
17	5		1912803-00	LS04 INVERTER GATE,HEX	2	E3,E10
18	19		1912805-00	LS08 AND GATE-QUAD 2IN,PO	1	E18
19	11		1912807-00	LS10 NAND GATE-TRIPLE 3IN	1	E9
20	7		1912808-00	LS11 AND GATE-TRIPLE 3IN	1	E8
21	34		1912813-00	LS27 NOR GATE-TRIPLE 3IN	1	E45
22	6		1912820-00	LS51 A-O-I GATE 2-WIDE 2I	1	E4
23	21		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	4	E21,E23,E44,E/
24	27		1912842-00	LS138 DECODER-THREE INPUT,	1	E29
25	30		1912853-00	LS175 FF-D QUAD	1	E37
26	15		1912863-00	LS273 FF-D OCTAL W/CLEAR	1	E14
27	14		1913493-00	748241 OCTAL BUFFER,TRI-STA	1	E13
28	26		1913671-00	748374 FF-D,OCTAL,TRI STATE	1	E27
29	24		1913788-00	26510 TRANSCEIVER,BUS,QUA	4	E25,E31,E42,E43
30	33		1914085-00	748260 NOR GATE-DUAL,POS	1	E40

REVISION HISTORY		BASIC PART NO: M3100		DRN: F.SMART	DATE: 22-JAN-82	D I G I T A L	
ENG: ECO NUMBER	REV	SECTION A OF A	CHK'D: F. GARCIA	DATE: 11-APR-85	TITLE PARTS LIST		
--- INITIAL	B	SECTION VARIATION INDEX		DATE: 22-JAN-82	SYNC/ASYN SERIAL		
JB M3100-TW002	IC	[A] 00			LINE UNIT		
JB M3100-TW003	ID	[B]		DES.ENG: JOHN BUZYNSKI	DATE: 19-AUG-83	DOCUMENT NUMBER	
JD M3100-MK004	IE	[C]				SIZE:CODE: NUMBER	REV
ML M3100-MK005	IF	[D]		RESP.ENG.: J.C. Tremblay	DATE: 11-APR-85	-----	
		[E]		M.LAWRENCE	DATE: 02-APR-85	K PL M3100-0-0	F
		[F]					
		[G]					
		[H]					
		[I]					
		[J]					
		[K]					
		[L]					
		[M]					
		[N]					
MFG.ENG.: W.BROOKE				DATE: 02-APR-85	RELEASE DATE: 03-APR-85		
ASSEMBLY NUMBER:				TOP DOCUMENT NUMBER:	FILE NAME:		
ID-UA-M3100-0-0				#K-DD-M3100-0	MK1140.PLS		
					EDIT: 16		

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AUTOMATED BY PRTLST.4Q(50)

# PARTS LIST

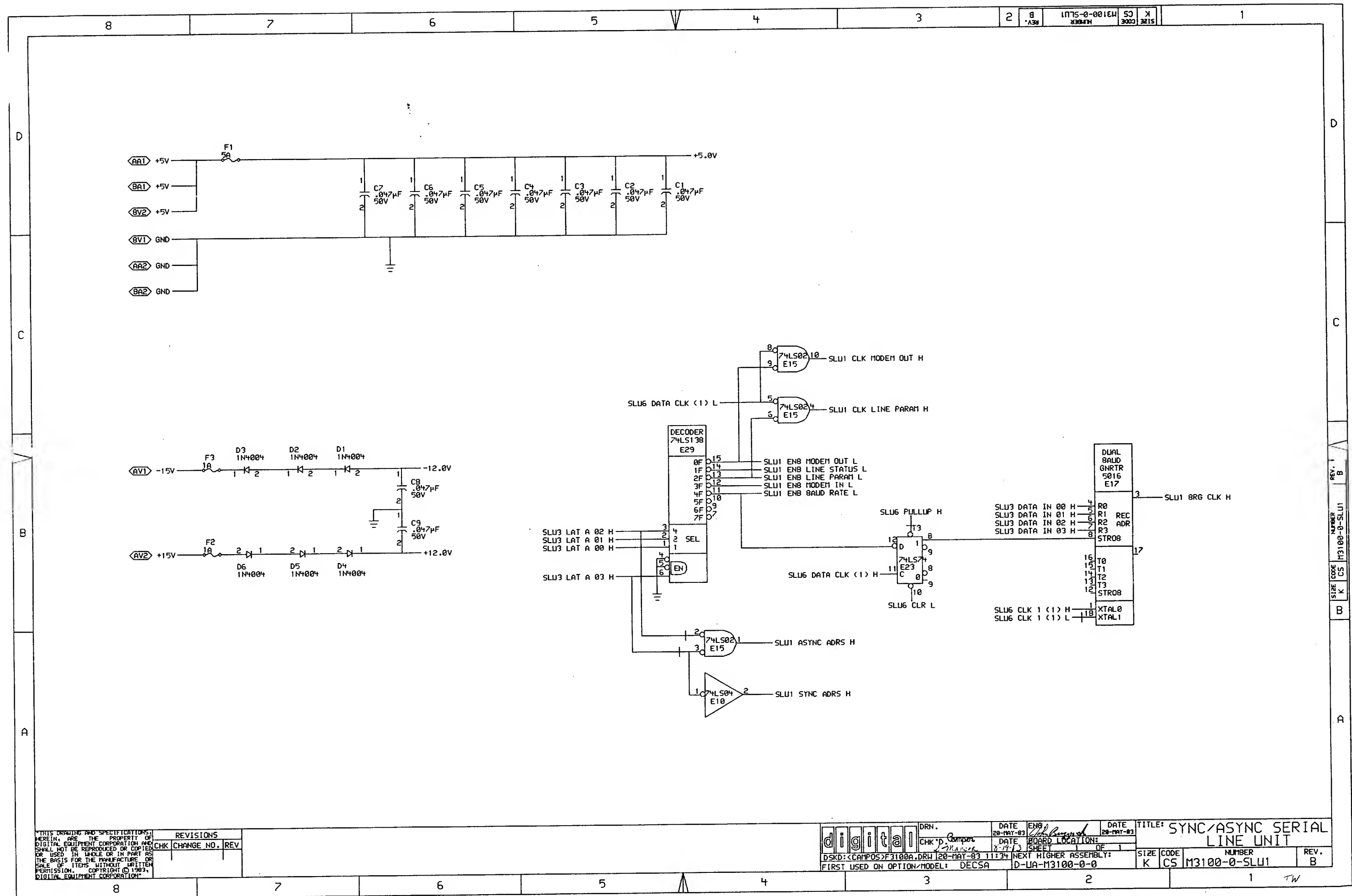
SHEET A2 OF A2

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					VARIATION REVISION LEVEL:	00 D3	
31	12		1914451-00		LS393 COUNTER,BINARY,4BIT	1	E11
32	25		1914866-00	AM	93848PCGEN/CHECK PARITY, 4B	2	E26,E36
33	20		1915193-00		LS244 DRIVER,LINE,OCTAL,TR	2	E20,E32
34	17		1915219-00		LS373 FF-D OCTAL-TRANSPARE	2	E16,E28
35	23		1915415-00		9636 DRIVER,DUAL,EIA RS-	3	E24,E30,E41
36	13		1919542-01		9639 RECEIVER,LINE,DUAL	4	E12,E35,E46,E47
37	3		2112517-01		RECIEVER/TRANSMITTER	1	E1
38	18		2112623-00		DUAL BAUD RATE GEN/PROG DIVIDER,	1	E17
39	39		9009185-00		JUMPER, WIRE, INSULATED, BLACK B	1	W1
40	44	D-MD-7430547-0-DBU	7430547-02		HANDLE,LC	1	
41	45		9009000-00		EYELET,ROLLED 0.1210DX0.156	3	
42	51		1319728-01		R NETWORK 8-27K 1.0 & 10PIN	2	R5,R6
43	52		9008451-01		SCREW LOCK,STANDOFF ONLY .060TH	2	
44	54		1300365-00		1.0 K .25 W 5.0 & CF	6	R10,R12-R16
45	55		1221494-01		SHIELDING,RFI	2	
46	56		1912837-00		LS123 ONE SHOT-DUAL,RETRIG	1	E48
47	57		1005334-00		3.3MFD 20V 10% 8.TANT	1	C15
48	58		1300539-00		120.0 K .25 W 5.0 & CF	1	R17
49	59		1013466-12		2200.0 MMF 50V 10% X7R CER	1	C10
50	60		1913670-00		74S373 LATCH,8BIT TRANS TRI	1	E19
51	61		1220398-07		CONN,D SUB 25PIN ASSY RT ANGLE	1	J1

52 NOTE: 1.R4,C16-C23 ARE NOT INSTALLED.(OPTIONAL)  
 53 NOTE: 2.SPARE I.C. LOCATION E2.

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							SYNC/ASYN SERIAL LINE UNIT		K	PL	M3100-0-0	F

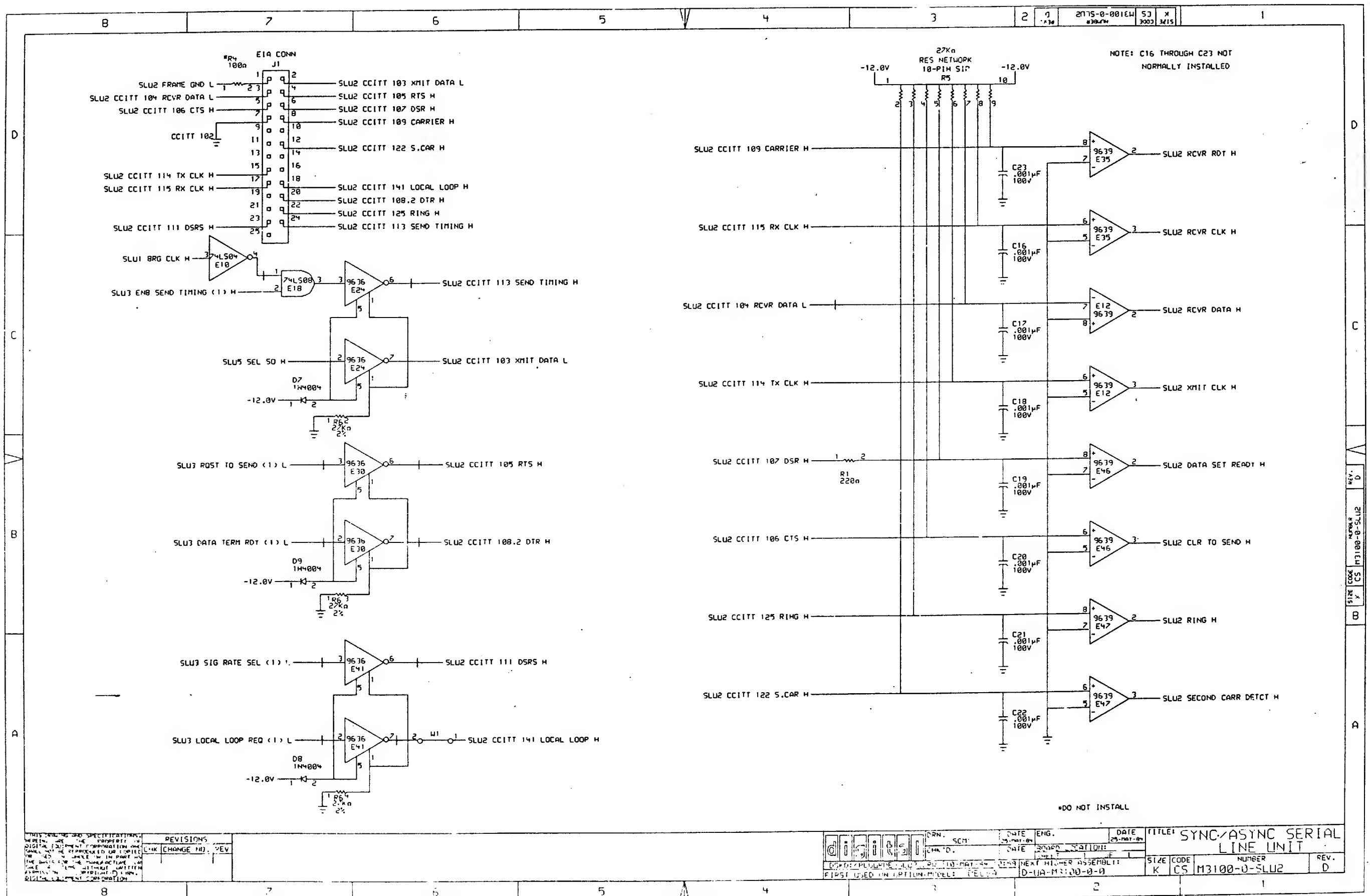




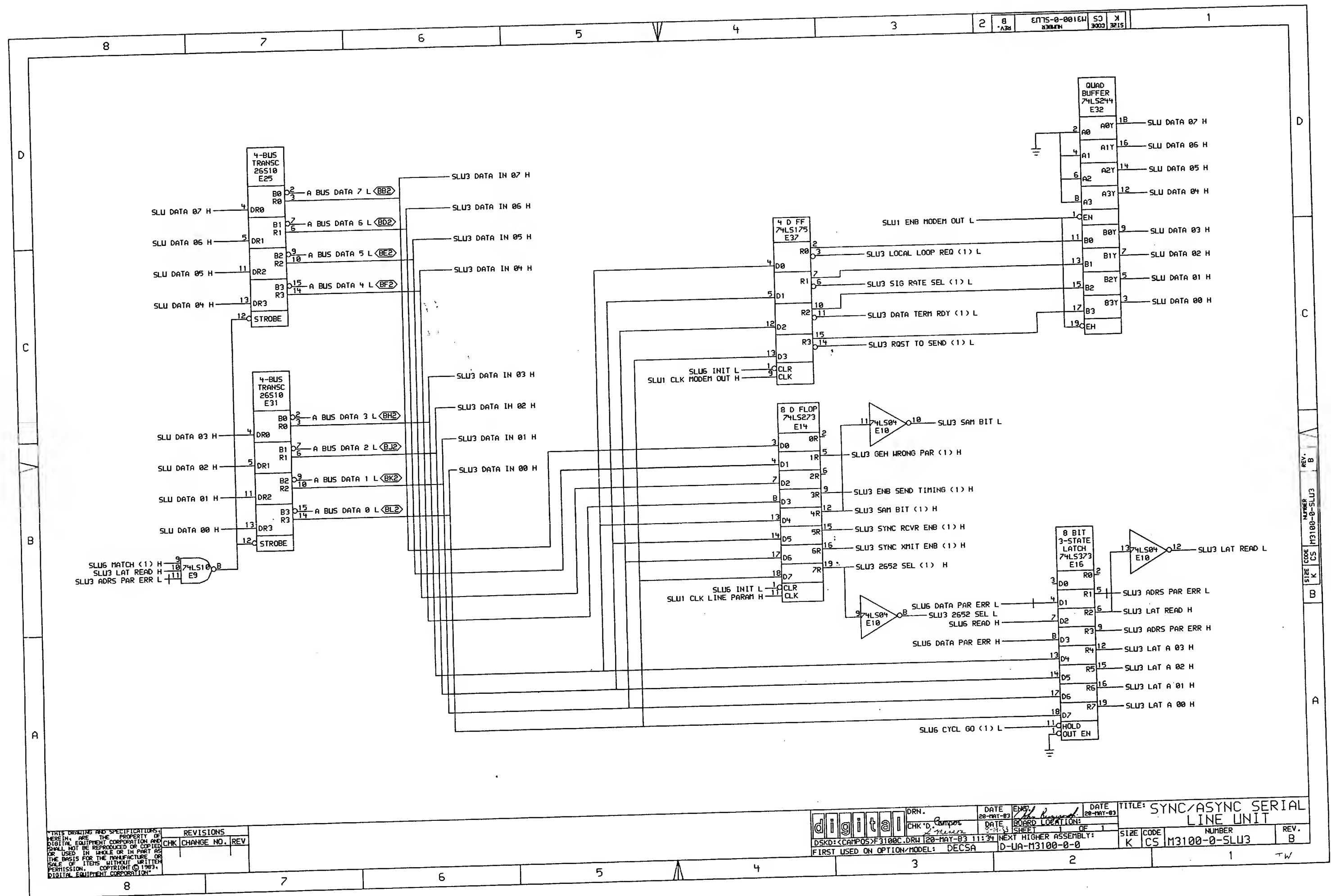
REVISIONS		
CHK	CHANGE NO.	REV

DIGITAL		DRN.	DATE	ENG	DATE	TITLE
CHK'D		28-MAY-83	28-MAY-83	28-MAY-83	28-MAY-83	SYNC/ASYNC SERIAL LINE UNIT
DSKD: <CAMPOS> F3100A.DRW		DATE	BOARD LOCATION	DATE	DE	
FIRST USED ON OPTION/MODEL: DECSA		8-19-83	1	8-19-83	1	
NEXT HIGHER ASSEMBLY: D-UA-M3100-0-0		SIZE	CODE	NUMBER	REV.	
		K	CS	M3100-0-SLU1	B	









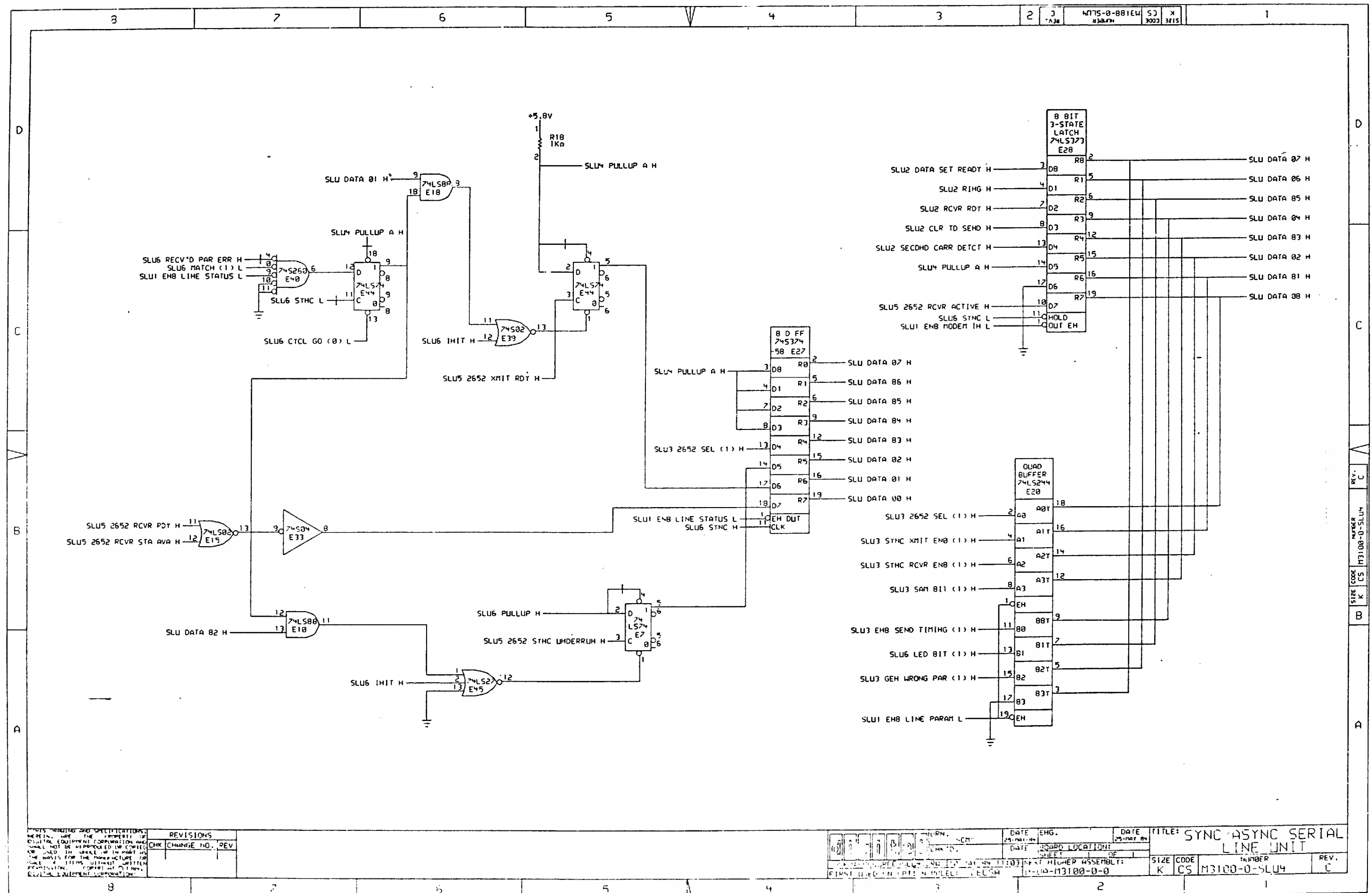
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REVISIONS  
CHK CHANGE NO. REV

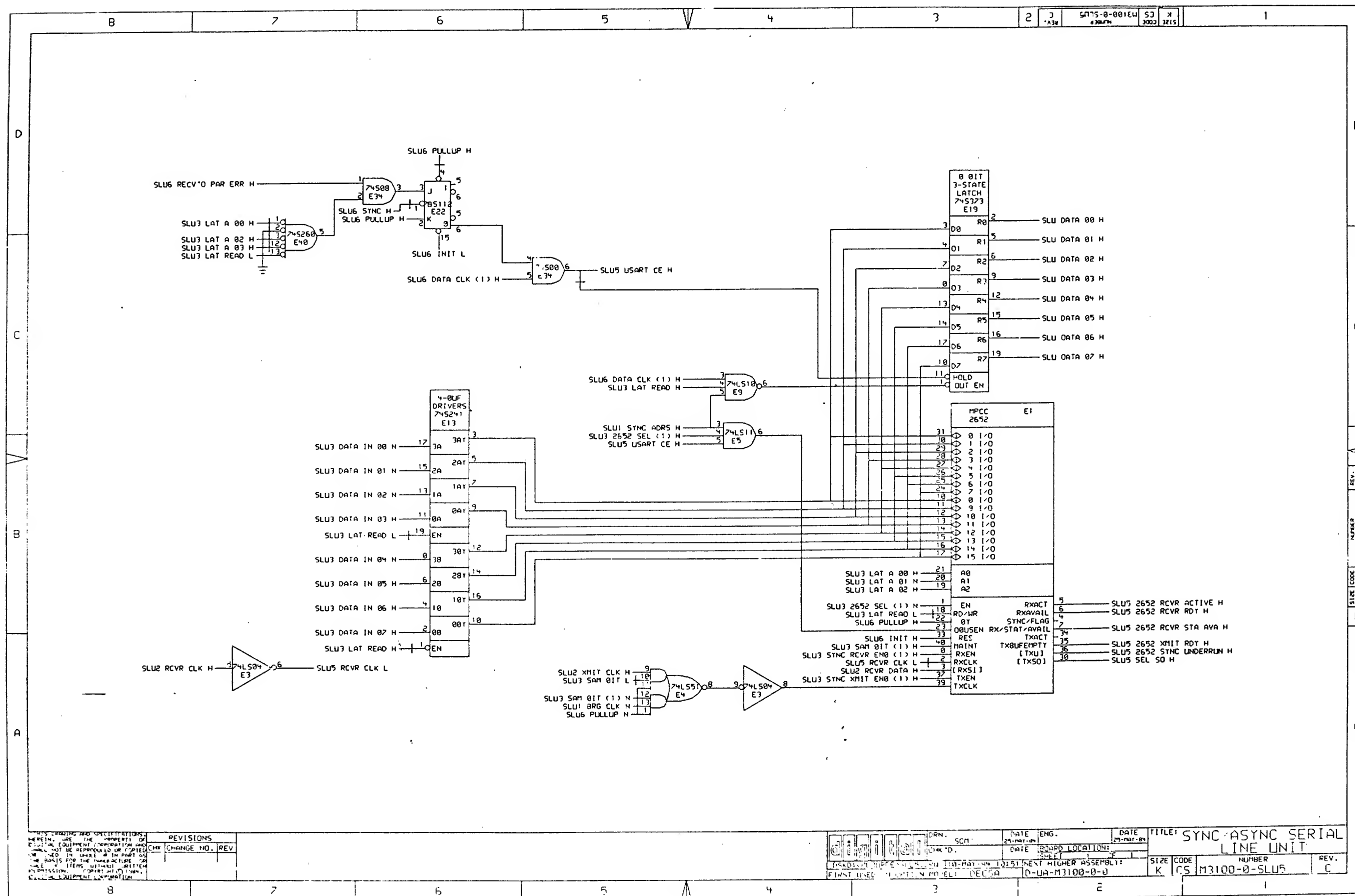
digital DRN. DATE 20-MAY-83 ENG. DATE 20-MAY-83 TITLE: SYNC/ASYNC SERIAL LINE UNIT  
CHK'D. Campos DATE 20-MAY-83 BOARD LOCATION: 1 OF 1  
DSKD: CAMPOS\F3100C.DRW 120-MAY-83 11:34 NEXT HIGHER ASSEMBLY: D-UA-M3100-0-0  
FIRST USED ON OPTION/MODEL: DECSA

SIZE CODE NUMBER REV.  
K CS M3100-0-SLU3 B

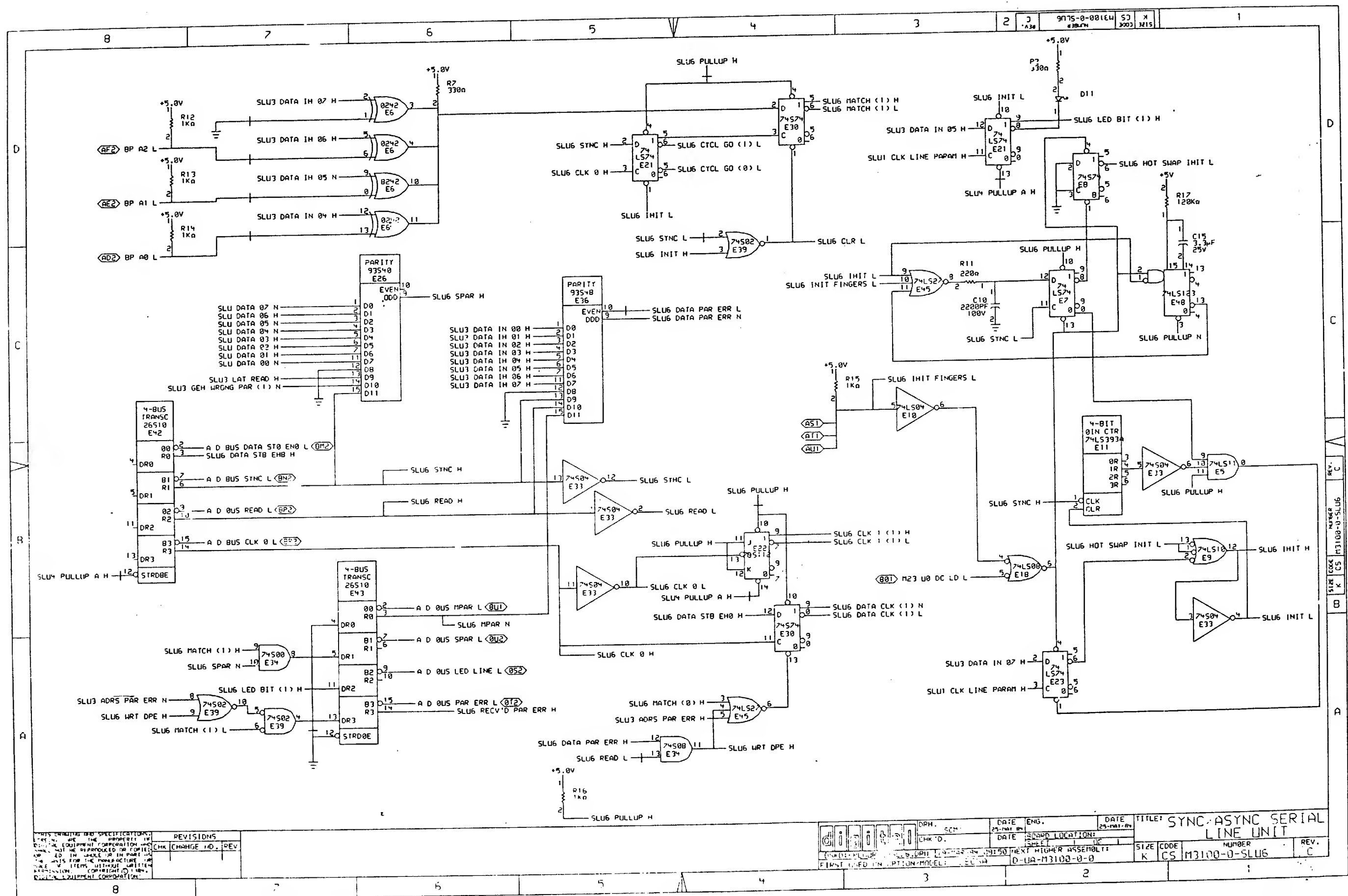




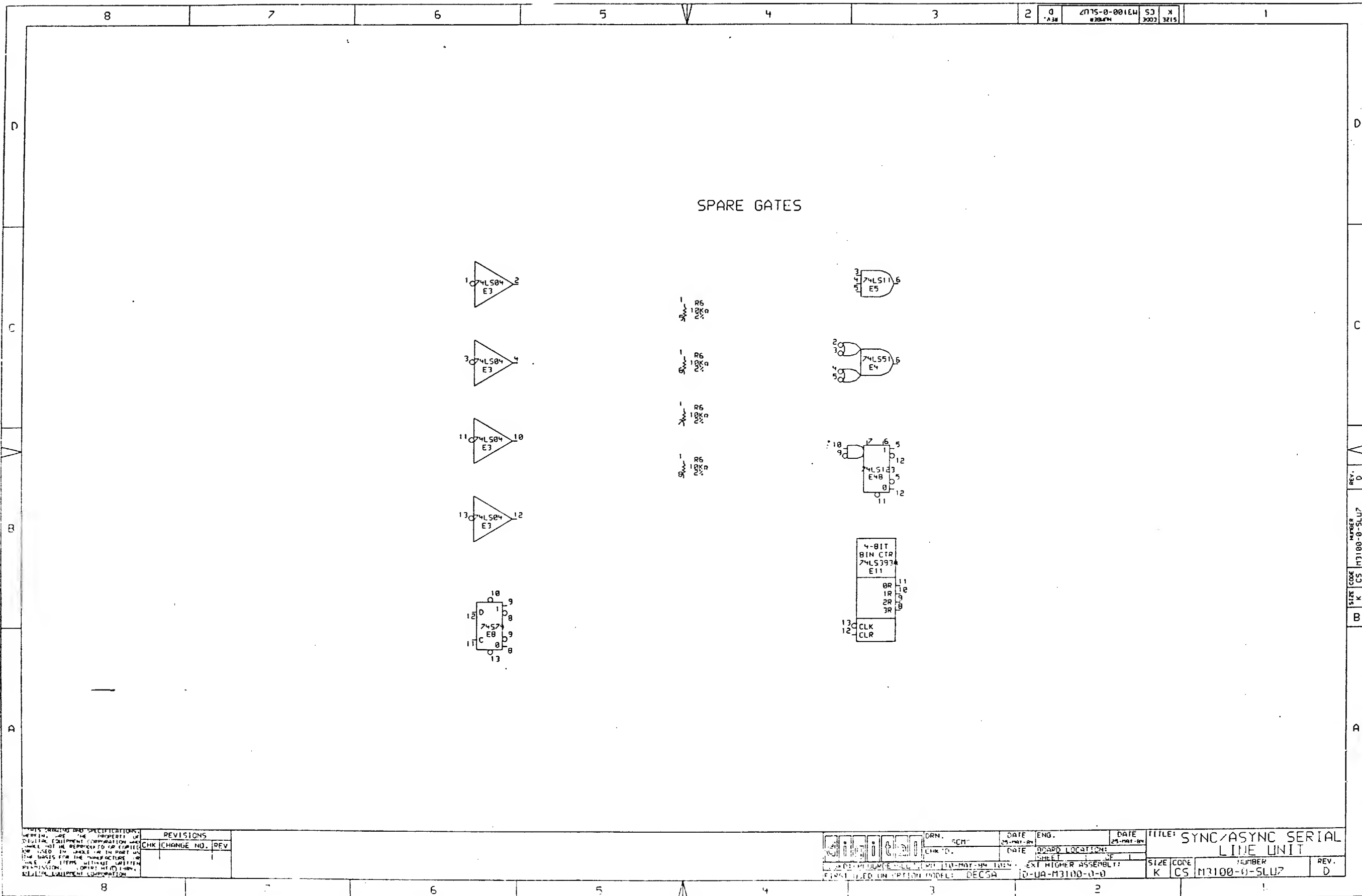




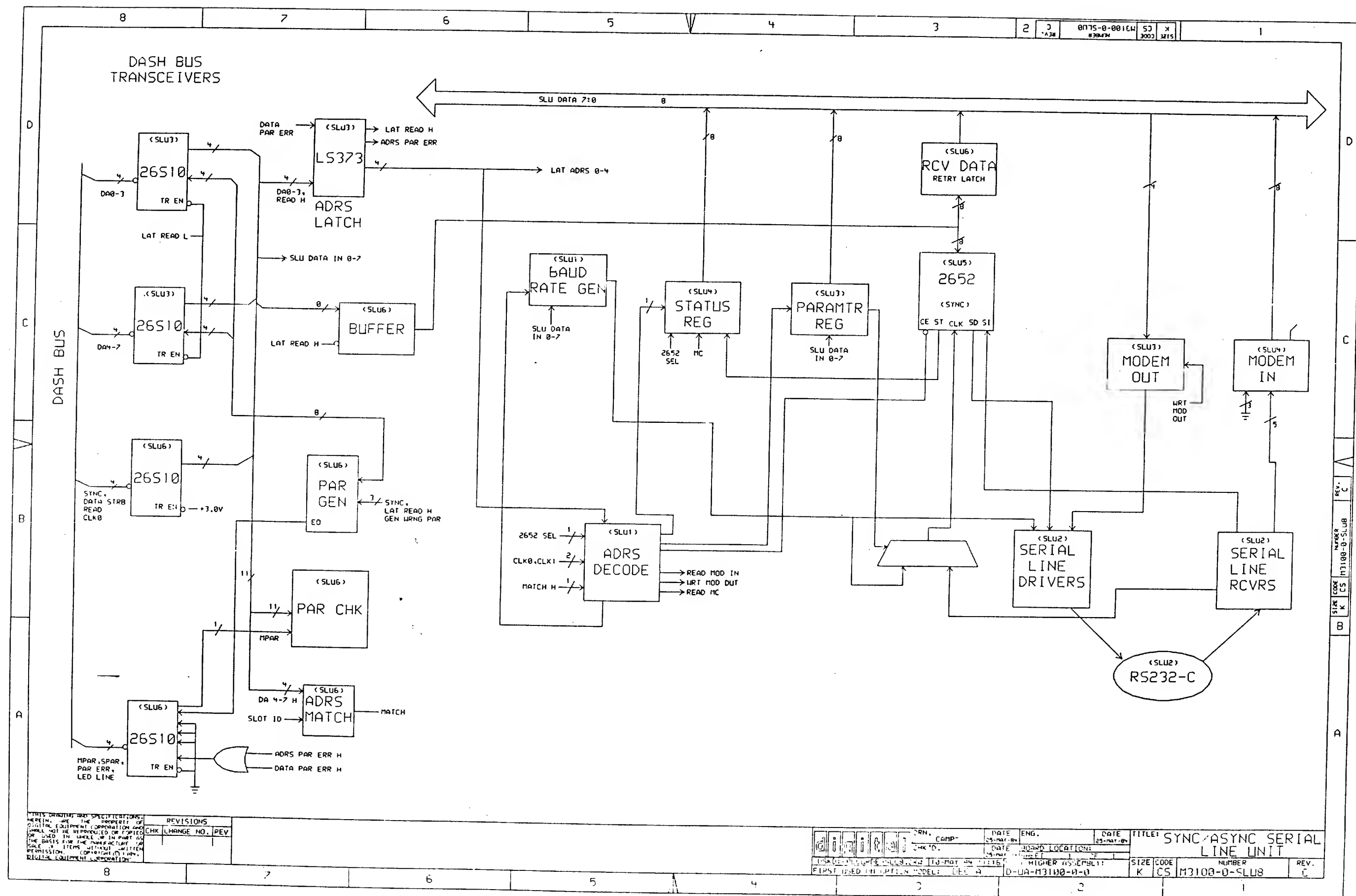














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DRAWING DIRECTORY

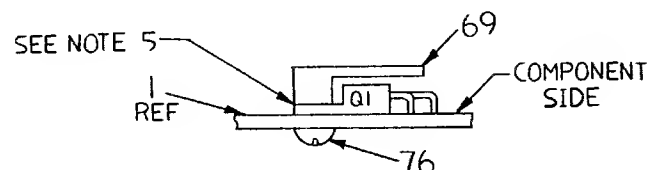
ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS											
						A1	A2										
1		-	M3101-00	V.35 LINE CARD		A1	A2										
2	D-UA-M3101-0-0	1		UNIT ASSEMBLY		A	B										
3	K-PL-M3101-0-DBP	2		PARTS LIST		A	B										
4	K-CS-M3101-0-DBS	-		SUDS DATA BASE		A	A										
5	K-CS-M3101-0-SL1	1		SCHEMATIC		A	A										
6	K-CS-M3101-0-SL2	1		SCHEMATIC		A	A										
7	K-CS-M3101-0-SL3	1		SCHEMATIC		A	A										
8	K-CS-M3101-0-SL4	1		SCHEMATIC		A	A										
9	K-CS-M3101-0-SL5	1		SCHEMATIC		A	A										
10	K-CS-M3101-0-SL6	1		SCHEMATIC		A	A										
11	K-CS-M3101-0-SL7	1		SCHEMATIC		A	A										
12	K-CS-M3101-0-SL8	1		SCHEMATIC		A	A										
13	K-CS-M3101-0-SL9	1		SCHEMATIC		A	A										
14	K-CS-M3101-0-SL10	1		SCHEMATIC		A	A										
15	K-CS-M3101-0-SL11	1		SCHEMATIC		A	A										
16	K-CS-M3101-0-SL12	1		SCHEMATIC		A	A										
17	K-CS-M3101-0-SL13	1		SCHEMATIC		A	A										
18	K-PC-MM3101-0-DBI	-		PC DATA BASE		C	C										
19	K-DD-5014991-0-0	1		DRAWING DIRECTORY		A	A										
NOTES:					DD REV	A	B										
REVISION HISTORY			CONT. REVISION HISTORY			MADE BY: A. KRAWIECKI DATE: 10 SEP 84			d i g i t a l								
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	CHECKED BY: J. FALKOWSKI DATE: 10 SEP 84			TITLE DRAWING DIRECTORY								
--	INITIAL	A				DESIGN ENGINEER: J. DOHERTY DATE: 10 SEP 84			V.35 LINE CARD								
JD	M3101-MK001	B				RESPONSIBLE ENG: J. DOHERTY DATE: 10 SEP 84											
						PRODUCTION ENG: W. BROOKE DATE: 10 SEP 84			SIZE K	CODE DD	DOCUMENT NUMBER M3101-0-0	REV. B					
												SHEET 1 OF 1 MKO					



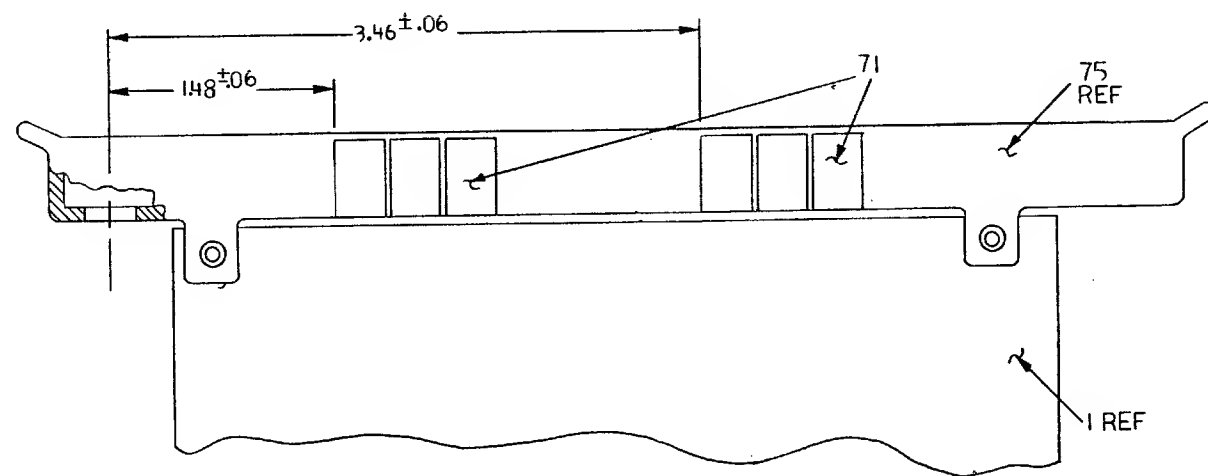
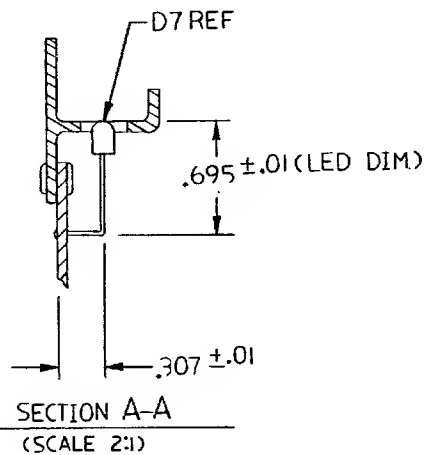
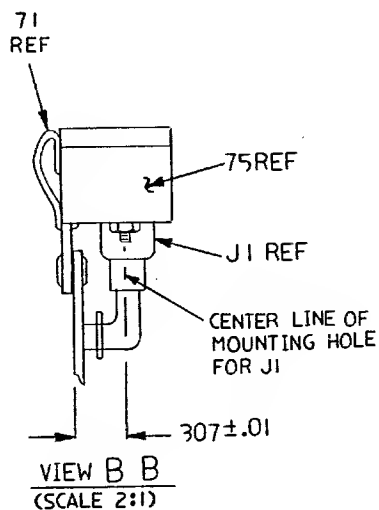
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8 7 6 5 4 3 2 1

0-0-10101-0-0



VIEW C-C  
(NO SCALE)



SIDE 2 VIEW  
(NO SCALE)

73  
(2 PLACES)

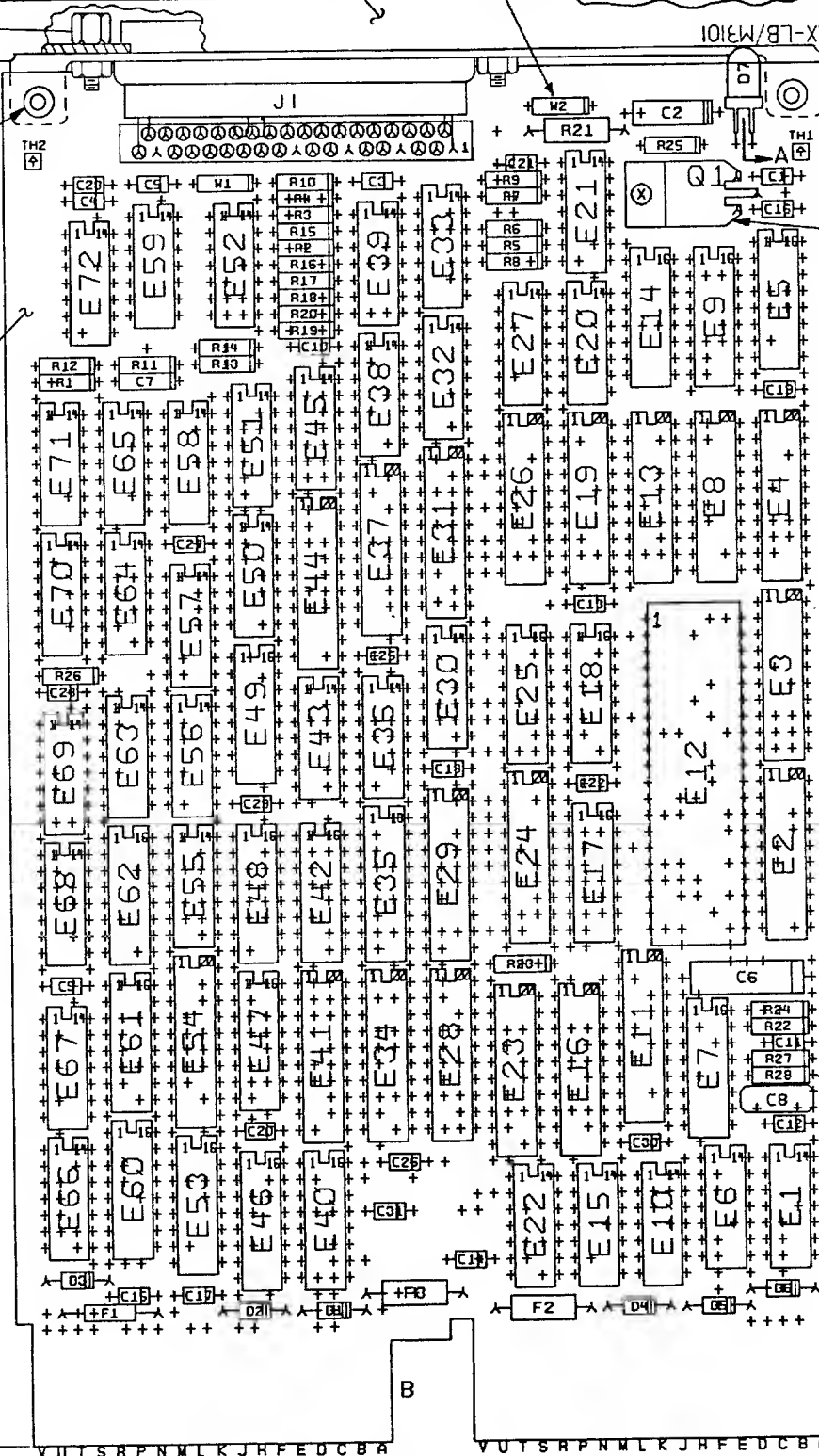
NOTE 6

COMPONENT SIDE VIEW

NOTE 2

NOTE 5

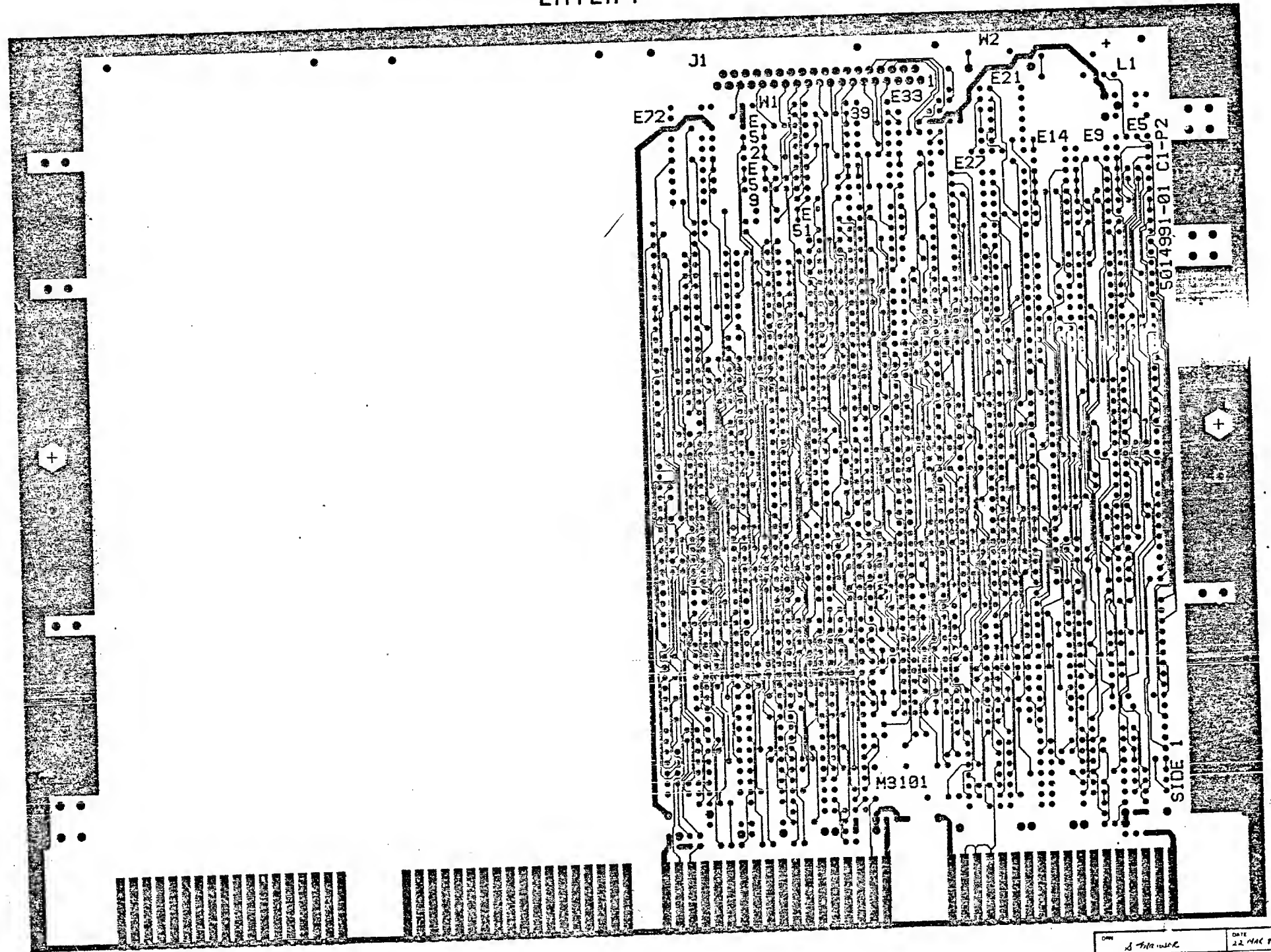
8.41 REF.





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LAYER 1



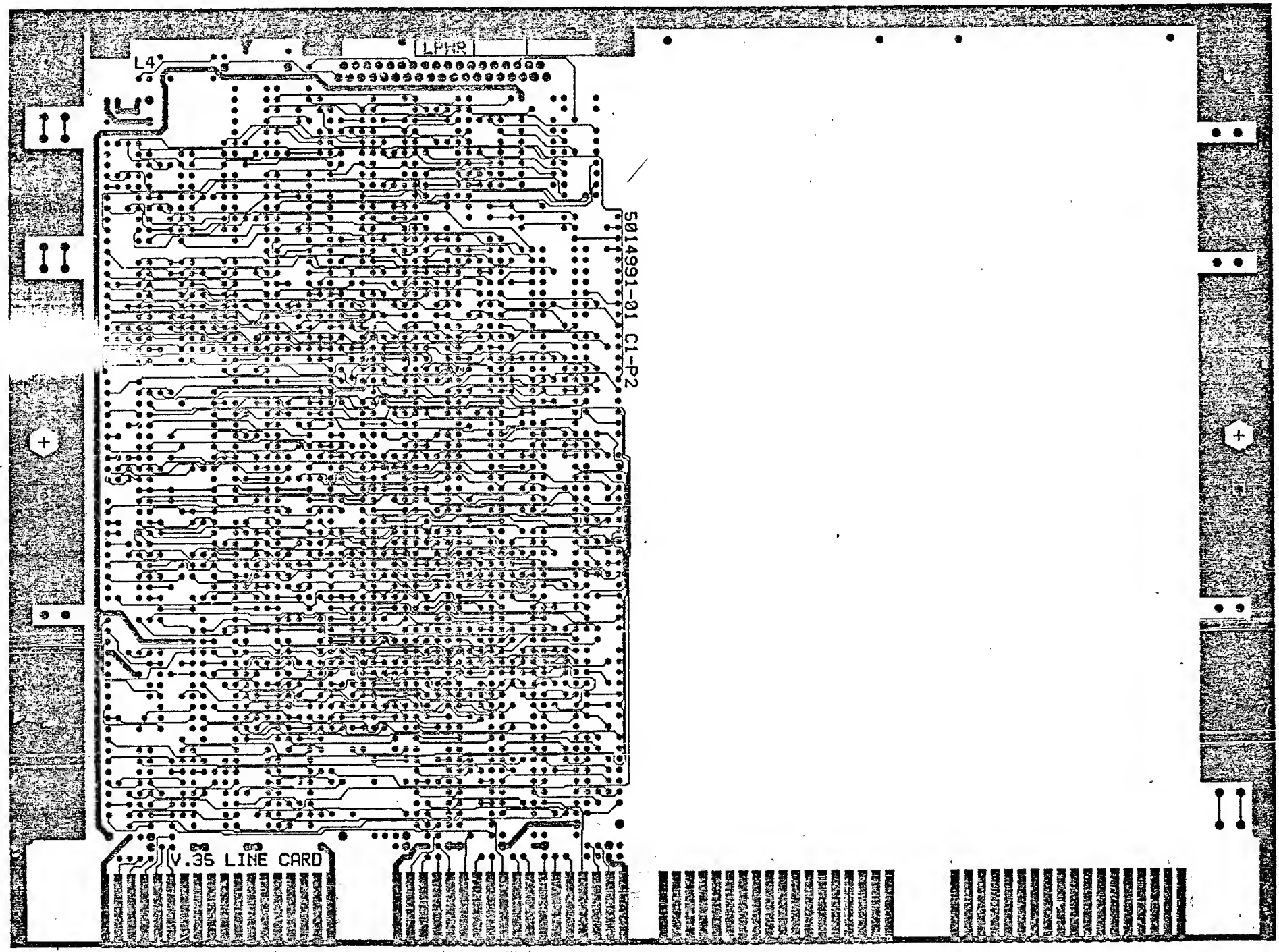
REVISION HISTORY	
REV	DATE
1	22 MAR 84
2	26 MAR 84
3	26 MAR 84
4	26 MAR 84
5	26 MAR 84
6	26 MAR 84
7	26 MAR 84
8	26 MAR 84

DATE	22 MAR 84	TITLE	ETCH CUT DRAWING
CHKD	J. J. J. J.		
DESIGN	J. J. J. J.	DOCUMENT NUMBER	B-DD-5014991-0-0
DATE	26 MAR 84		
DATE	26 MAR 84	SCALE	2/1
DATE	26 MAR 84		
DATE	26 MAR 84	SHEET	1 OF 2
DATE	26 MAR 84		



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LAYER 1



REVISION HISTORY		
DATE	ECO NUMBER	REV

TITLE  
ETCH CUT DRAWING

DOCUMENT NUMBER		
SIZE CODE	NUMBER	REV
DEC	5014991-0-0	A
SCALE 2/1	SHEET 2	OF 2

DRD 137A

TW



AUTOMATED BY PRTLST,4Q(50)

## P A R T S   L I S T

Sheet A1 OF A3

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					VARIATION REVISION LEVEL:	00	

1	1	D-MD-5014991-0-0	5014991-01		V.35 LINE CARD	1	
2	2		1105796-00		PIV= 400 IO= 1.00A 1M4004 DO-41	6	D1-D6
3	3		1220398-05		CONN,D SUB 37PIN ASSY RT ANGLE	1	J1
4	4		1300005-01		R NETWORK 13-1K 5.0 % 14PIN	1	E5
5	5		1300228-00		100.0 .50 W 5.0 % CF	1	R21
6	6		1300247-00		120.0 .25 W 5.0 % CF	3	R2,R5,R8
7	7		1300271-00		220.0 .25 W 5.0 % CF	1	R1
8	8		1300295-00		330.0 .25 W 5.0 % CF	2	R25,R26
9	9		1300365-00		1.0 K .25 W 5.0 % CF	2	R23,R24
10	10		1300539-00		120.0 K .25 W 5.0 % CF	1	R22
11	11		1301890-00		560.0 .25 W 5.0 % CF	2	R19,R20
12	12		1312928-00		51.0 .25 W 5.0 % CF	6	R3,R4,R6,R7,R9,R10
13	13		1312930-00		5.10 K .25 W 5.0 % CF	1	R27
14	14		1909712-00		DEC 8242 COMPARTICR-48BIT N,	1	E1
15	15		1910268-00		DEC 75107B RECEIVER,LINE,DUAL,	2	E33,E39
16	16		1910327-00		DEC 1488L DRIVER,LINE,QUAD,EI	1	E59
17	17		1913130-00		DEC 1489A RECEIVER,LINE,QUAD,	1	E72
18	18		1910532-00		74S00 NAND GATE-QUAD 2IN	1	E38
19	19		1910534-00		74S04 INVERTER GATE-HEX 1I	1	E57
20	20		1910544-00		74S74 FF-D DUAL,EDGE TRIGG	2	E50,E58
21	21		1910545-00		74S112 FF-JK DUAL,EDGE TRIG	1	E62
22	22		1910548-00		74S157 MUX 1 CF 2 (QUAD)	1	E18
23	23		1911675-00		74S138 DECODER/DEMUX 3-8 LI	1	E48
24	24		1912388-00		74S02 NOR GATE-QUAD 2IN,PO	1	E67
25	25		1912389-00		74S08 AND GATE-QUAD 2IN,PO	1	E64
26	26		1912742-00		75112 DRIVER,DUAL LINE	1	E52
27	27		1912801-00		LS02 NOR-GATE-QUAD 2IN	2	E32,E55
28	28		1912803-00		LS04 INVERTER GATE,HEX	3	E15,E27,E30
29	29		1912805-00		LS08 AND GATE-QUAD 2IN,PO	1	E22
30	30		1912807-00		LS10 NAND GATE-TRIPLE 3IN	1	E70

REVISION HISTORY			BASIC PART NO: M3101		DRN:	S.MANSOR	DATE: 24-CCT-83	D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A					TITLE PARTS LIST			
---	INITIAL	A	SECTION VARIATION INDEX		CHK'D:	E.T.GARPY	DATE: 24-CCT-83	V.35 LINE CARD			
JD	M3101-MK001	B	[A] 00								
			[B]		DES.ENG:	D.REMBUSKI	DATE: 25-OCT-83	DOCUMENT NUMBER			
			[C]					SIZE	CODE	NUMBER	REV
			[D]								
			[E]		RESP.ENG.:	J.BUZYNSKI	DATE: 25-OCT-83	K	PL	M3101-0-DBP	B
			[F]								
			[H]								
			[J]								
			[K]		MFG.ENG.:	BIGL SWAN	DATE: 16-DEC-83	RELEASE DATE: 15-OCT-84			
			[L]								
			[M]		ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:			FILE NAME:		EDIT #
			[N]		D-UA-M3101-0-0	B-DD-M3101-0-0			MK1141,PLS		15

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## P A R T S L I S T

SHEET A2 OF A3

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY 00	PER VARIATION	REFERENCE DESIGNATOR
VARIATION REVISION LEVEL:								
31	31		1912813-00		LS27 NOR GATE-TRIPLE 3IN	1		E66
32	32		1912820-00		LS51 A-Q-I GATE 2-WIDE 2I	1		E36
33	33		1912824-00		LS74 FF-D DUAL,EDGE TRIGG	7		E10,E43,E45,E51,E56,E58,E63
34	34		1912830-00		LS90 COUNTER,ASYNCH UP,DE	1		E71
35	35		1912837-00		LS123 ONE SHOT-DUAL,RETRIG	1		E7
36	36		1912847-00		LS157 MUX 1 OF 2(QUAD)	1		E25
37	37		1912849-00		LS161 COUNTER,SYNCHR,4BIT	3		E5,E9,E14
38	38		1912853-00		LS175 FF-D QUAD	1		E42
39	39		1912863-00		LS273 FF-D OCTAL W/CLEAR	1		E29
40	40		1913340-00		74S32 OP GATE-QUAD 2IN	1		E21
41	41		1913493-00		74S241 OCTAL BUFFER,TRI-STA	1		E24
42	42		1913670-00		74S373 LATCH,REIT TRANS TRI	2		E41,E54
43	43		1913671-50		74S374 FF-D,OCTAL,TRI STATE	1		E16
44	44		1913788-00		26S19 TPA-SCRIVER,BUS,QUA	4		E40,E46,E53,E60
45	45		1914085-00		74S260 NOR GATE-DUAL,POS	1		E69
46	46		1914866-00	AM	93S48PCGEN/CHECK PARITY, 4B	3		E17,E47,E61
47	47		1915193-00		LS244 DRIVER,LINE,OCTAL,T	4		E3,E11,E28,E34
48	48		1915219-00		LS245 TRANSCRIVER,BUS,OCT	1		E2
49	49		1915219-00		LS373 FF-D OCTAL-TRANSPARE	1		E23
50	50		1915932-00		LS125 BUFFER,QUAD BUS	1		E20
51	51		2112517-01		RECEIVER/TRANSMITTER	1		E12
52	52		2112623-00		DUAL BAUD RATE GEN/PRGG DIVIDER,	1		E35
53	53		2119250-00		4KX4 STATIC RAM 55NS	4		E4,E8,E13,E19
54	54		9009185-00		JUMPER, WIRE, INSULATED, BLACK B	1		W1
55	55		1110324-01		LED 1MCD@10MA PIV=3.750 MIN. LE	1		D7
56	56		1012784-00		.047 MFD 50V +80-20% CER	26		C1,C3-C5,C9-C23,C25-C31
57	57		1001776-00		1 MFD 35V 10% S.TANT	1		C2
58	58		1005334-00		3.3MFD 20V 10% S.TANT	1		C6
59	59		1013466-12		2200.0 MME 50V 10% X7R CER	1		C7
60	60		23060K5-00		K5-03 PAL,OCTAL,REG	1		E26
61	61		23049K4-00		K4-03 PAL,HEX REG	1		E31
62	62		23131J5-00		J5-03 PAL,LOGIC	1		E37
63	63		23132J5-00		J5-03 PAL,LOGIC	1		E44
64	64		1205747-00		FUSE, SUB-MINI, 5.000A, 125V, A	1		F1
65	65		1210929-02		FUSE, SUB-MINI 1.000A, 125V, A	2		F2,F3
66	66		1316736-00		374.0 .25 W 1.0 % RN55D-F10	4		R11,R12,R15,R16
67	67		1316115-00		35.70 .25 W 1.0 % RN55D-F10	4		R13,R14,R17,R18
68	68		1914437-00		7905 VOLT REG,FLX -5V	1		Q1
69	69		7414765-00		HEATSINK	1		
70	70		1002424-00		1200.0 MME 100V 5%200PPM MICA	1		C8
71	71		1221494-01		SHIELDING,RFI	2		
72	72		9009000-00		EYELET,ROLLED 0.1210DX0.156	2		
73	73		9008451-01		SCREW LOCK,STANDOFF ONLY .060TH	2		
74	75	D-MD-7430547-0-DRU	7430547-04		HANDLE,LC	1		
75	76		9006010-01		SCREW,MACH PAN PHIL 4-	1		
76	77		9008268-00		COMPOUND, THERMAL JOINT	A/R		

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							V-35 LINE CARD		K	PL	M3101-0-CBP	B



AUTOMATED BY PRTLST.4Q(50)

PARTS LIST

SHEET A3 OF A3

LINE ITEM TOP DOCUMENT

MIN  
PART NUMBER REV DESCRIPTION

QTY PER VARIATION  
00

REFERENCE DESIGNATOR

VARIATION REVISION LEVEL:

77 NOTE: DO NOT INSTALL JUMPER W2 (CUSTOMER OPTION).  
78 NOTE: REF.DES. C24 IS NOT USED.  
79 NOTE: SPARE LOCATIONS ARE E49,E65,R28.

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							V.35 LINE CARD		K	PL	M3101-0-DBP	B

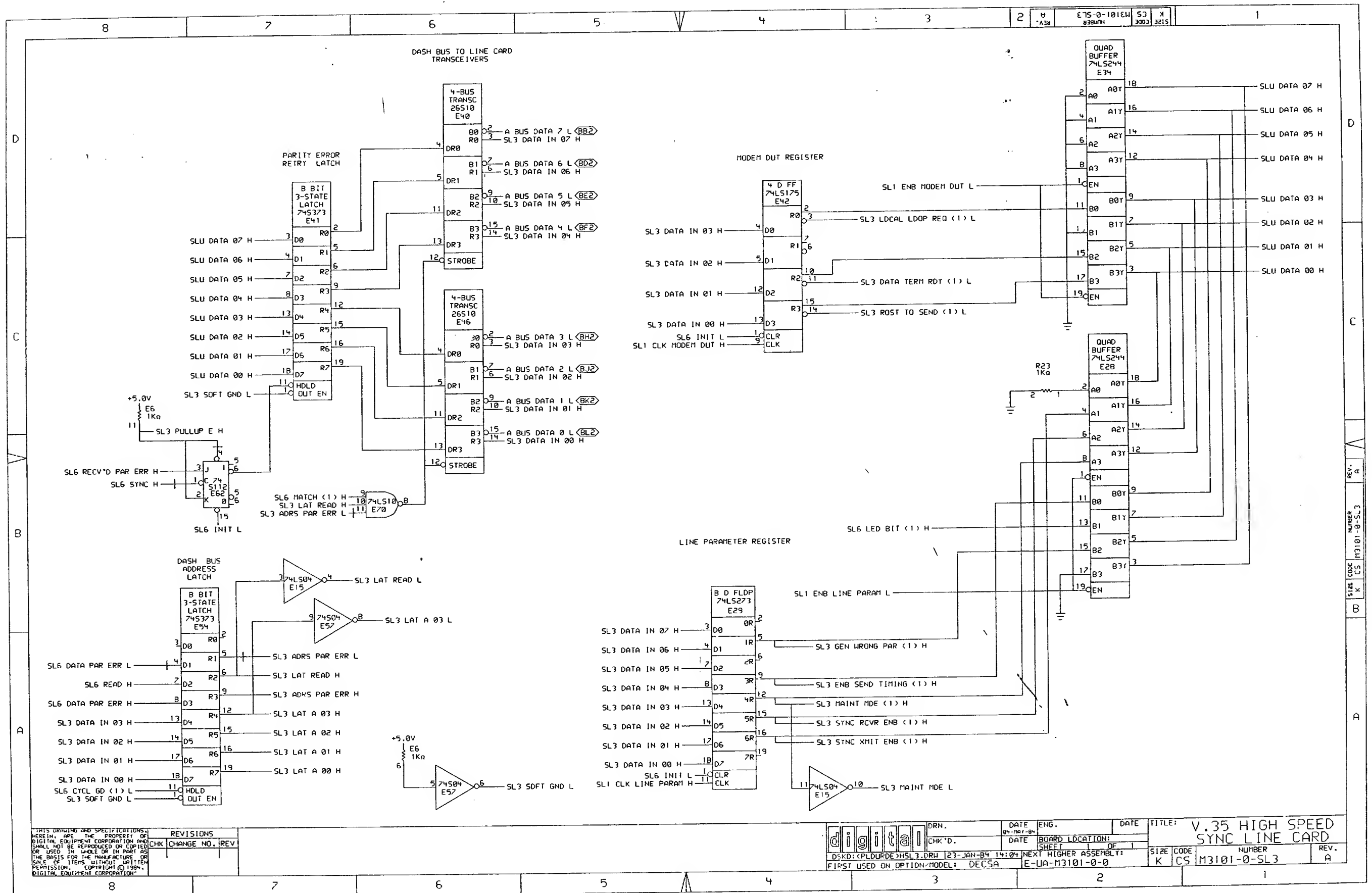




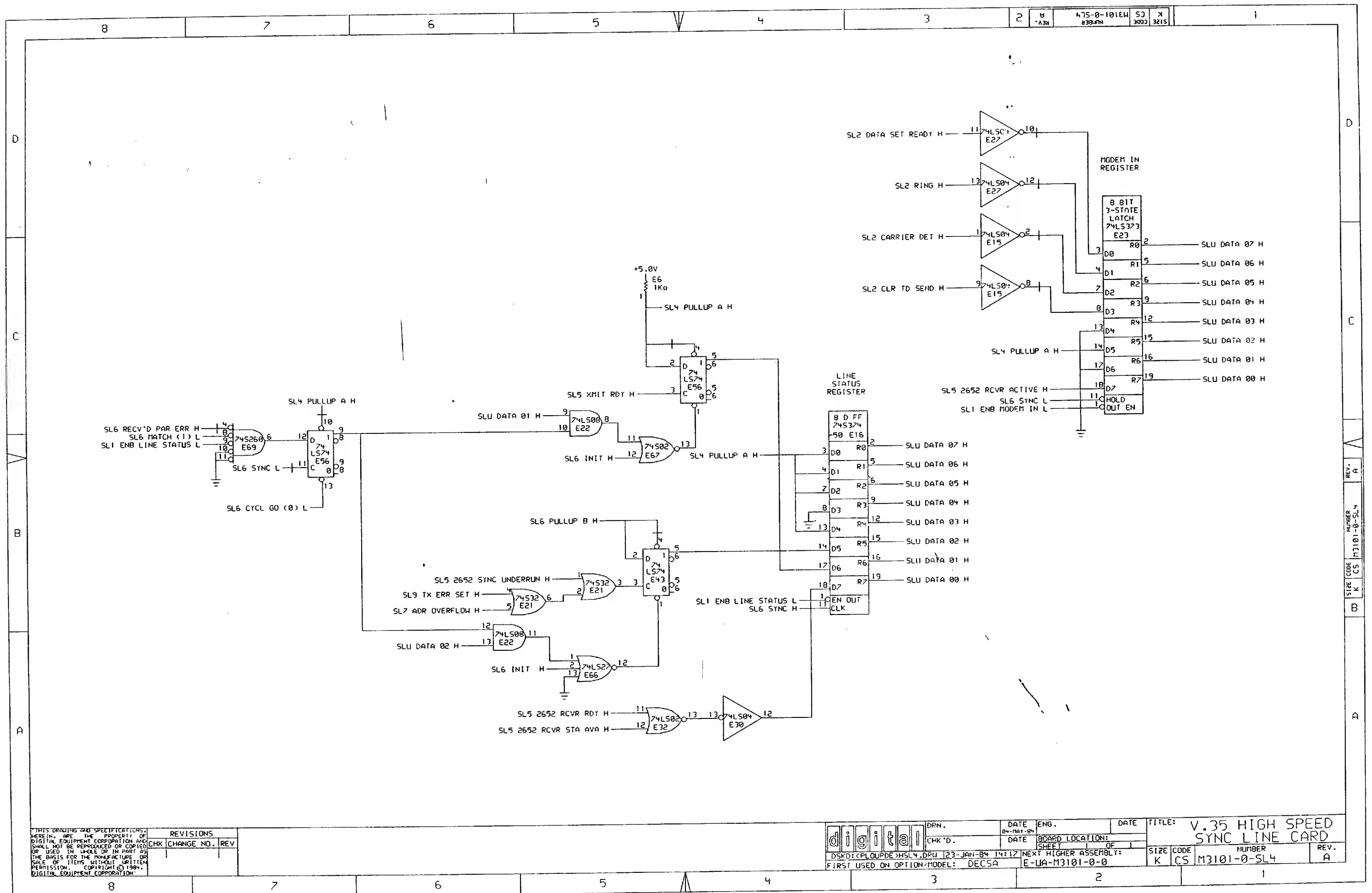




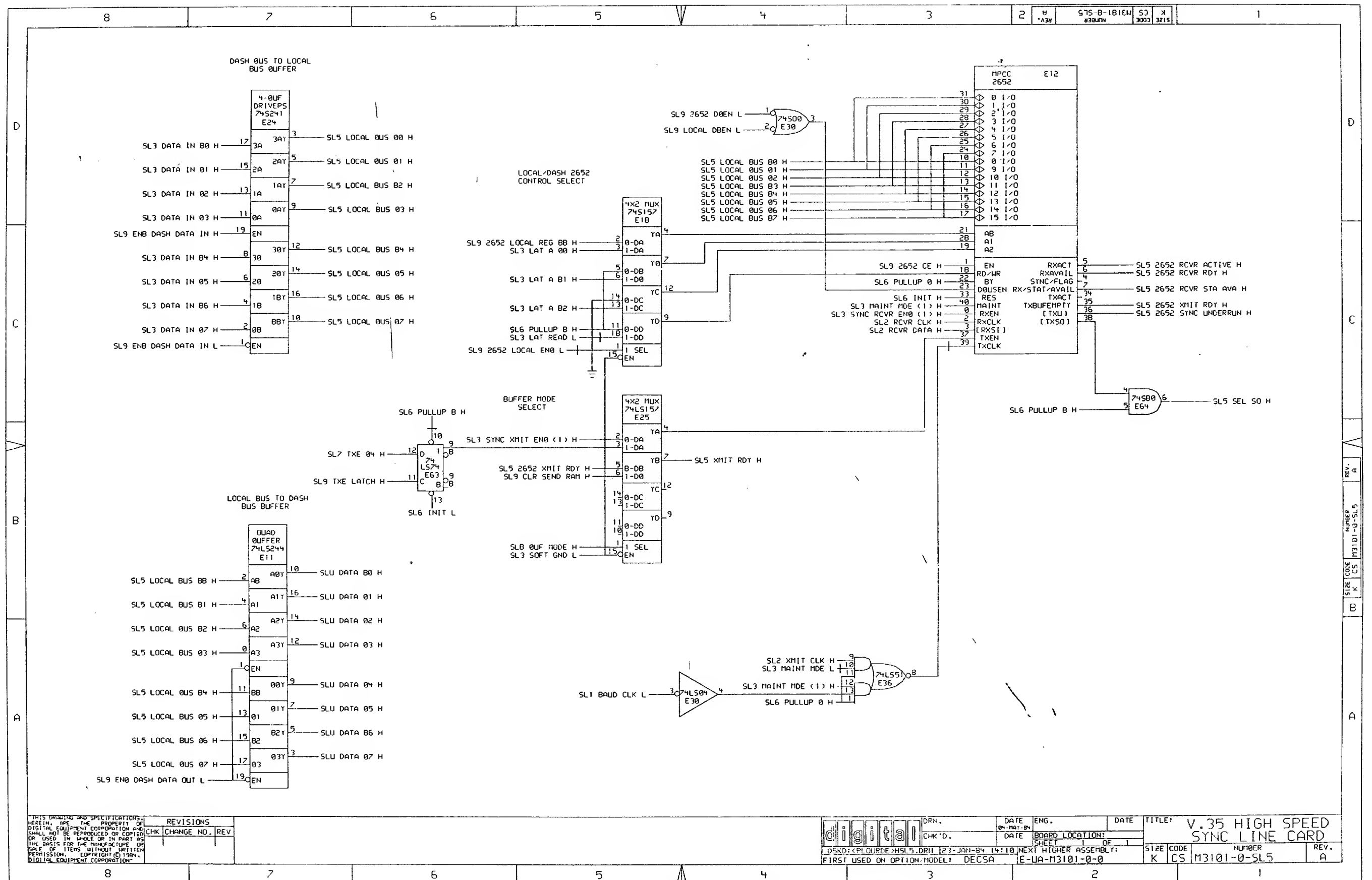




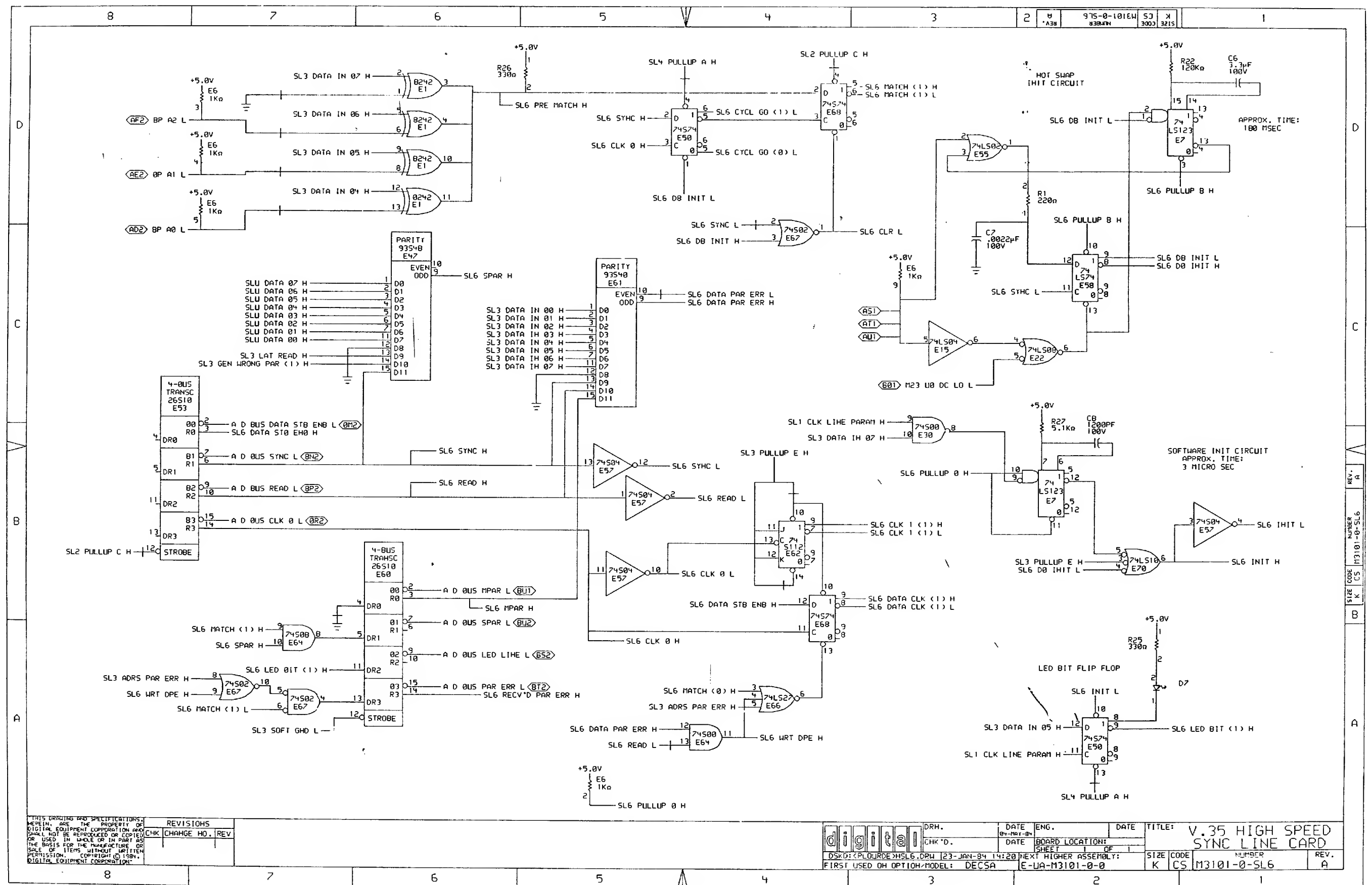




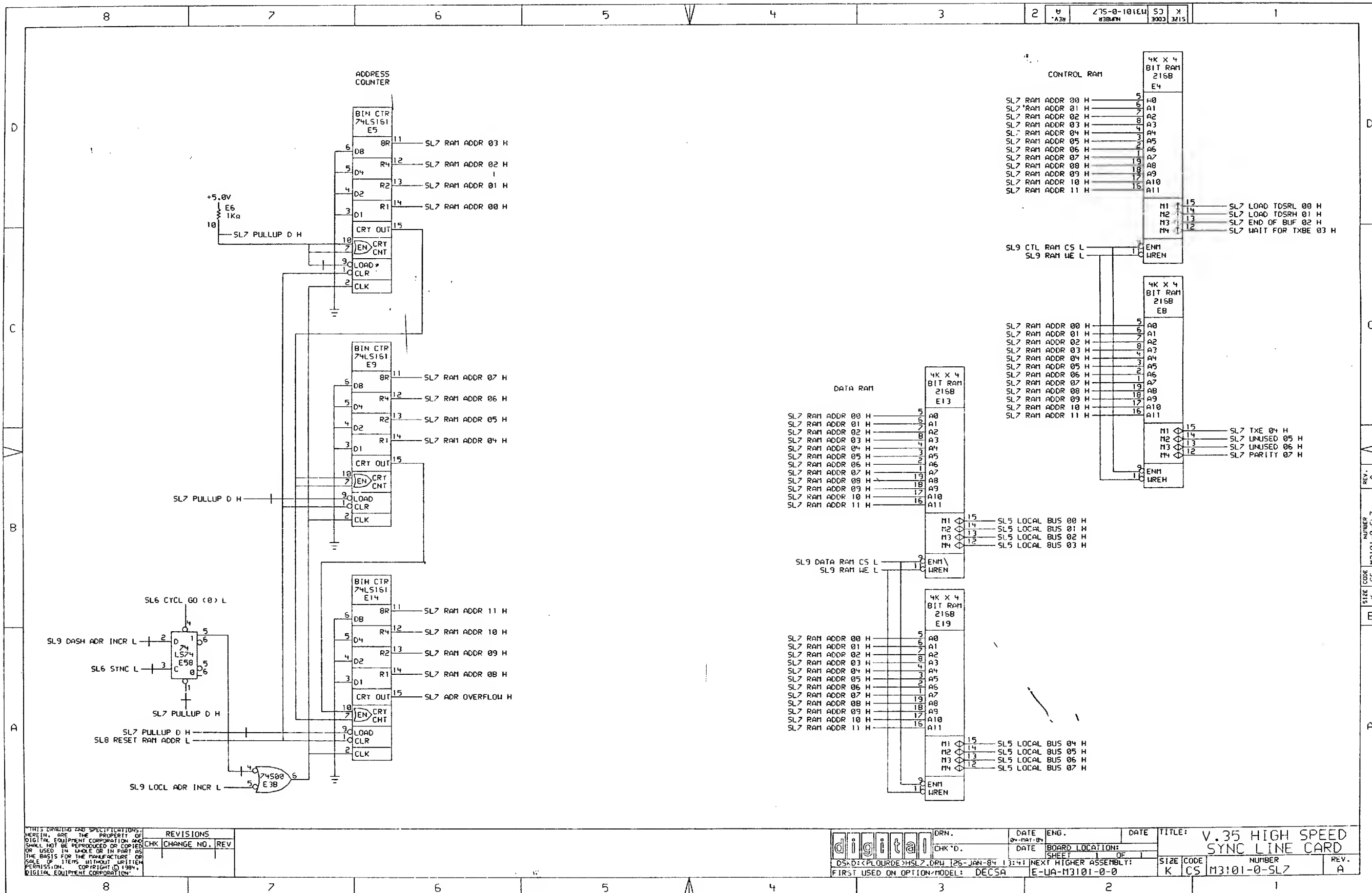










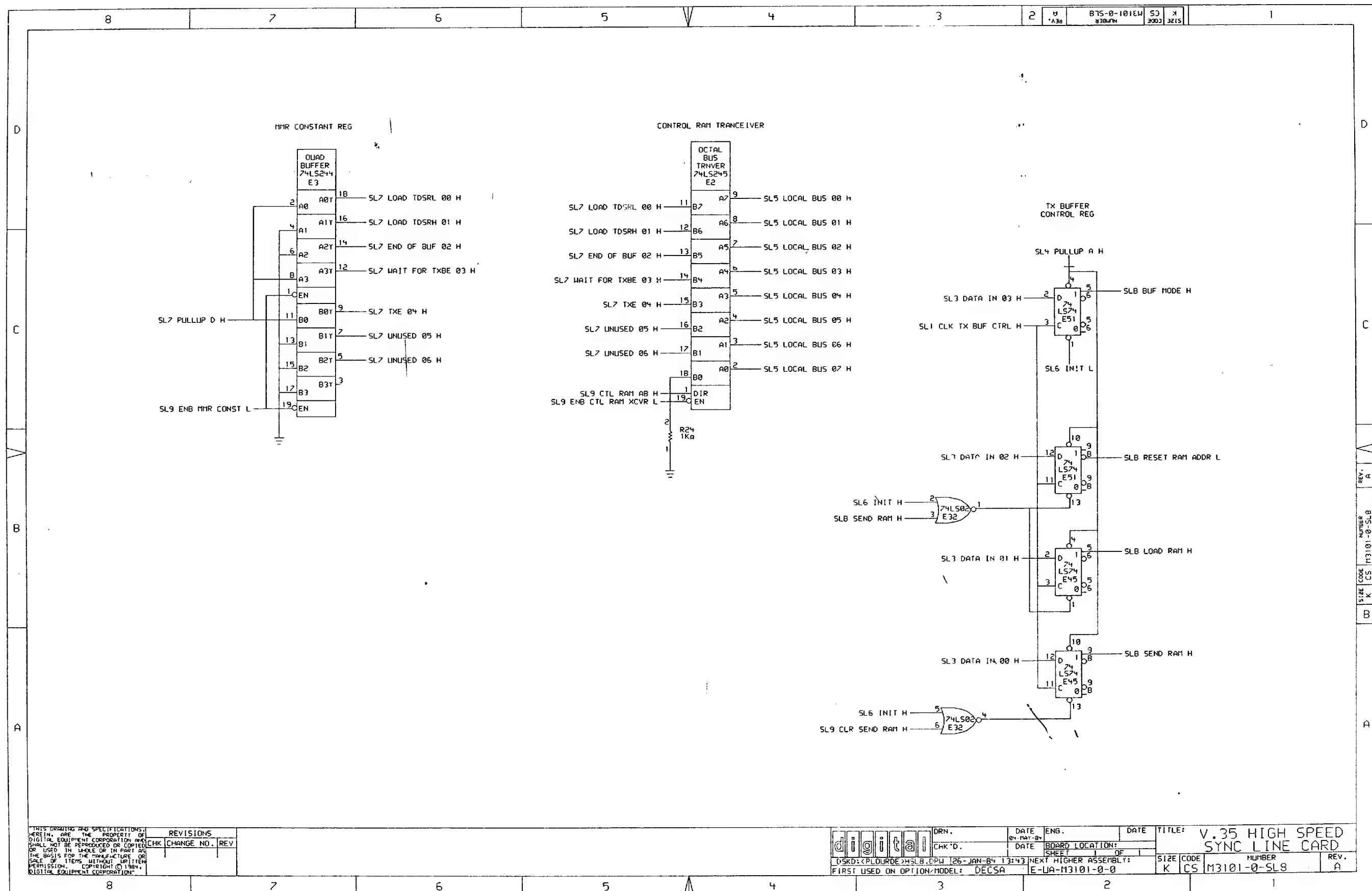


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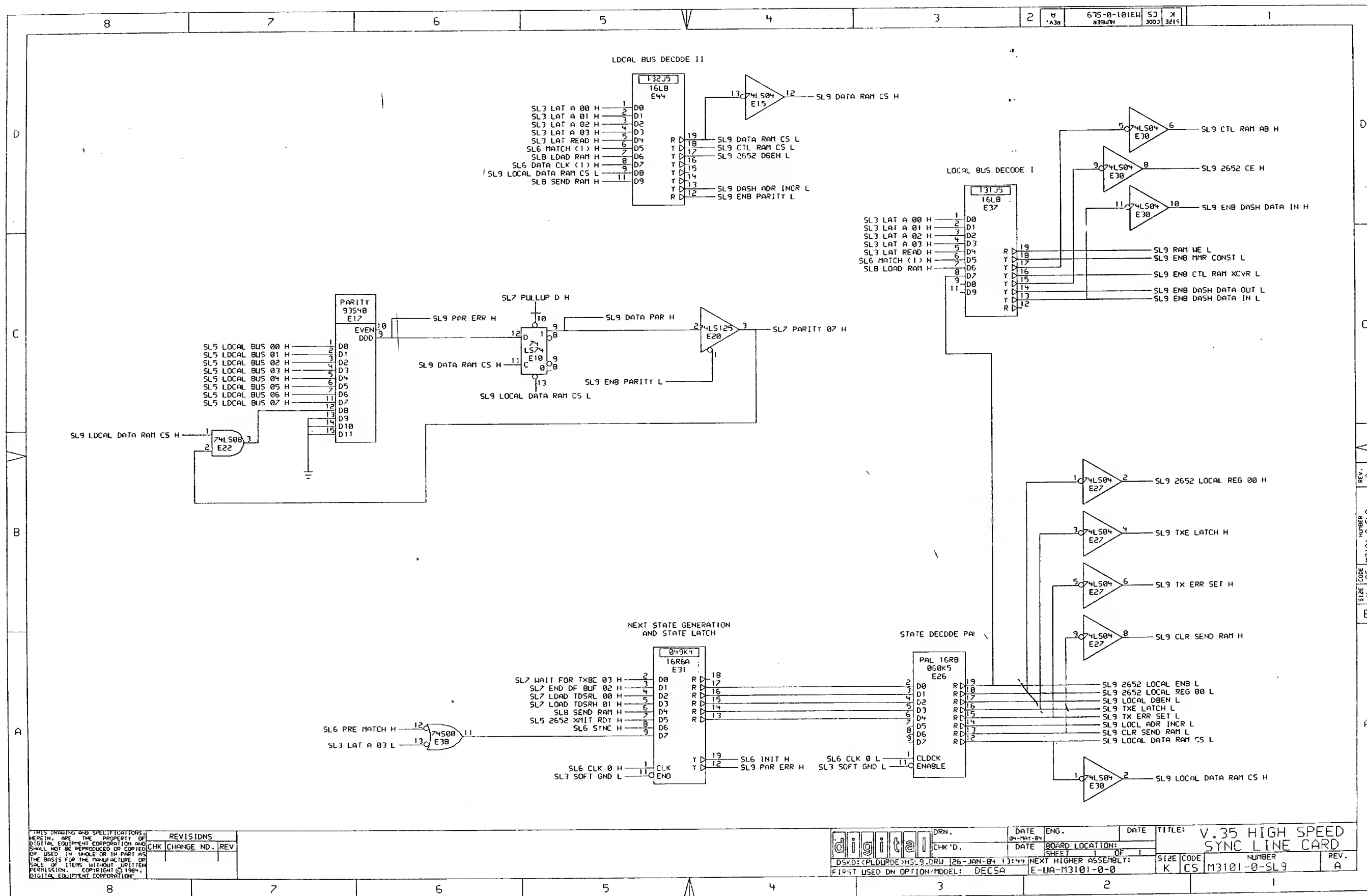
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN.	DATE	ENG.	DATE	TITLE: V.35 HIGH SPEED SYNC LINE CARD			
	CHK'D.	DATE	BOARD LOCATION:					
		SHEET		OF				
DS-X-PL0URDE2SL7.DRW 125 JAN 84 13:41 NEXT HIGHER ASSEMBLY:					SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION MODEL: DECSA					K	CS	M3101-0-SL7	A
E-UA-M3101-0-0								







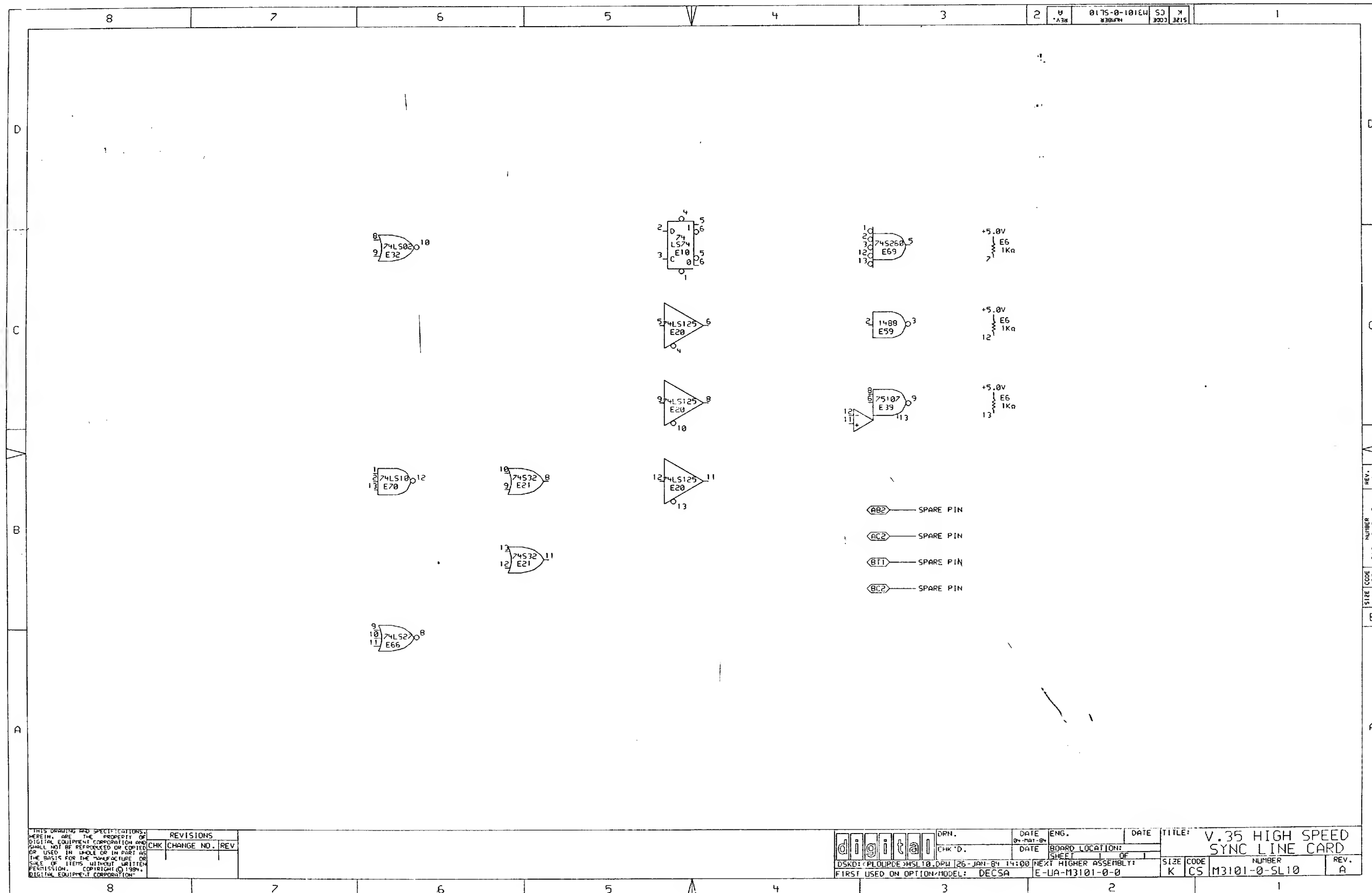


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REVISIONS		
CHK	CHANGE NO.	REV

digital	ORN.	DATE	ENG.	DATE	TITLE: V.35 HIGH SPEED SYNC LINE CARD		
	CHK'D.	DATE	BOARD LOCATION:	SHEET	SIZE	CODE	NUMBER
DSKD:PLDURDE:SL9.DRW 26-JAN-84 13:44 NEXT HIGHER ASSEMBLY:					K	CS	M3101-0-SL9
FIRST USED DN OPTION/MODEL: DECSA E-UA-M3101-0-0					REV. A		





74LS02  
E32

74LS74  
E10

74LS260  
E69

+5.0V  
E6  
1K $\Omega$

74LS125  
E20

1488  
E59

+5.0V  
E6  
1K $\Omega$

74LS125  
E20

75107  
E39

+5.0V  
E6  
1K $\Omega$

74LS10  
E70

74S32  
E21

74LS125  
E20

AB2 — SPARE PIN

AC2 — SPARE PIN

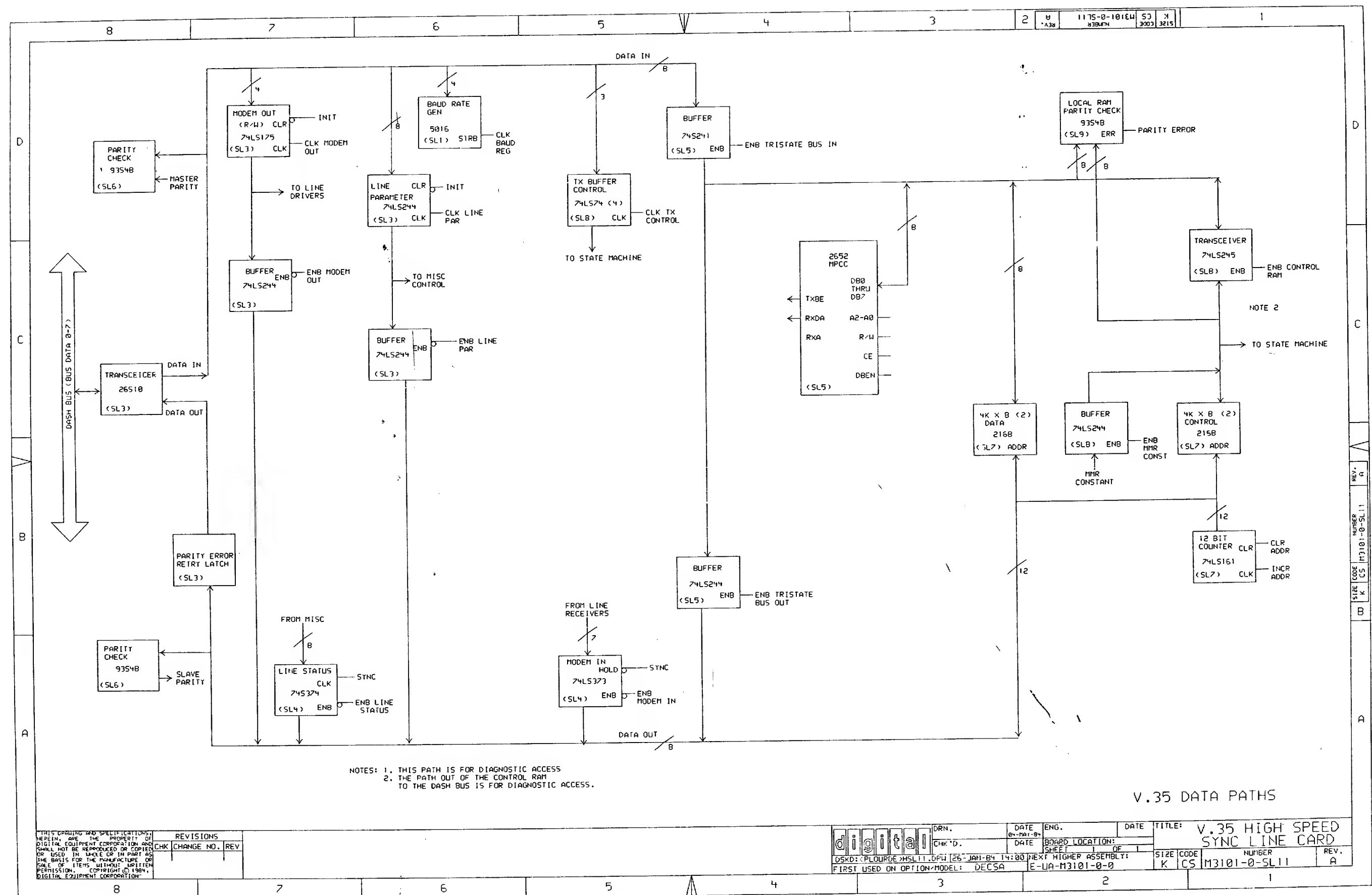
BT1 — SPARE PIN

BC2 — SPARE PIN

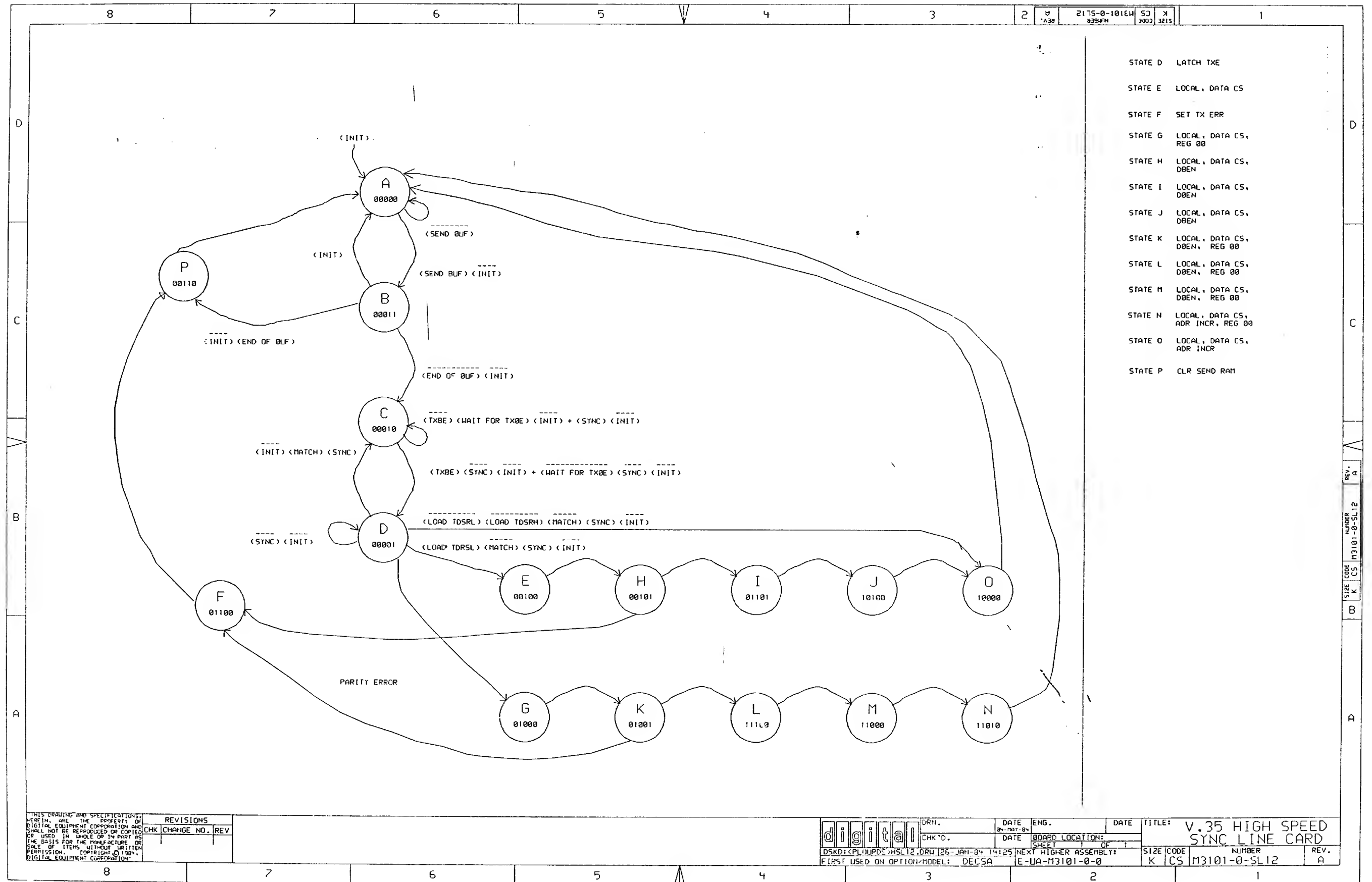
74LS27  
E66

74S32  
E21















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DRAWING DIRECTORY

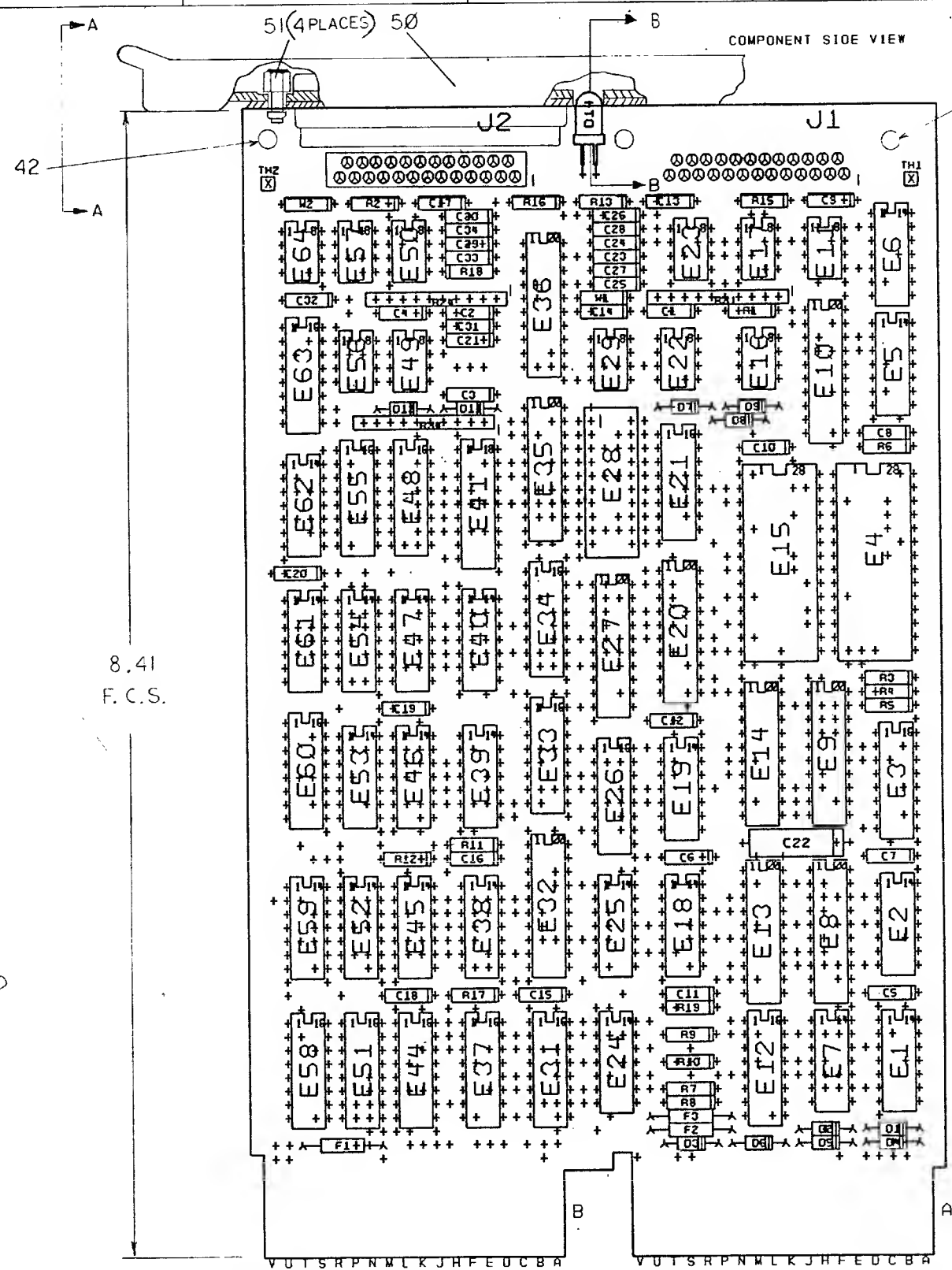
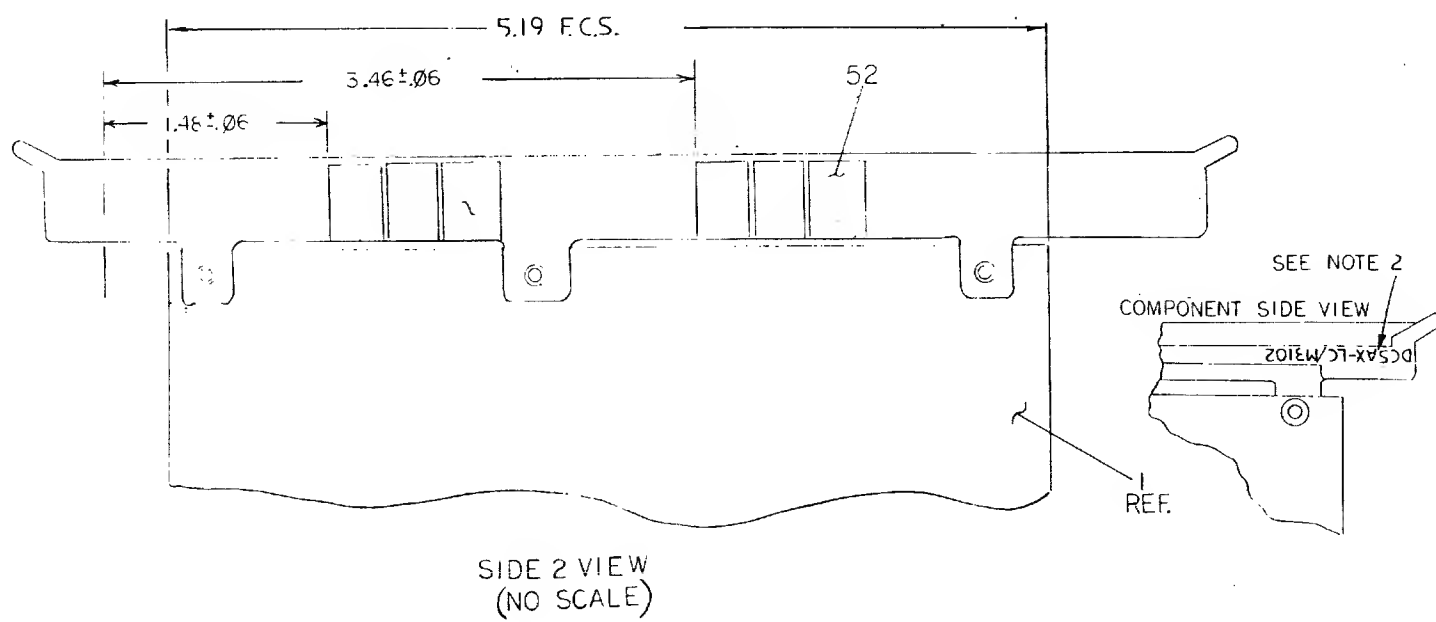
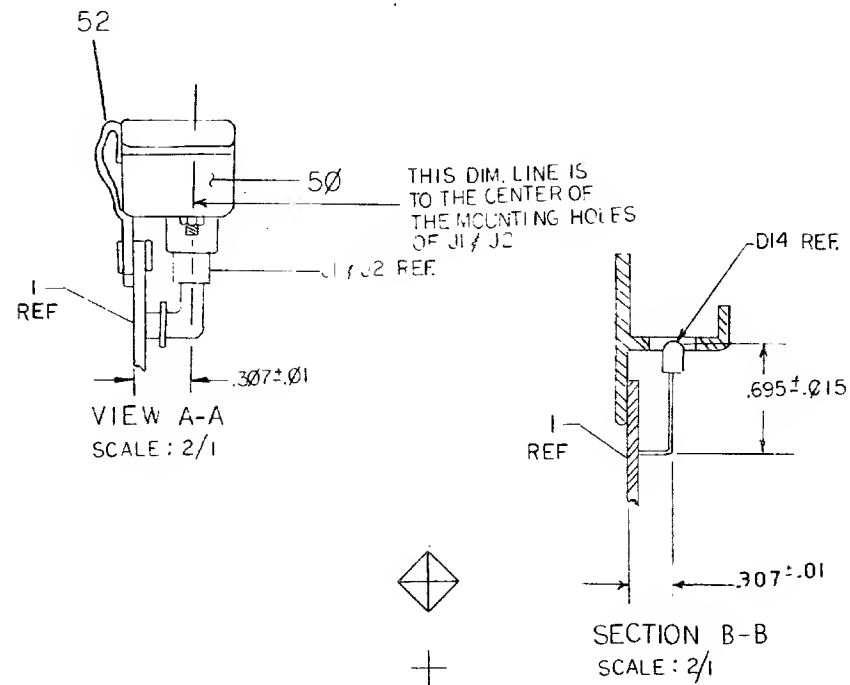
ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS									
						A	B	C	D	E	F	G	H	I	J
1			M3102-00	DUAL RS-232 LINE CARD		A	B	C	D	E	F	G	H	I	J
2	D-UA-M3102-0-0	1		UNIT ASSEMBLY		A	B	C	D	E	F	G	H	I	J
3	K-PL-M3102-0-DBP	2		PARTS LIST		A	B	C	D	E	F	G	H	I	J
4	K-CS-M3102-0-DBS			SUDS DATA BASE		A	B	C	D	E	F	G	H	I	J
5	K-CS-M3102-0-SLU1	1		SUDS SCHEMATIC		A	A	A	A	A	A	A	A	A	A
6	K-CS-M3102-0-SLU2	1		SUDS SCHEMATIC		A	A	A	B	C	B	C	C	C	C
7	K-CS-M3102-0-SLU3	1		SUDS SCHEMATIC		A	A	A	B	C	B	C	C	C	C
8	K-CS-M3102-0-SLU4	1		SUDS SCHEMATIC		A	A	A	A	B	A	B	B	B	B
9	K-CS-M3102-0-SLU5	1		SUDS SCHEMATIC		A	A	A	A	B	A	B	B	B	B
10	K-CS-M3102-0-SLU6	1		SUDS SCHEMATIC		A	B	B	B	C	B	C	C	C	C
11	K-CS-M3102-0-SLU7	1		SUDS SCHEMATIC		A	A	B	B	B	B	B	B	B	B
12	K-CS-M3102-0-SLU8	1		SUDS SCHEMATIC		A	A	A	A	B	A	B	B	B	B
13	K-CS-M3102-0-SLU9	1		SUDS SCHEMATIC		A	A	A	A	A	A	A	A	A	A
14	K-CS-M3102-0-SLU10	1		SUDS SCHEMATIC		A	A	B	C	D	C	D	D	D	D
15	K-PC-M3102-0-DBI			PC DATABASE		A	A	A	B	B	B	B	B	B	B
						B	B	B	B	C	B	C	C	D	
16			5014992-00	ETCHED BOARD		B	B	B	B	C	B	C	C	D	
17	D-MD-5014992-0-0	2		DRILL AND ETCH DRAWINGS		A	A	A	A	B	A	B	B	C	
18	D-EC-5014992-0-0	1		ETCH CUT DRAWING		A	B	C	C	D	C	D	D	E	

NOTES:

						DD REV	A	B	C	D	E	F	G	H	I	J
REVISION HISTORY			CONT. REVISION HISTORY			MADE BY: A. KRAWIECKI DATE: 10 SEP 84		DIGITAL								
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	CHECKED BY: J. FALKOWSKI DATE: 11 SEP 84		TITLE DRAWING DIRECTORY								
--	INITIAL	A				DESIGN ENGINEER: J. DOHERTY DATE: 30 JUL 84		DUAL RS-232 LINE CARD								
	M3102-TW001	B				RESPONSIBLE ENG: J. DOHERTY DATE: 30 JUL 84		SIZE CODE DOCUMENT NUMBER REV.								
	M3102-TW002	C				PRODUCTION ENG: W. BROOKE DATE: 10 AUG 84		K DD M3102-0-0 J								
	M3102-TW003	D						SHEET 1 OF 1 MKO								
	M3102-TW004	E														
	M3102-MK005	F														
	M3102-MK006	H														
ML	M3102-MK007	J														



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NOTES:

1. 3. F. STEEL/PEPA. 2ML2.
2. STAMP LOCATION/DIRECTION OF MODULE
3. E24 IN 7 SPARE LOCATION.
4. IN REQ. IS LPWK.
5. R15, R16, C23-C34 OPTIONAL.

STEP	Y AXIS	STEP	TIMES
1	1	0	1
2	2	1	1

CHANGE NO	REV	DESCRIPTION
1	1	INITIAL
2	1	INITIAL
3	1	INITIAL
4	1	INITIAL
5	1	INITIAL
6	1	INITIAL
7	1	INITIAL
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98	1	INITIAL
99	1	INITIAL
100	1	INITIAL

6.E30,E42&E43 IC, POSITIONS NOT USED.

ETCH REV.	DATE
0	11-18-83

SIGNATURES	DATE	digital
DRN: Frank E. Pout	11-18-83	
CHK: O. E. TERRY	11-18-83	
MECH. ENG: [Signature]	11-18-83	
PROJ. ENG: [Signature]	11-18-83	
PROD: [Signature]	11-18-83	
SCALE: 2:1		
SHT. 1 OF 1		
NEXT HIGHER ASSY: B-DD-M3102-0-0		

MK 1 WO#173383



AUTOMATED BY PRTLST,4Q(50)

## P A R T S   L I S T

SHEET A2 OF A2

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
				VARIATION REVISION LEVEL:	00 E4	
31	29	1914085-00		74S260 NOR GATE-DUAL,POS	1	E62
32	9	1914214-00		LS374 FF-D OCTAL EDGE TRIG	2	E10,E36
33	6	1914451-00		LS393 COUNTER,BINARY,4BIT	1	E6
34	11	1914866-00	AM	93S48PCGEN/CHECK PARITY, 4B	2	E12,E31
35	8	1915193-00		LS244 DRIVER,LINE,OCTAL,TR	3	E9,E27,E35
36	12	1915219-00		LS373 FF-D OCTAL-TRANSPARE	1	E32
37	13	1915415-00		9635 DRIVER,DUAL,EIA RS-	5	E16,E22,E29,E49,E56
38	10	1919342-01		9639 RECEIVER,LINE,DUAL	6	E11,E17,E23,E50,E57,E64
39	24	2112623-00		DUAL BAUD RATE GEN/PROG DIVIDER,	1	E41
40	4	2118623-00		2661-3COMMUNICATIONS,INTER	2	E4,E15
41	50	7430547-01		HANDLE,LC	1	
42	42	9009000-00		EYELET,ROLLED 0.1210DX0.156	2	
43	38	9009185-00		JUMPER, WIRE, INSULATED, BLACK B	2	W1,W2
44	51	9008451-01		SCREW LOCK,STANDOFF ONLY .060TH	4	
45	52	1221494-01		SHIELDING,RFI	2	
46	54	1912837-00		LS123 ONE SHOT-DUAL,RETRIG	1	E3
47	55	1005334-00		3.3MFD 20V 10% S.TANT	1	C22
48	56	1300539-00		120.0 K .25 W 5.0 % CF	1	R19
49	57	1013466-12		2200.0 MMF 50V 10% X7R CER	1	C7

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							DUAL RS-232 LINE CARD		K	PL	M3102-0-DBP	J

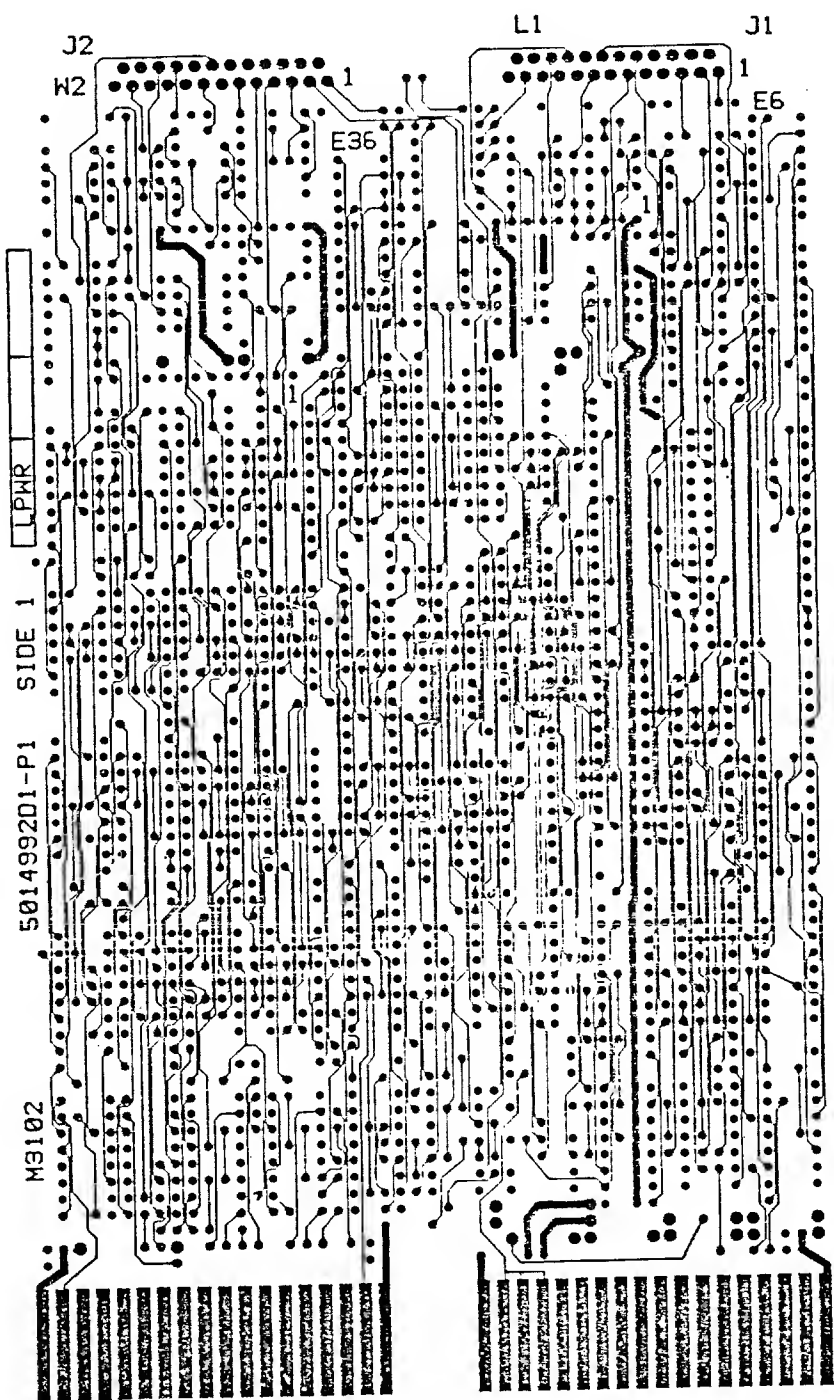
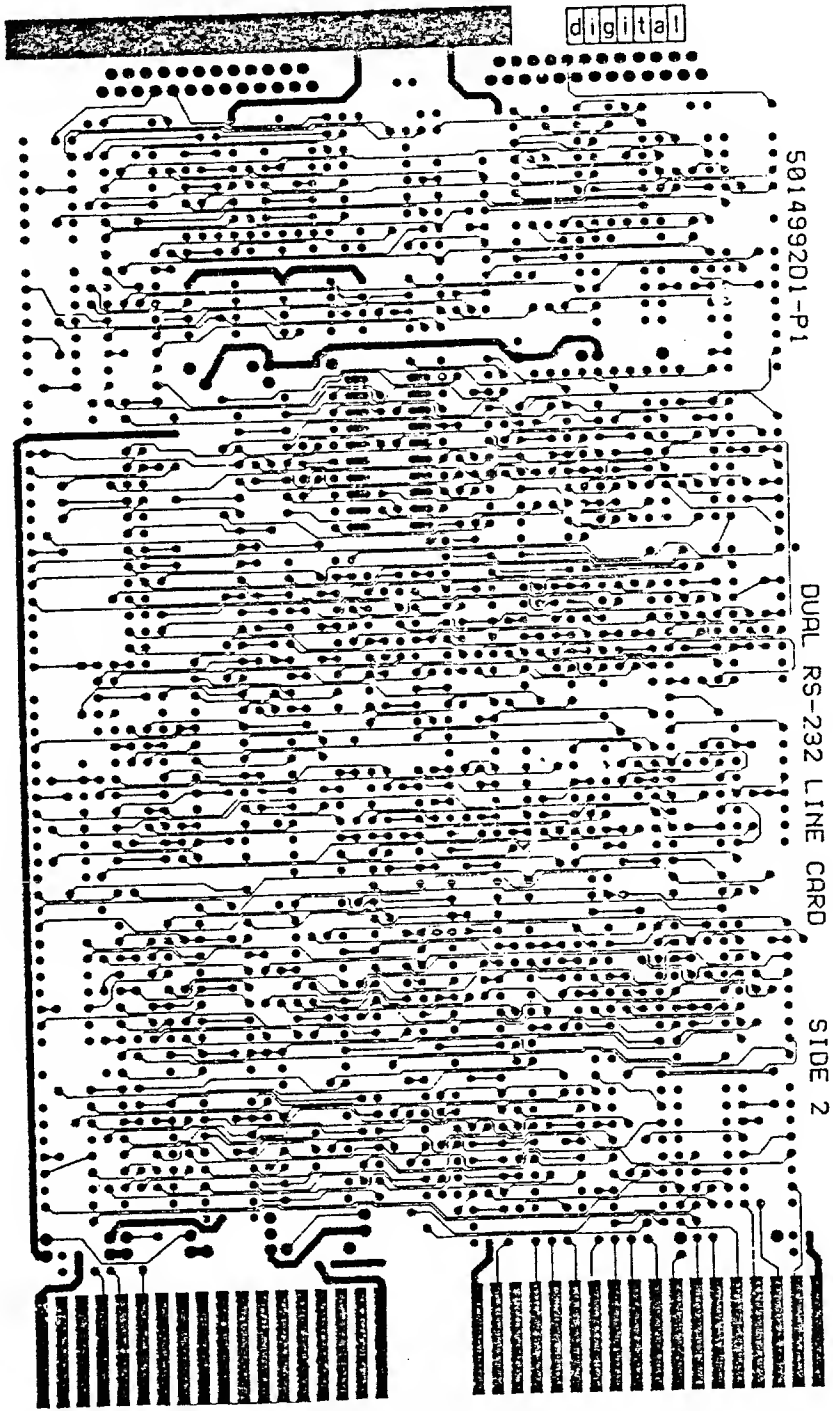


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3 0-0-2664105 33.0

D  
C  
B  
A

REVISIONS  
DATE  
BY  
1



D  
C  
B  
A

DEC 5014992-0-0

digital  
ETCH CUT DRAWING  
D-UA-M3102-0-0  
2/1



AUTOMATED BY PRTLST.4Q(50)

## P A R T S   L I S T

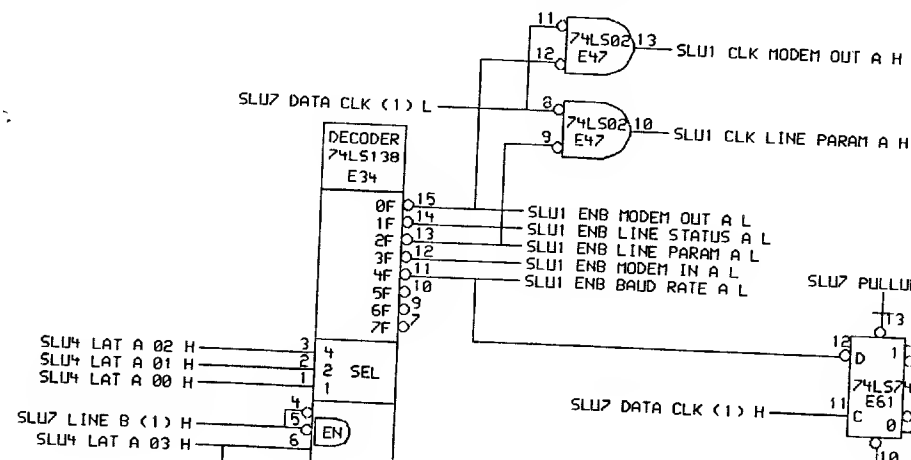
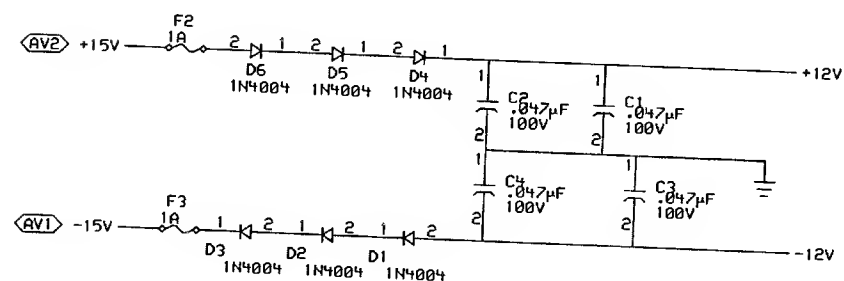
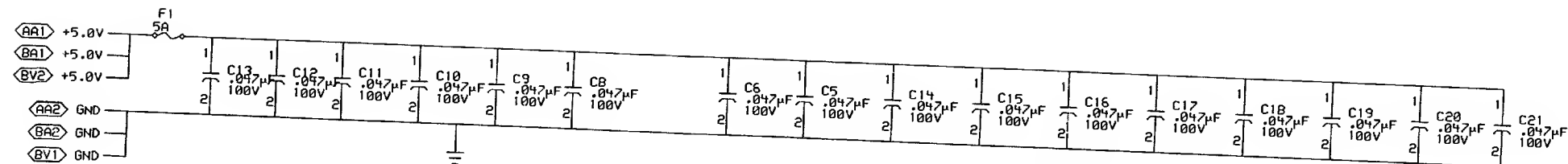
SHEET A1 OF A2

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
				VARIATION REVISION LEVEL:	00	
					E4	
1	40	D-MD-5014992-0-0	5014992-00	DRILL&ETCH BOARD M3102 DUAL RS-2	1	
2	39		1012784-00	.047 MFD 50V +80-20% CER	20	C1-C6,C8-C21
3	1		1105796-00	PIV= 400 IO= 1.00A 1N4004 DO-41	11	D1-D9,D11,D12
4	2		1110324-00	LED 1MCD@10MA PIV=3	1	D14
5	44		1205747-00	FUSE, SUB-MINI, 5.000A, 125V, A	1	F1
6	49		1210929-02	FUSE, SUB-MINI 1.000A, 125V, A	2	F2,F3
7	43		1220398-07	CONN,D SUB 25PIN ASSY RT ANGLE	2	J1,J2
8	31		1300271-00	220.0 .25 W 5.0 % CF	3	R1,R2,R18
9	35		1300295-00	330.0 .25 W 5.0 % CF	2	R10,R13
10	33		1300365-00	1.0 K .25 W 5.0 % CF	6	R7,R8,R9,R11,R12,R17
11	32		1300479-00	10.0 K .25 W 5.0 % CF	4	R3,R4,R5,R6
12	37		1319728-01	R NETWORK 8-27K 1.0 % 10PIN	3	R24,R31,R34
13	16		1909712-00	DEC 8242 COMPARATOR-4BIT N,	1	E24
14	21		1910534-00	74S04 INVERTER GATE-HEX 11	1	E38
15	3		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	3	E1,E45,E52
16	30		1910545-00	74S112 FF-JK DUAL,EDGE TRIG	1	E63
17	22		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1	E39
18	17		1912389-00	74S08 AND GATE-QUAD 2IN,PO	2	E25,E59
19	25		1912801-00	LS02 NOR-GATE-QUAD 2IN	2	E46,E47
20	5		1912803-00	LS04 INVERTER GATE,HEX	2	E5,E19
21	23		1912805-00	LS08 AND GATE-QUAD 2IN,PO	1	E40
22	7		1912807-00	LS10 NAND GATE-TRIPLE 3IN	2	E7,E18
23	26		1912813-00	LS27 NOR GATE-TRIPLE 3IN	1	E53
24	27		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	3	E54,E61,E2
25	28		1912833-00	LS109 FF-JK DUAL,POS EDGE	1	E60
26	19		1912842-00	LS138 DECODER-THREE INPUT,	2	E34,E55
27	15		1912853-00	LS175 FF-D QUAD	4	E21,E26,E33,E48
28	14		1913493-00	74S241 OCTAL BUFFER,TRI-STA	1	E20
29	46		1913671-50	74S374 FF-D,OCTAL,TRI STATE	3	E8,E13,E14
30	20		1913788-00	26S10 TRANSCEIVER,BUS,QUA	4	E37,E44,E51,E58

REVISION HISTORY		BASIC PART NO: M3102		DRN: <i>ANK</i>	DATE: 12-JUN-85	D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A	F TELLIER	DATE: 20-JAN-82				
---	INITIAL	X02	SECTION VARIATION INDEX	CHK'D: <i>F. Garofalo</i>	DATE: 20-JAN-82	TITLE PARTS LIST			
---	INITIAL	A	[A] 00			DUAL RS-232 LINE CARD			
JB	M3102-TW001	B	[B]	DES.ENG: <i>W.L. Hyde</i>	DATE: 12-JUN-85				
JB	M3102-TW002	C	[C]	J. BUZYNSKI	DATE: 25-FEB-82	DOCUMENT NUMBER			
JB	M3102-TW03A	D	[D]			SIZE	CODE	NUMBER	REV
JB	M3102-TW004	E	[E]	RESP.ENG.: <i>W.L. Hyde</i>	DATE: 12-JUN-85				
JD	M3102-MK005	F	[F]	J. BUZYNSKI	DATE: 25-FEB-82	K	PL	M3102-0-DBP	J
JD	M3102-MK006	H	[H]						
PD	M3102-MK007	J	[J]	MFG.ENG.: <i>W.L. Hyde</i>	DATE: 12-JUN-85				
			[K]	D. VANASSE	DATE: 25-FEB-82	RELEASE DATE: 12-JUN-85			
			[L]						
			[M]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:		EDIT #	
			[N]	D-UA-M3102-0-0	#K-DD-M3102-0	MK1143,PLS		56	

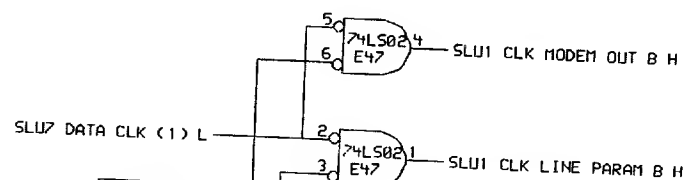
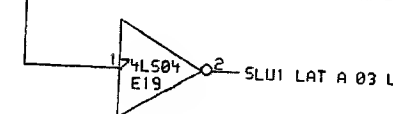
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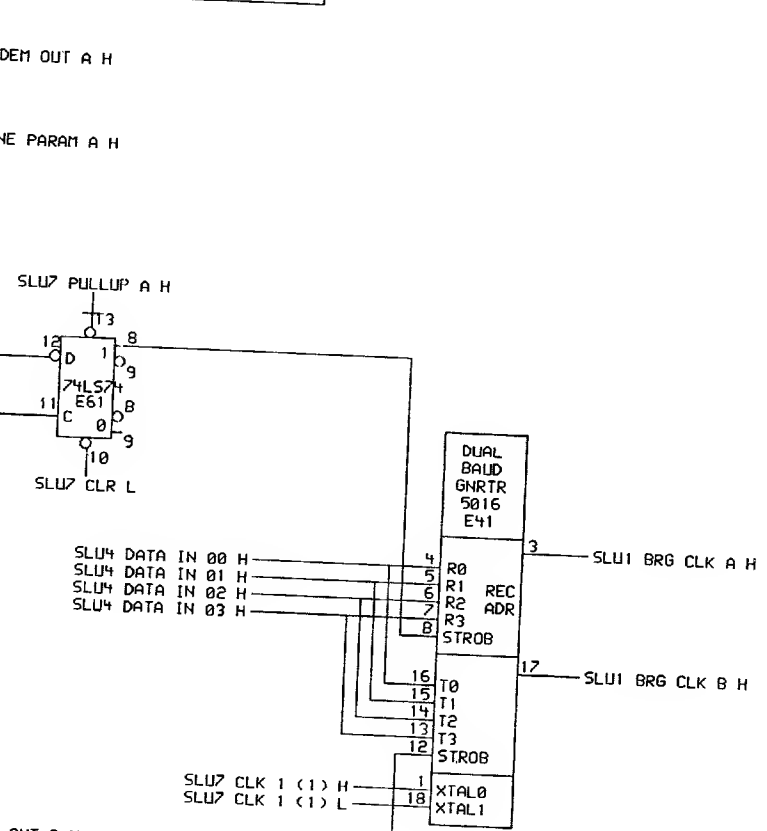
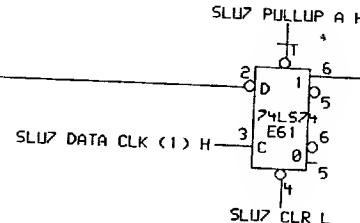
LINE A SEL

SLU4 LAT A 02 H  
SLU4 LAT A 01 H  
SLU4 LAT A 00 H  
SLU7 LINE B (1) H  
SLU4 LAT A 03 H



LINE B SEL

SLU4 LAT A 02 H  
SLU4 LAT A 01 H  
SLU4 LAT A 00 H  
SLU7 LINE B (1) L  
SLU4 LAT A 03 H

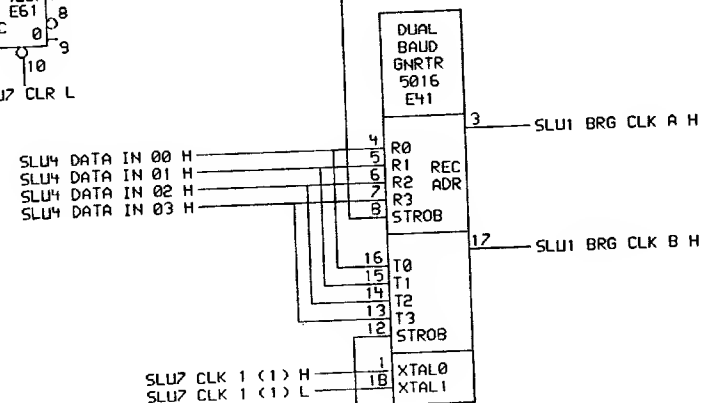
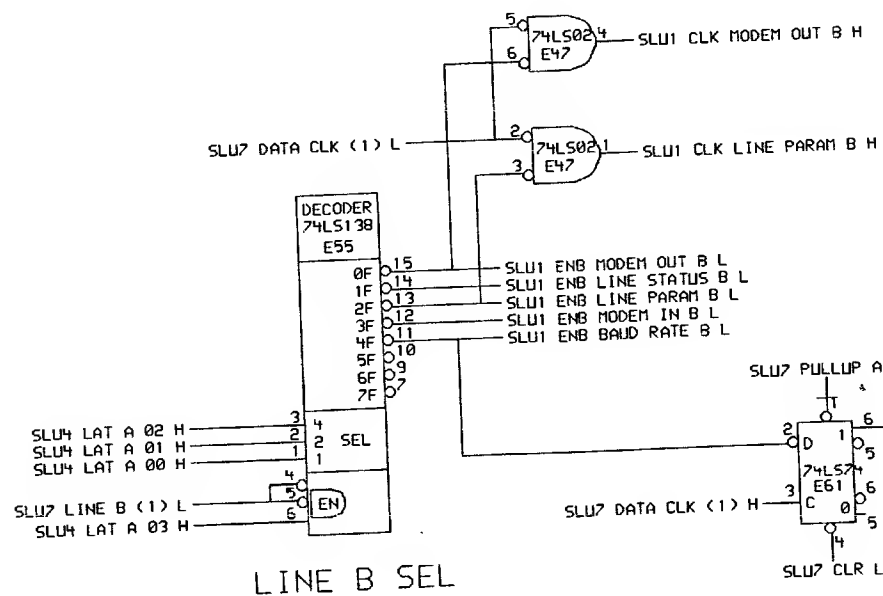
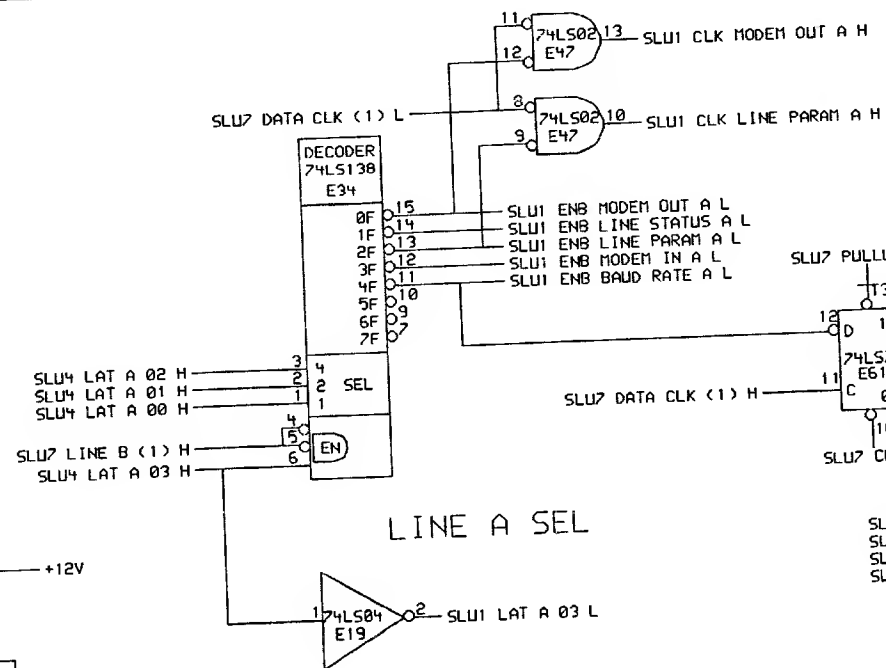
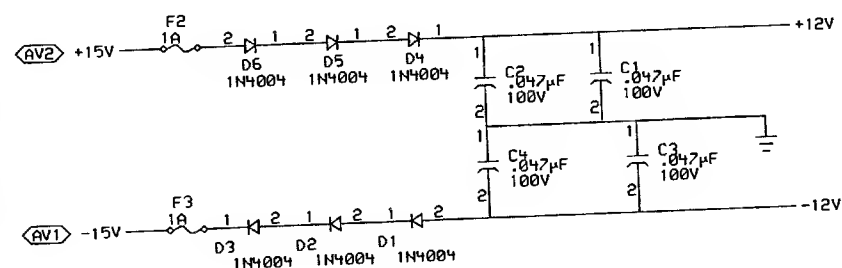
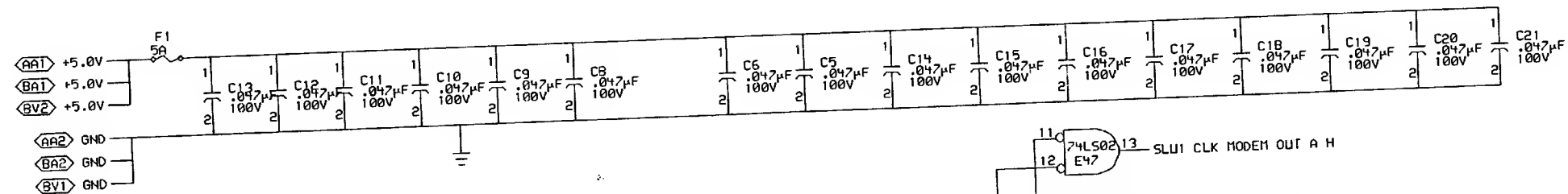


EXTERNAL XMIT  
BAUD RATE GEN

REG SELECT & BAUD RATE GEN

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DUAL ASYNC SERIAL
DSKD: <BUZYNSKI> M3102A.DRW	02-NOV-82	09:58	SHEET	OF 1
FIRST USED ON OPTION/MODEL:	DECSA	ID-UA-M3102-0-0	SIZE	CODE
			K	C5
				M3102-0-SLU1
				REV. A



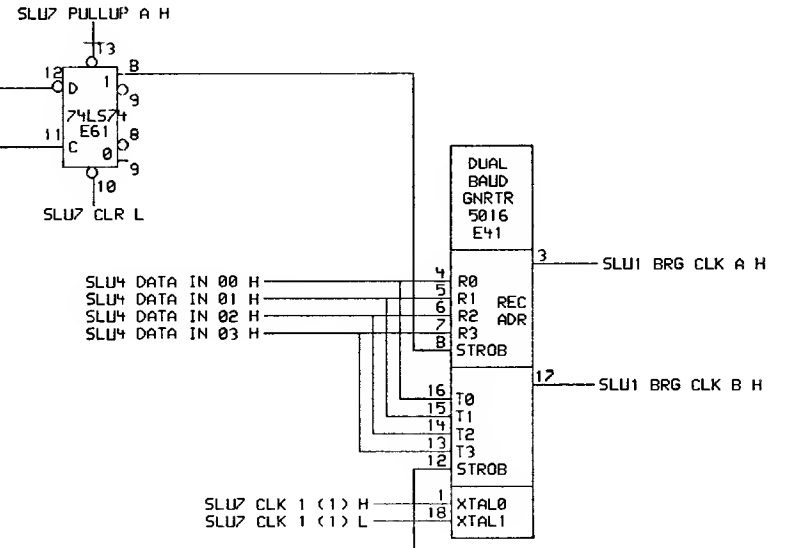
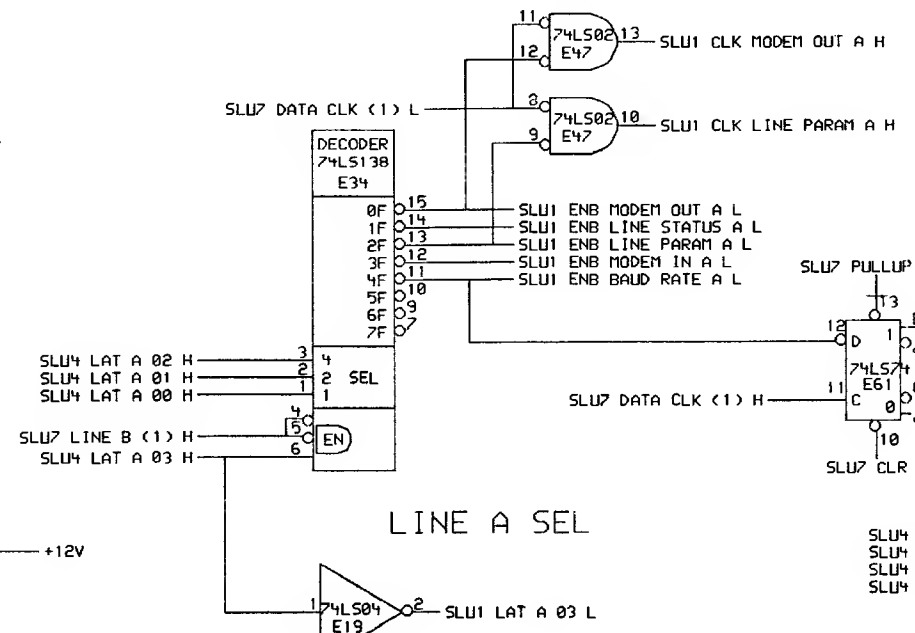
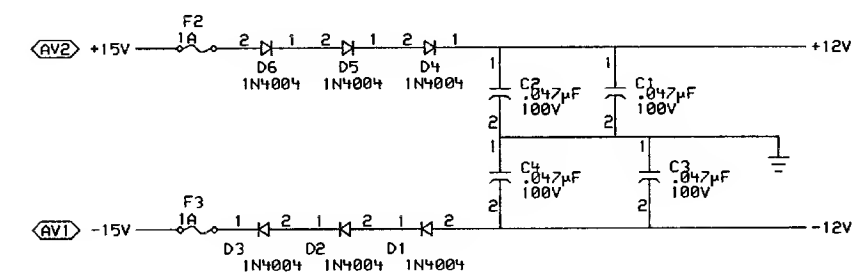
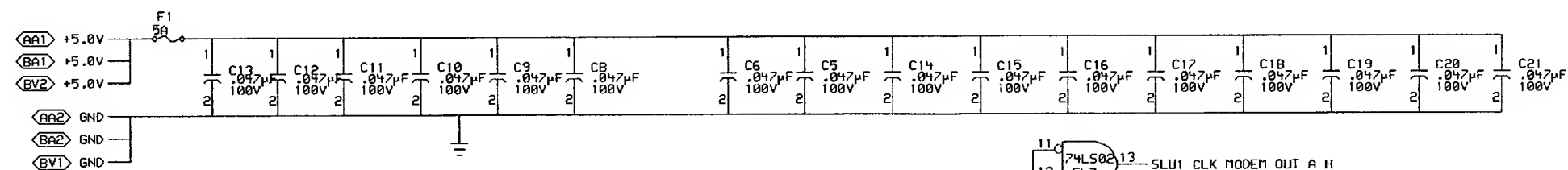


EXTERNAL XMIT  
BAUD RATE GEN

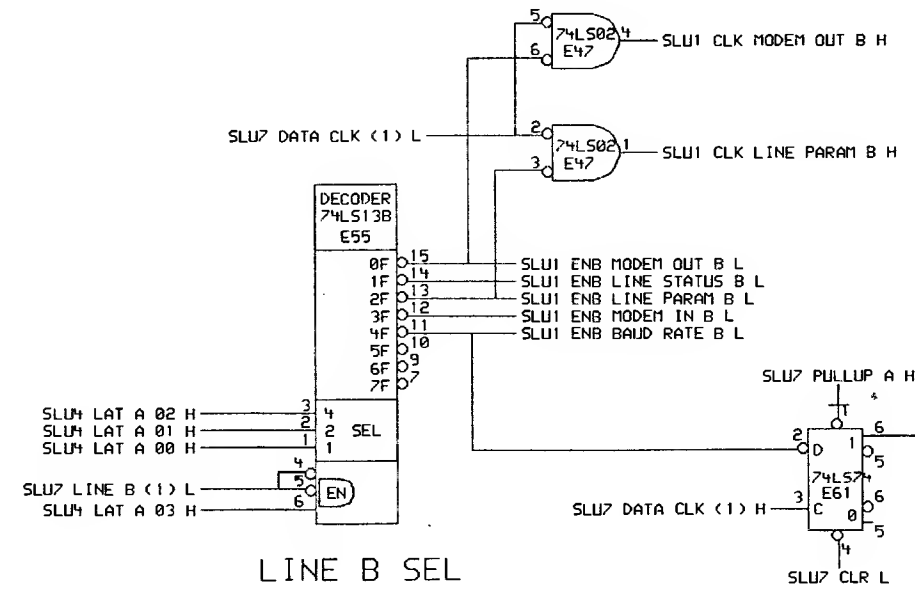
REG SELECT & BAUD RATE GEN

DRN.	DATE	ENG.	DATE	TITLE
CHK'D.	02-NOV-82	02-NOV-82	02-NOV-82	DUAL ASYNC SERIAL LINE UNIT
DSKD: <BUZYNSKI>M3102A.DRW	02-NOV-82 09:58	NEXT HIGHER ASSEMBLY:	02-NOV-82	NUMBER
FIRST USED ON OPTION/MODEL:	DECSA	02-NOV-82	02-NOV-82	REV.
				A





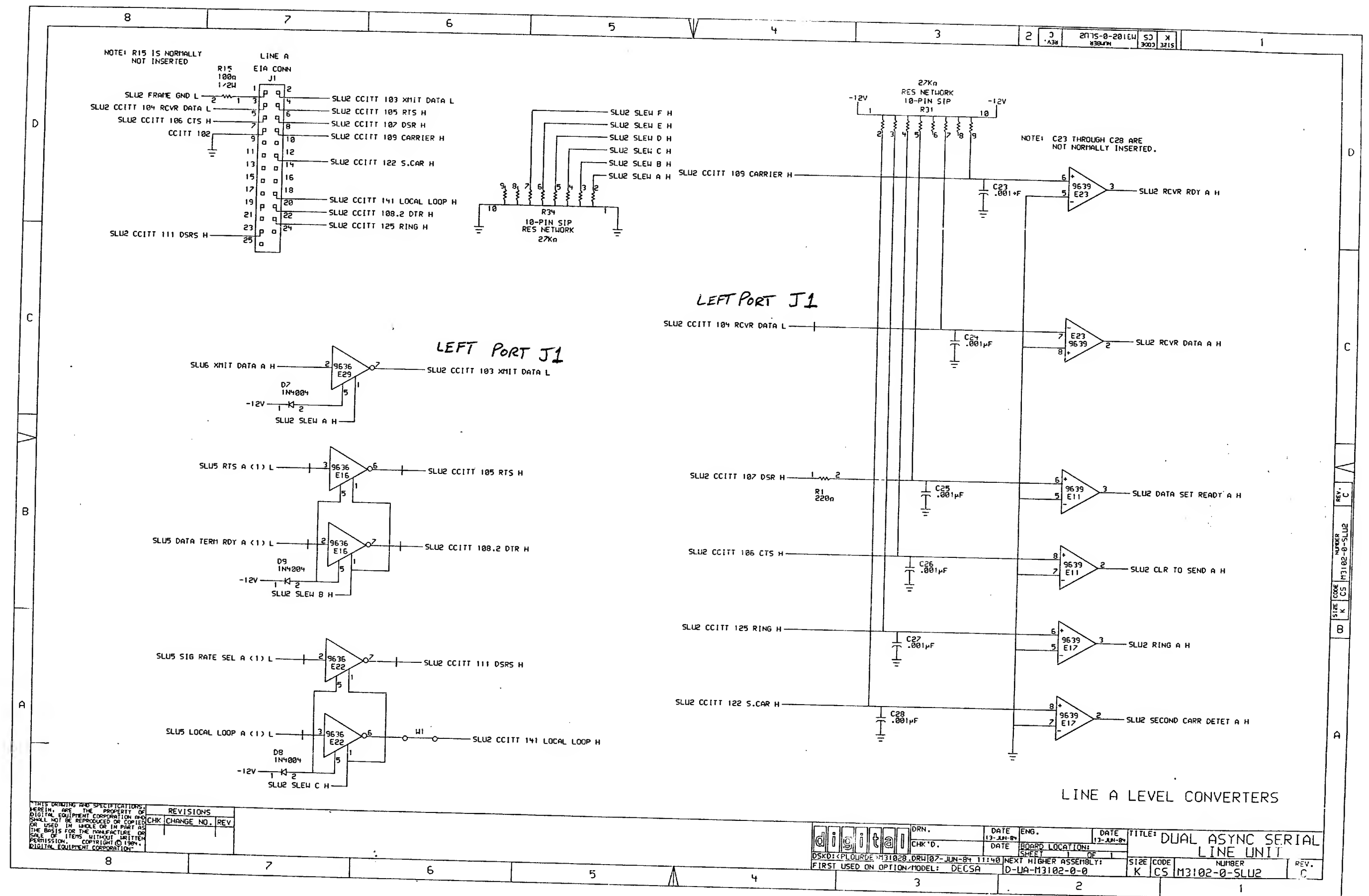
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BAUD RATE GEN



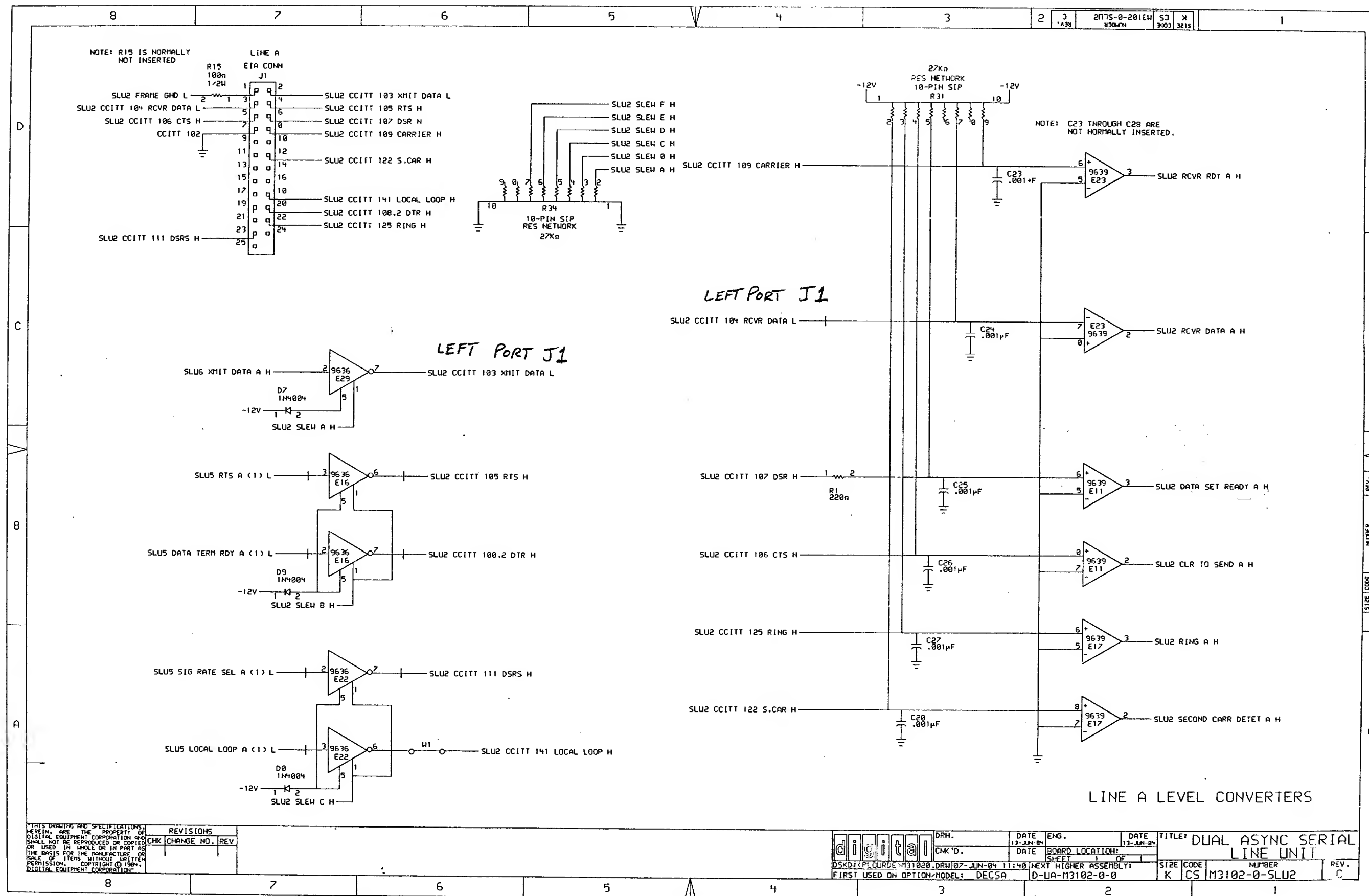
REG SELECT & BAUD RATE GEN

	DRN.	DATE 02-NOV-82	ENG.	DATE	TITLE: DUAL ASYNC SERIAL LINE UNIT				
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1						
DSKD: <BUZYNSKI> M3102A.DRW 02-NOV-82 09:58					NEXT HIGHER ASSEMBLY:				
FIRST USED ON OPTION/MODEL: DECSA					D-UA-M3102-0-0				
SIZE	CODE	NUMBER			REV.				
K	CS	M3102-0-SLU1			A				





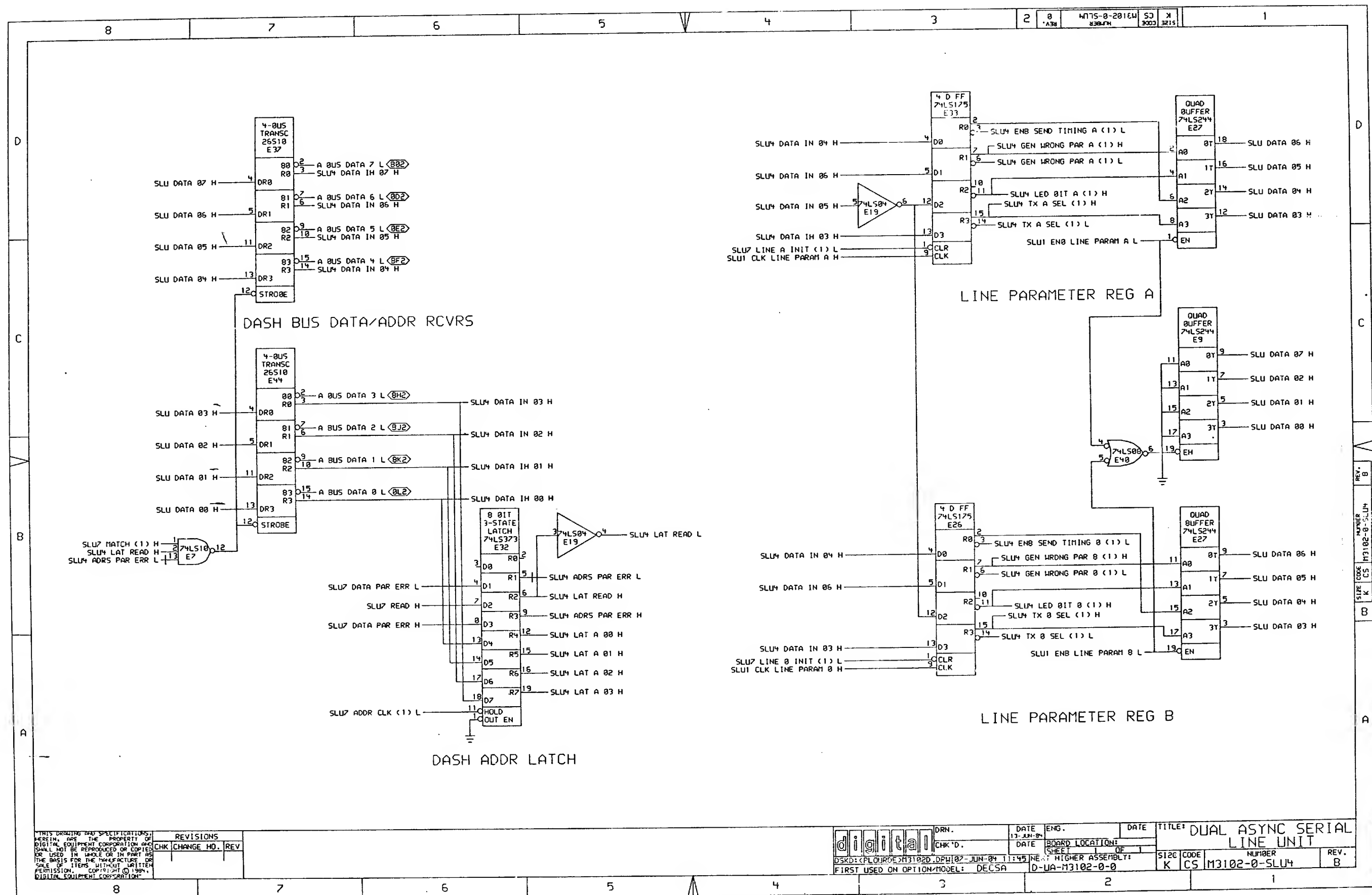










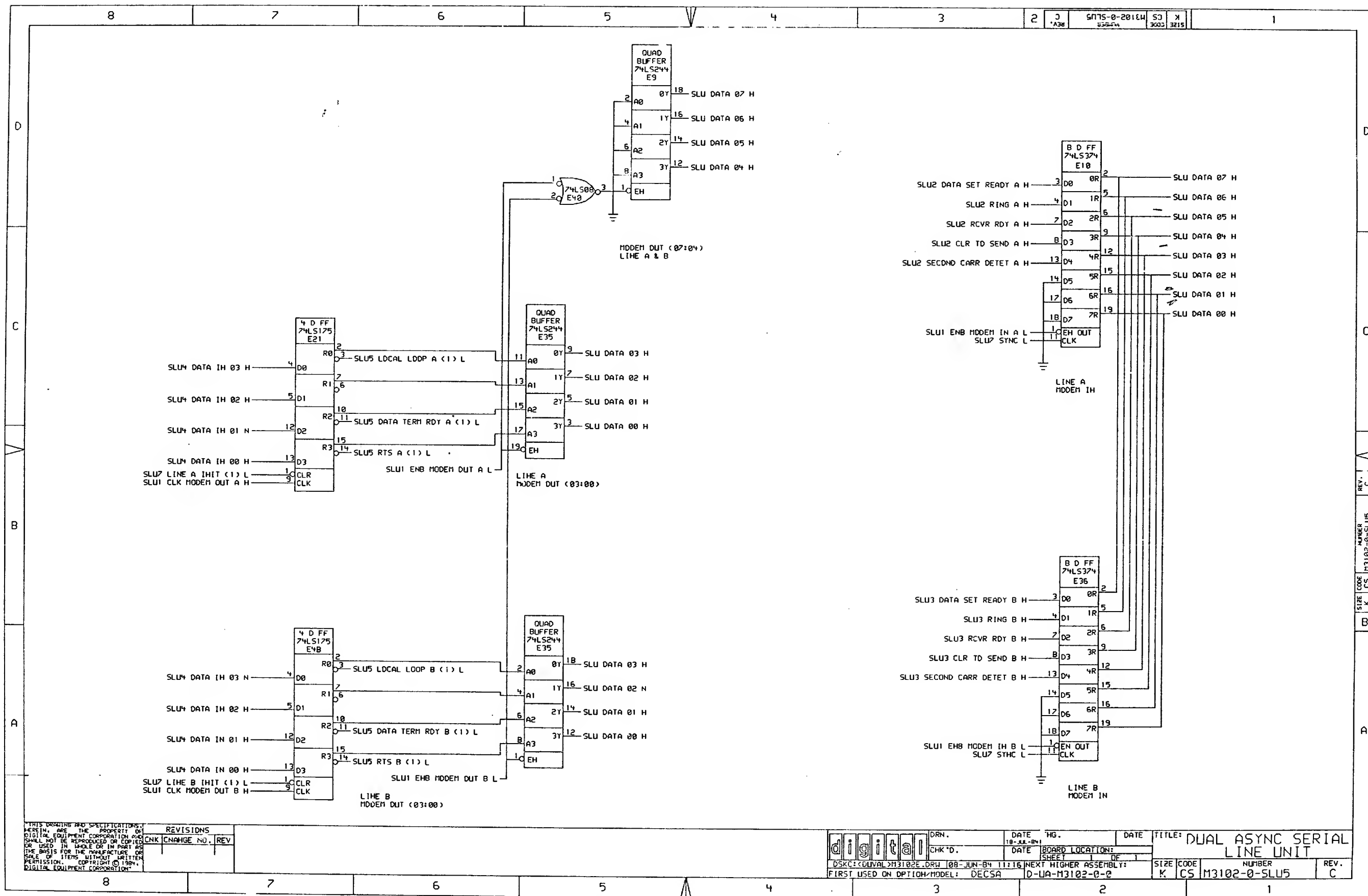


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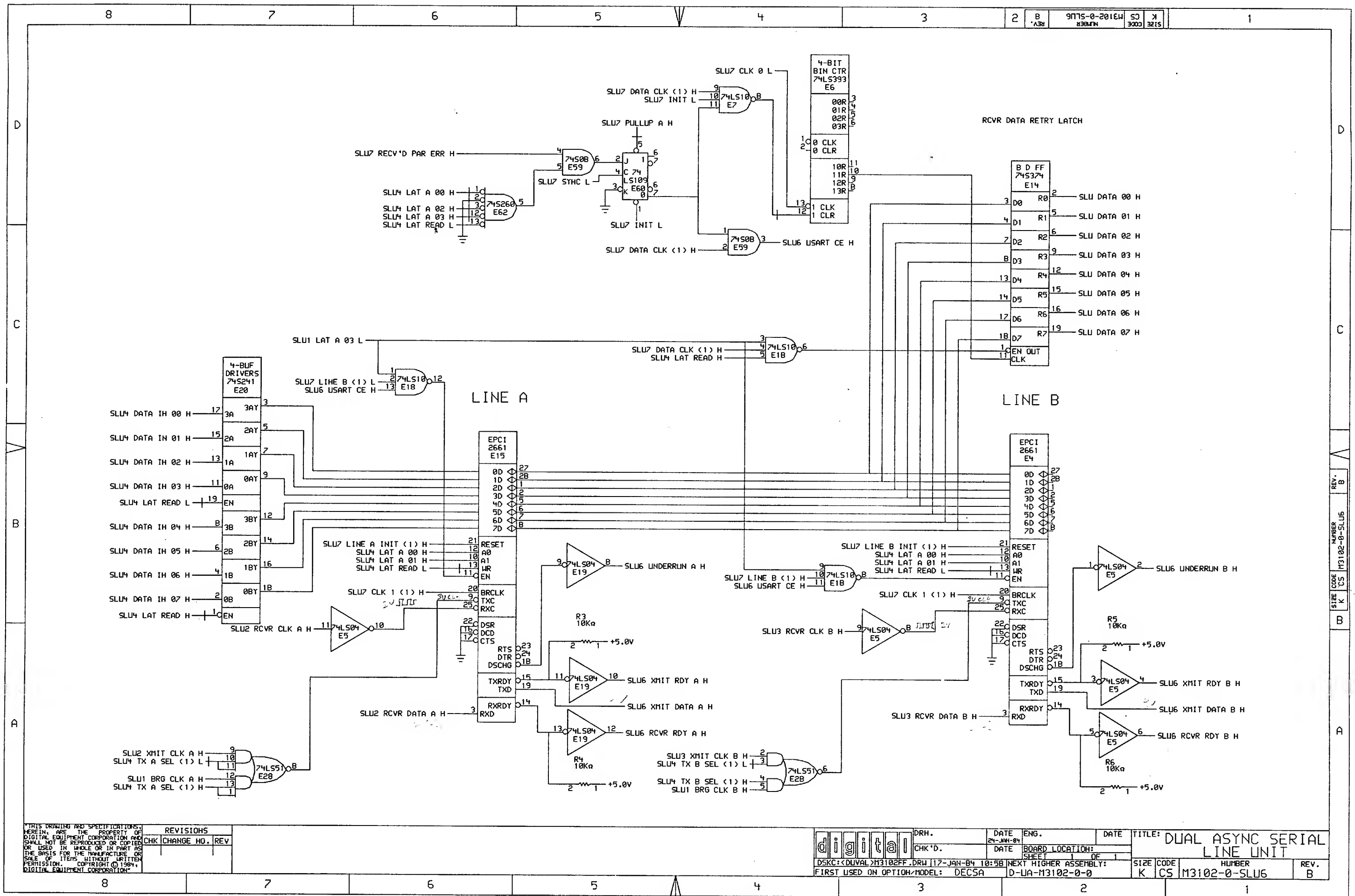
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN.	DATE	ENG.	DATE	TITLE: DUAL ASYNC SERIAL LINE UNIT	
	CHK'D.	DATE	BOARD LOCATION:	SHEET	SIZE CODE	NUMBER
DSKD: (PLOWRDE) M3102D.DPW (02-JUN-84 11:45) NEAR HIGHER ASSEMBLY:					K	CS M3102-0-SLU4
FIRST USED ON OPTION/MODEL: DEC5A						REV. B

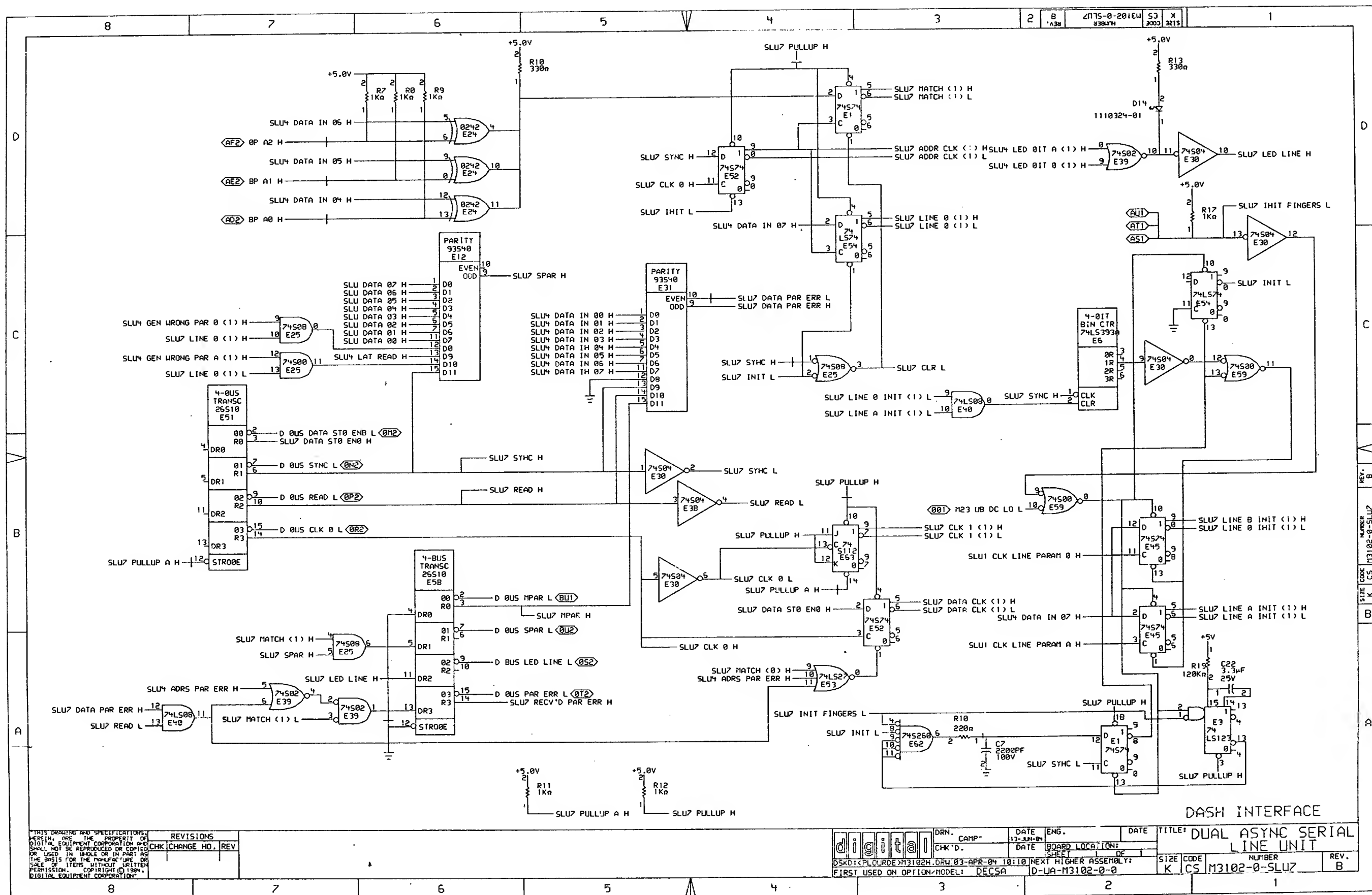




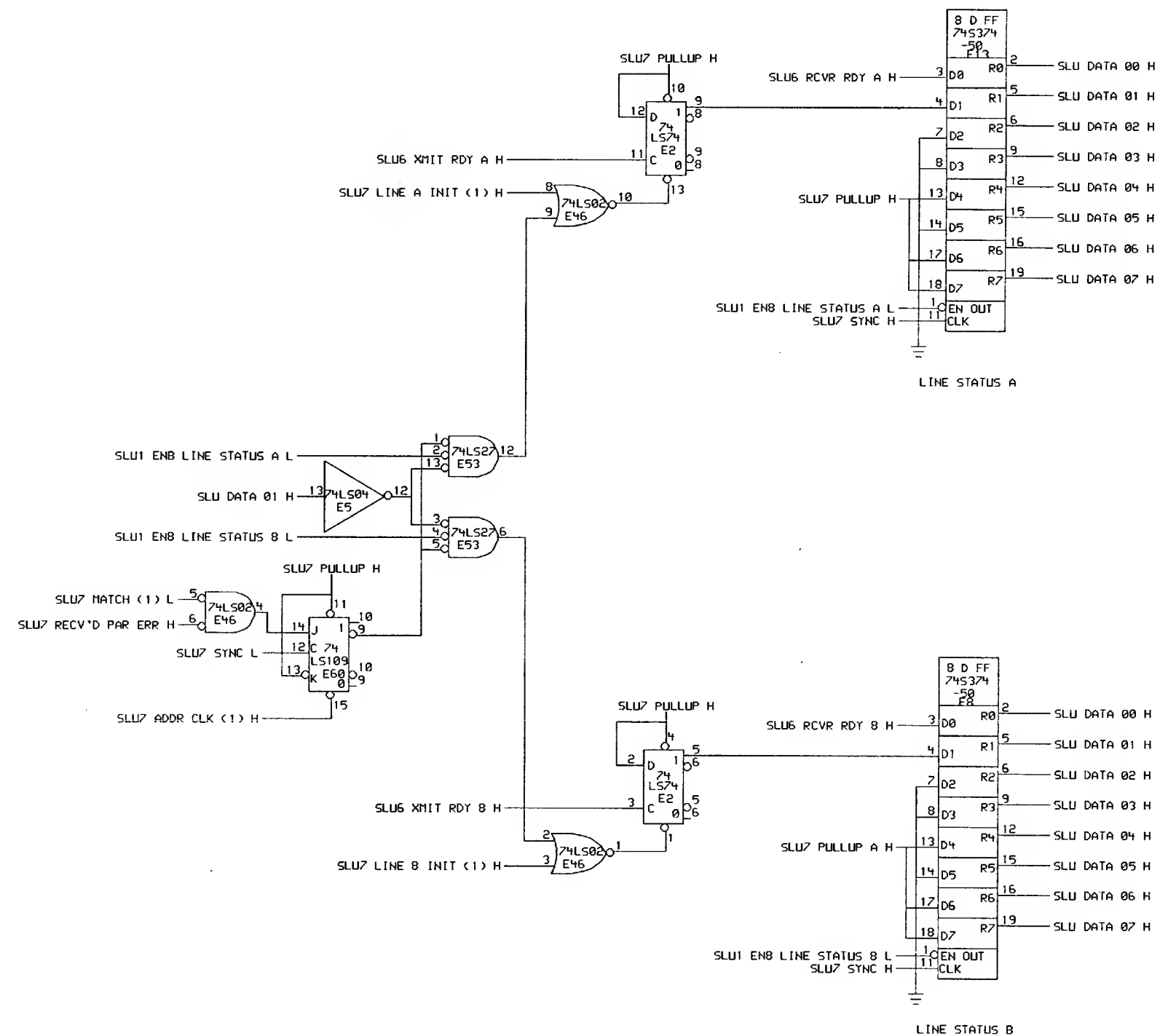








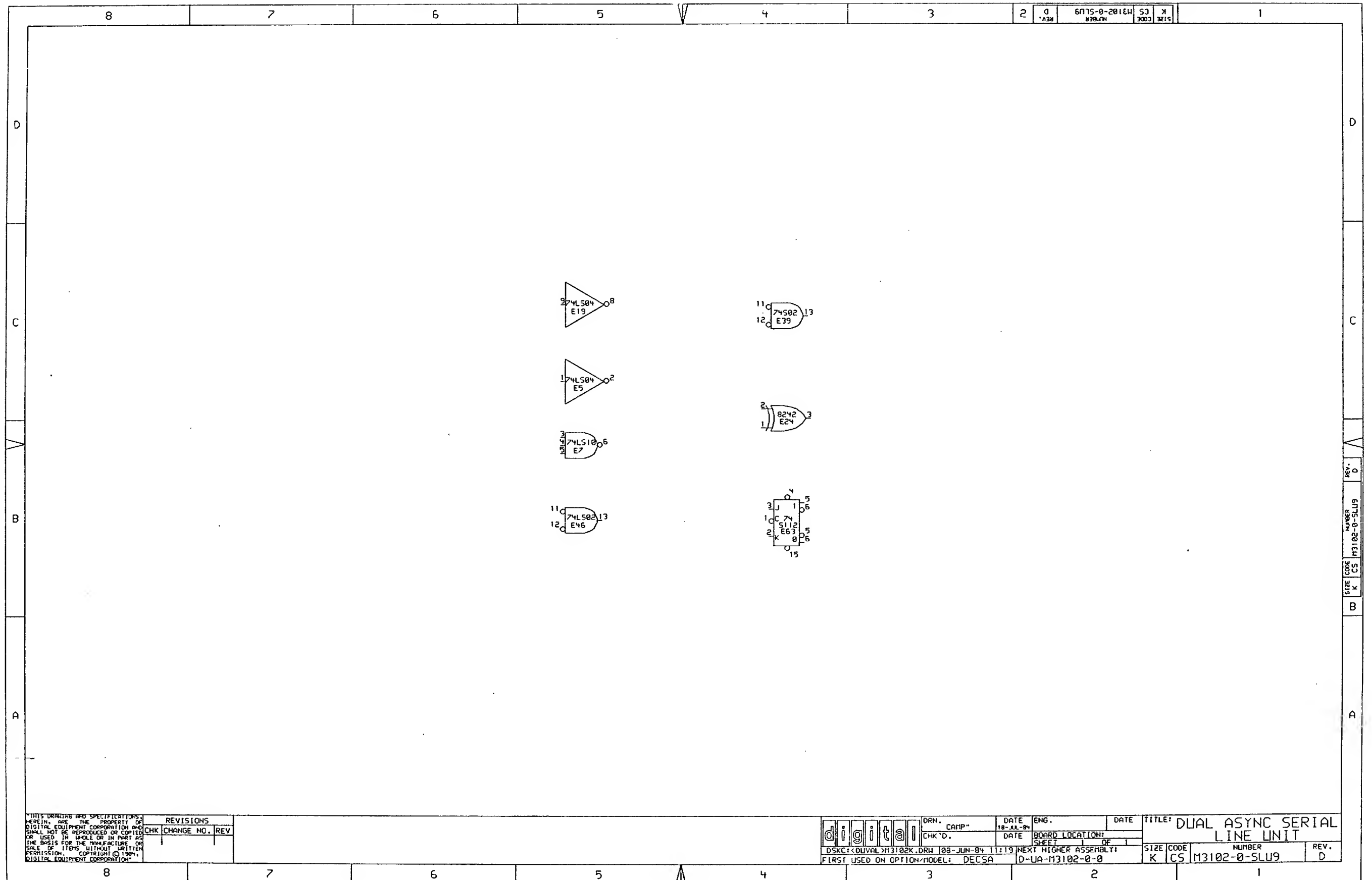




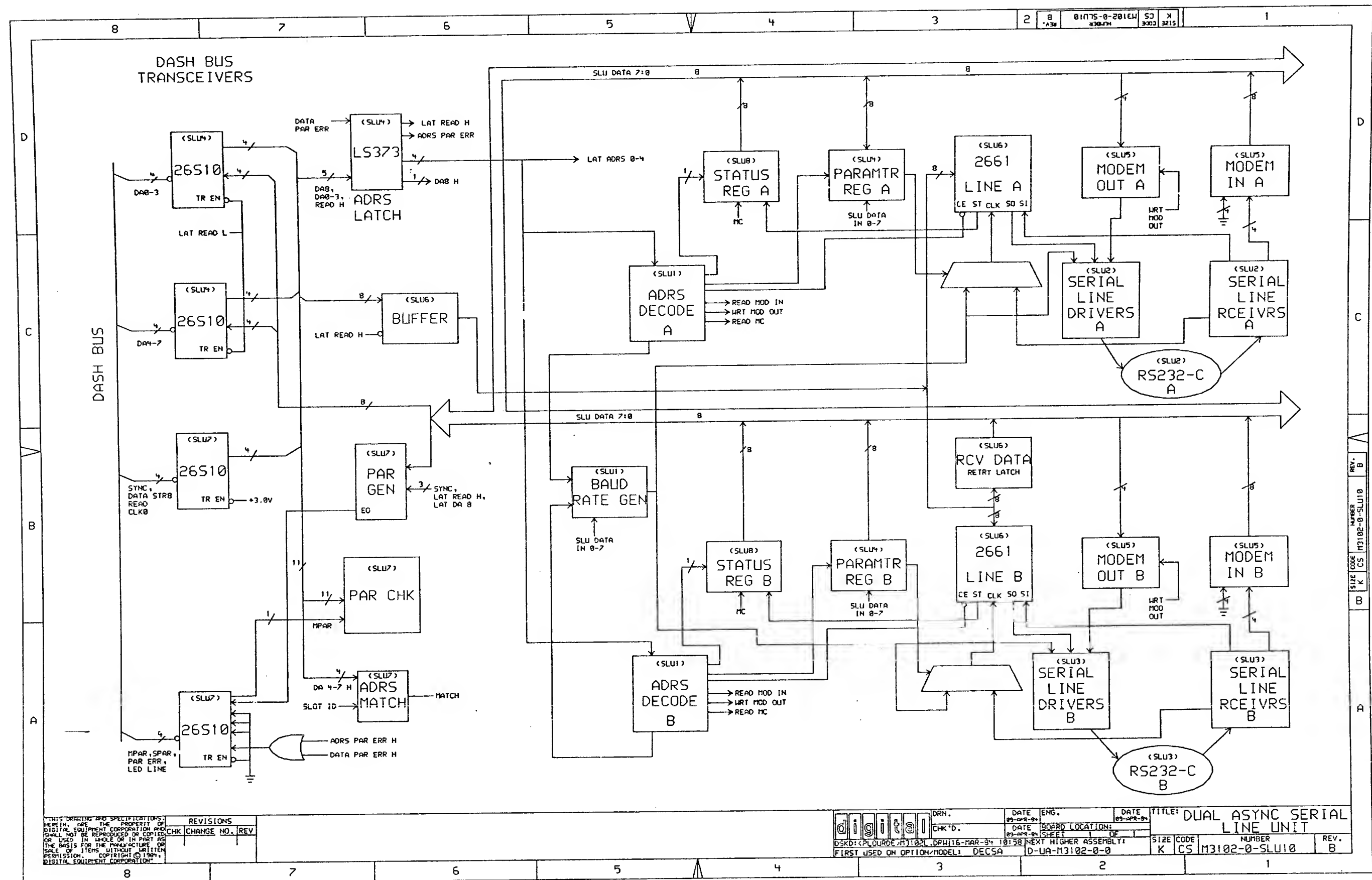
DRN.	DATE	ENG.	DATE	TITLE: DUAL ASYNC SERIAL LINE UNIT			
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	1	SIZE	CODE
DSKD:BUZYNSKI\M3102J.DRW\02-NOV-82 10:12				NEXT HIGHER ASSEMBLY:			
FIRST USED ON OPTION/MODEL: DECSA				D-UA-M3102-0-0			
				K	CS	M3102-0-SLU8	REV. A

TJ 1











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# DIGITAL EQUIPMENT CORPORATION PARTS LIST

QUANTITY / VARIATION

NOTES:

MADE BY A. ROCHA  
DATE 11 DECEMBER 80

CHECKED  
DATE *Ray H. H. 10/10/10*

SECTION

ENG  
DATE Ray Shuda 4/11/10

PROD	Ray Gruda
DATE	1.11.16

ISSUED SECTION	
----------------	--

ITEM NO.	DESCRIPTION	QTY	UNIT PRICE	TOTAL
1	...	...	...	...
2	...	...	...	...
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100	...	...	...	...

DRAWING NO.

PART NO.

DESCRIPTION

KDFLL-UA

REF DESIGNATION

1.

2.

B-PL-KDF11-UA-4

M7133

CPU MODULE

## SHIPPING LIST

11

REF

E.C.O. NO.

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TITLE
-------

## PARTS LIST

ASSY NO.	
----------	--

SHEET 1 OF 1

SIZE	
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B

CODE	DESCRIPTION	AMOUNT	DATE
1	...	...	...
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100	...	...	...

PL

**NUMBER**

KDF11-UA-0

REV.

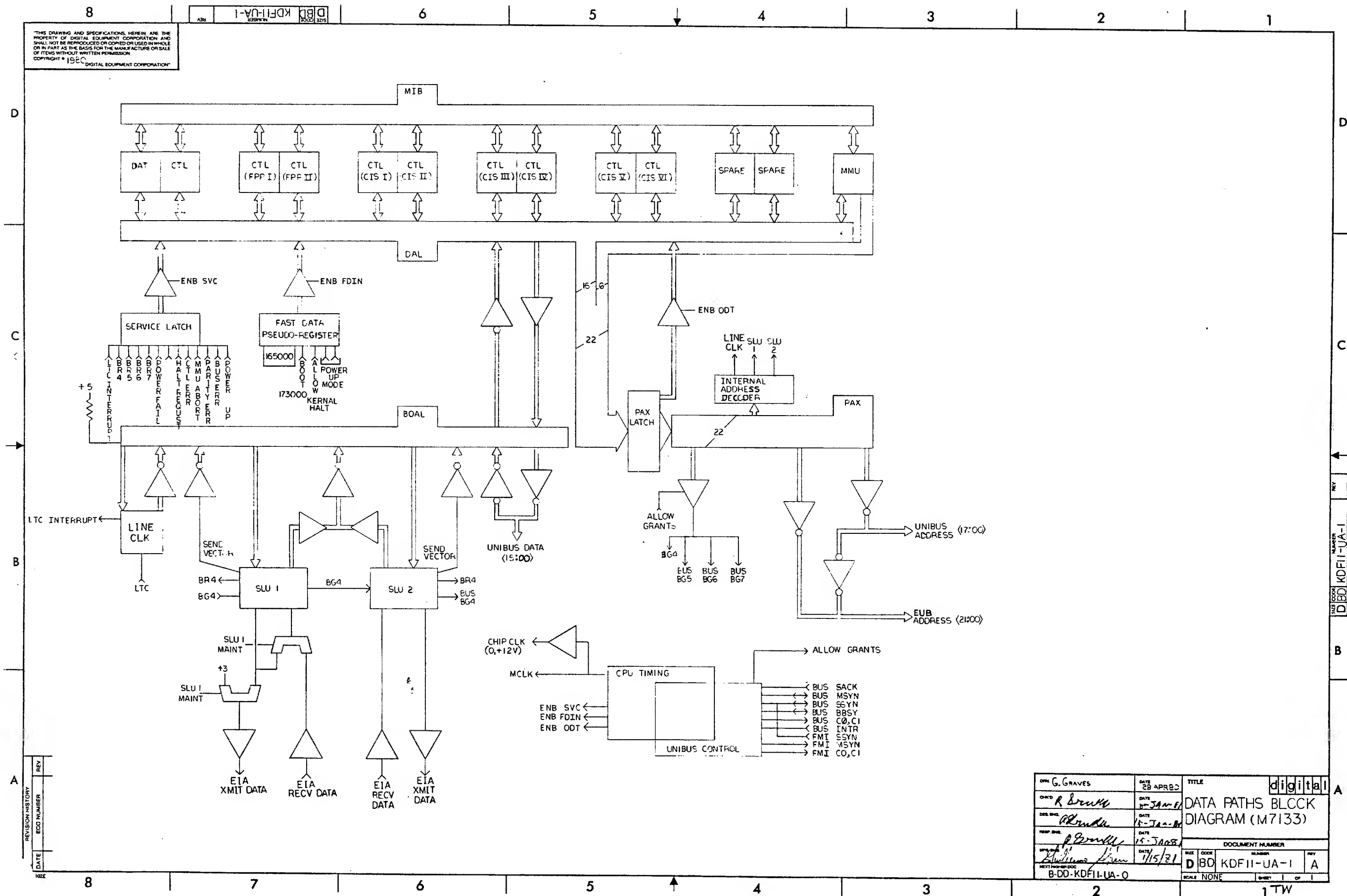
**A**

INSERTION PARTS LIST DATA BASE REV

**DRB 125**

TW







[illegible]



THIS DOCUMENT CONTAINS THE TRUTH TABLES AND EQUATIONS  
FOR THE ADDRESS DECODE PLA (E93 DEC PART# 23055C6-00) AND  
THE TIMING PLA (E39 DEC PART# 23064C6-00). IT SHOULD BE OF SOME  
HELP WHEN TRYING TO UNDERSTAND HOW THE ABOVE MENTIONED COMPONENTS  
INTERACT WITH THE M7133 COMBINATIONAL LOGIC.

REVISION HISTORY			BASIC PART NO: KOF11		DPN: RAY GRUDA		DATE: 09-MAR-82		DIGITAL	
ENG	ECO NUMBER	REV	NOTES:	CHK'D:	DATE:	TITLE	PARTS LIST			
	INITIAL	1A				KDF11-UA (1124)				
				DES.ENG.: RAY GRUDA	DATE: 09-MAR-82	PLA INFORMATION				
				RESP.ENG.: RAY GRUDA	DATE: 09-MAR-82	DOCUMENT NUMBER				
				MFG.ENG.: WALT BOWSE	DATE: 09-MAR-82	SIZE CODE NUMBER	REV			
				ASSEMBLY NUMBER:		K	GL	KDF11-UA-2	1	A
				ID-UA-M7133-0-0		TOP DOCUMENT NUMBER:				
						B-DD-KDF11-UA-1				
						FILE NAME: 1 SHEET				
						Z4230A.KGL	1 OF 11			

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ANDMATRIX

LCIMAA:AAAAAA:AAAA  
E PI10:  
A AN :  
D GT :  
L EL :

IIXEED:DD  
TT BB2:10  
AS WR :  
DS TD :  
DY LL :  
LN :  
L :

1 : ( 0)11 11 : 1  
0 00:000 00:000 ( 1)11 :  
0 00:0000 0:000 ( 2)11 :  
0 00:000001:100 ( 3)11 :  
1 01:000001:100 ( 4)11 :  
1 01:000010:000 ( 5)11 :  
10 00:000000:0000 ( 6) :1  
0 0 00:000000:000 ( 7) 1 :11  
0 0 00:000001:100 ( 8) 1 : 1  
0 0 00:000010:000 ( 9) 11: 1  
0 1 01:000001:100 ( 10) 1 :1  
0 1 01:000010:000 ( 11) 11:1  
000 00:000 00:000 ( 12) 1 1: 1  
001 01:000001:100 ( 13) 1 :  
001 01:000010:000 ( 14) 1 :  
110 00:000 00:0000 ( 15) 1 1: 1  
111 01:000001:1000 ( 16) 1 :1  
111 01:000010:0000 ( 17) 1 1:1

LCIMAA:AAAAAA:AAAA  
R00A11:987654:3210  
E PI10:  
L EL :

IIXEED:DD  
NN NNA:AA  
TT BB2:10  
DY LL :  
LN :  
L :

STATISTICS

NUMBER OF INPUTS = 16  
NUMBER OF OUTPUTS = 8  
NUMBER OF TERMS = 18  
"1" INPUT DETECTIONS = 41  
"0" INPUT DETECTIONS = 182  
DON'T CARES = 65  
NUMBER OF TERMS DRIVING MORE THAN ONE OUTPUT = 15

D	I	G	I	T	A	L	TITLE
							KDF11-UA (1124)
							PLA INFORMATION

KDF11-UA (1124)  
PLA INFORMATION

SHEET 3 OF 11

SIZE	CODE	DOCUMENT NUMBER	REV
K	GL	KDF11-UA-2	A











MNEMONIC VALUE (SPACE=DON'T CARE)

AIO2	1		
AIO1	1		
AIO0	1		
CT0	1		
CT1	1		
LREAD	1		
S	1		
MCLK	1		
ENBPBT4	1		
C0	1		
BUSREQ	1		
PAUSE	1		
BUSCYC	1		
PT2_4	1		
MMUCYC	1		
FSMAINT	1		
COUNT0	00		
COUNT1	10		
COUNT2	01		
COUNT3	11		
AWO	000	0	1
ARW	001	0	1
ARO	011	0	1
DOUT0	100	0	1
DOUT	101	0	1
DIN	110	0	1
NOP	111	0	1
DATO	10	0	1

D	I	S	I	T	A	L	TITLE	SHEET	OF	11	SIZE	CODE	DOCUMENT NUMBER	REV
							KDF11-UA (1124) PLA INFORMATION						KDF11-UA-2	A







STATISTICS

NUMBER OF INPUTS = 16  
NUMBER OF OUTPUTS = 9  
NUMBER OF TERMS = 34  
"1" INPUT DETECTIONS = 110  
"0" INPUT DETECTIONS = 72  
DON'T CARES = 362  
NUMBER OF TERMS DRIVING MORE THAN ONE OUTPUT = 0

D	I	G	I	T	A	L	TITLE		SHEET 8 OF 11	SIZE	CODE	DOCUMENT NUMBER	REV
							KDF11-UA (1124)			K	GL	KDF11-UA-2	A
							PLA INFORMATION						







## TIMING DIAGRAMS AND DEFINITIONS

"MCLK, ENBPPT4, BUSCYC AND FS MAINT INPUT SIGNALS ARE ASSERTED LOW

"PT1= PHASE TIME ONE

"PT2=PHASE TIME TWO (USED FOR AIO DECODE)

"PT3=PHASE TIME THREE (USED IN BUS DATA CYCLES, THE OSC CLOCK TURNS OFF , GETS RESTARTED BY SSYN)

"ALSO USED IN ADDRESS AND LOCAL DATA CYCLES.

"PT4=PHASE TIME FOUR (USED ONLY IN A DATA IN CYCLE FOR DATA DESKEW)

"PBT1=PHASE BAR TIME ONE

"PBT2=PHASE BAR TIME TWO

"PBT3=PHASE BAR TIME THREE (RELOCATED ADDRESS STROBED , IF MMU CYCLE)

"PBT4=PHASE BAR TIME FOUR

"            S T A N D A R D        3 0 0 N S .        C Y C L E  
"            PBT2                            PBT1 , PBT2                            PBT1  
"            I PT1 , PT2 I                            I PT1 , PT2 I                            PBT1

"            K T        R E L O C A T I O N        C Y C L E  
"            PBT2                            PBT1 , PBT2 , PBT3 , PBT4                            PBT1  
"            I PT1 , PT2 , PT3 I                            I PT1 ,

"            K T        R E L O C A T I O N        C Y C L E W I T H        A B O R T  
"            PBT2                            PBT1 , PBT2 , PBT3 , PBT4                            PBT1  
"            I PT1 , PT2 , PT3 I                            I PT1 ,

"            S T A N D A R D        A D D R E S S        C Y C L E  
"            PBT2                            PBT1 , PBT2                            PBT1  
"            I PT1 , PT2 , PT3 I                            I PT1 ,

"            D A T A        O U T ( B )        C Y C L E  
"            O R        L O C A L        D O U T ( B )  
"            PBT2                            PBT1 , PBT2                            PBT1  
"            I PT1 , PT2 , PT3 I                            I PT1 , PT2 I                            PBT1

"            D A T A        I N        C Y C L E  
"            PBT2                            PBT1 , PBT2                            PBT1  
"            I PT1 , PT2 , PT3 , PT4 I                            I PT1 , PT2 I                            PBT1

TITLE										SHEET. 10 OF 11				DOCUMENT NUMBER				REV	
D	I	G	I	T	A	I	L							K	GL	KDF11-UA-2		A	
KDF11-UA (1124)																			
PLA INFORMATION																			

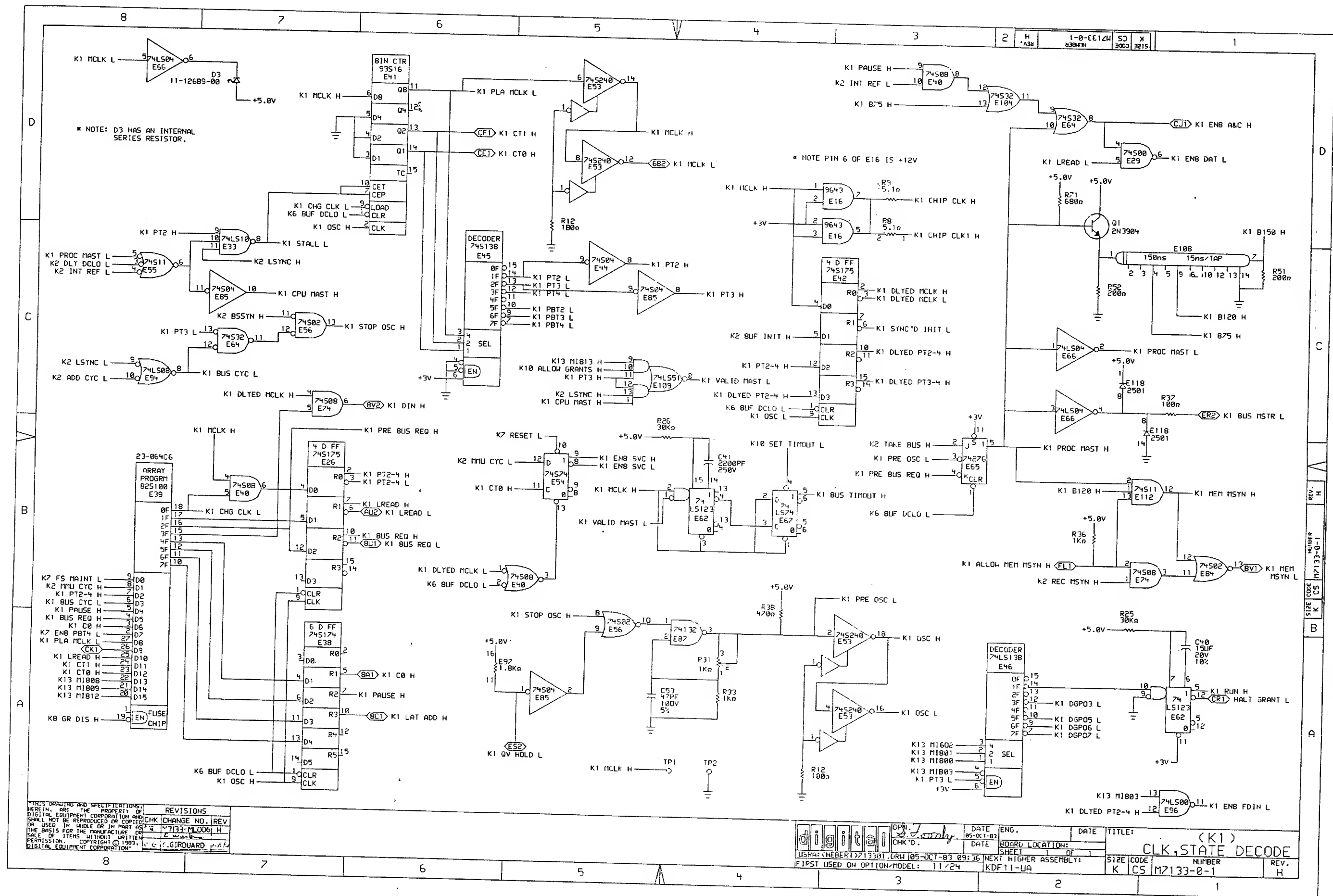




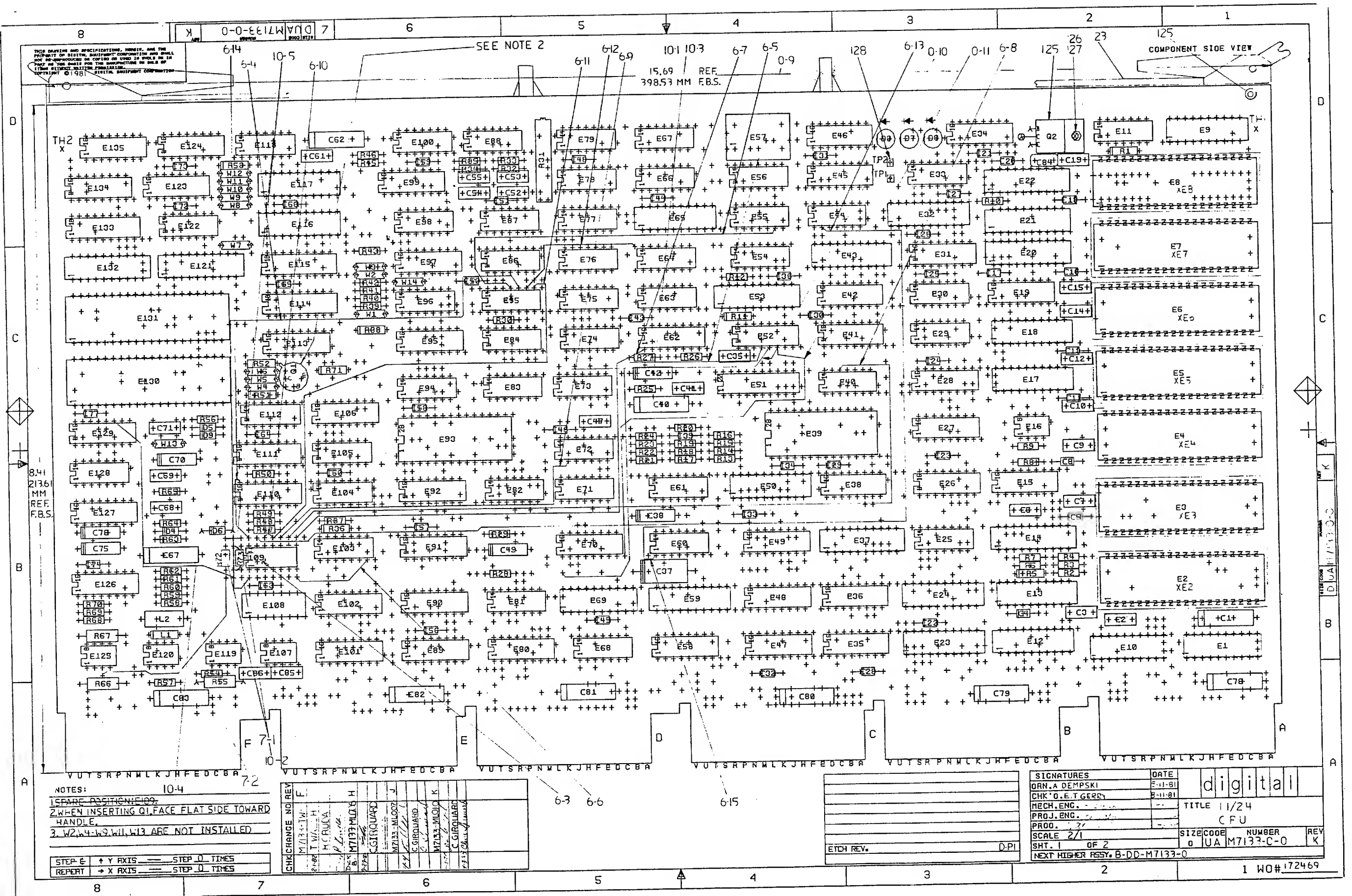


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REWORK INSTRUCTIONS  
WIRE ADDS SIDE 1

- ~~0-6 FROM E67-2 TO E77-2~~  
~~0-7 FROM E67-1 TO E45-10~~  
~~0-8 FROM E103-3 TO E84-6~~  
 0-9 FROM E52-4 TO PTH BELOW E70-1.  
 0-10 FROM E52-5 TO E73-13.  
 0-11 FROM E52-6 TO PTH TO THE RIGHT OF E70-8.  
 ECO M7133-ML006 COMPONENT ADD SIDE1  
 6-3 E109(RIGHT AND INNERMOST PTHS.)  
 WIRE ADDS SIDE1  
 6-4 FROM E85-15 TO E109-14(AT PTH BELOW E109-10).  
 6-5 FROM E85-11 TO E55-6  
 6-6 FROM E103-7 TO E109-7(AT PTH BELOW E109-7).  
 6-7 FROM E62-1 TO E109-8(AT PTH ABOVE E109-3).  
 6-8 FROM E40-12 TO E109-9(AT PTH ABOVE E109-9).  
 6-9 FROM E72-2 TO E109-10(AT PTH ABOVE E109-10).  
 6-10 FROM E109-11(AT PTH ABOVE E109-10) TO E11-9-12  
 (AT PTH ABOVE E109-12).  
 6-11 FROM E85-8 TO E109-11(AT PTH ABOVE E109-11).  
 6-12 FROM E85-9 TO E64-13(AT PTH ON LEFT OF E64-14).  
 6-13 FROM E44-2 TO E109-13(AT PTH ABOVE E109-13).  
 6-14 FROM E110-16 TO E109-14(AT PTH ABOVE E109-14).  
 6-15 FROM E63-12 TO E60-1.

## REWORK INSTRUCTIONS

- 7-1 COMPONENT ADD: 7-1 R72 TO FORMER EI09 PIN 1  
AND PIN 20
- 7-2 WIRE ADD: 7-2 FROM PTH BELOW R72 PIN 1 TO PTH  
BELOW EI25 PIN 3

## REWORK INSTRUCTIONS ECO 010

COMPONENT DELETE

10-1 DELETE C42

DELETE C42  
COMPONENT ADDS

10.2 R73 TO FORMER E109 PINS 2 AND 19

10-3 C42 TO FORMER C42 PINS 1 AND 2

110-4 FROM R73 PIN 1 TO E129 PIN 11

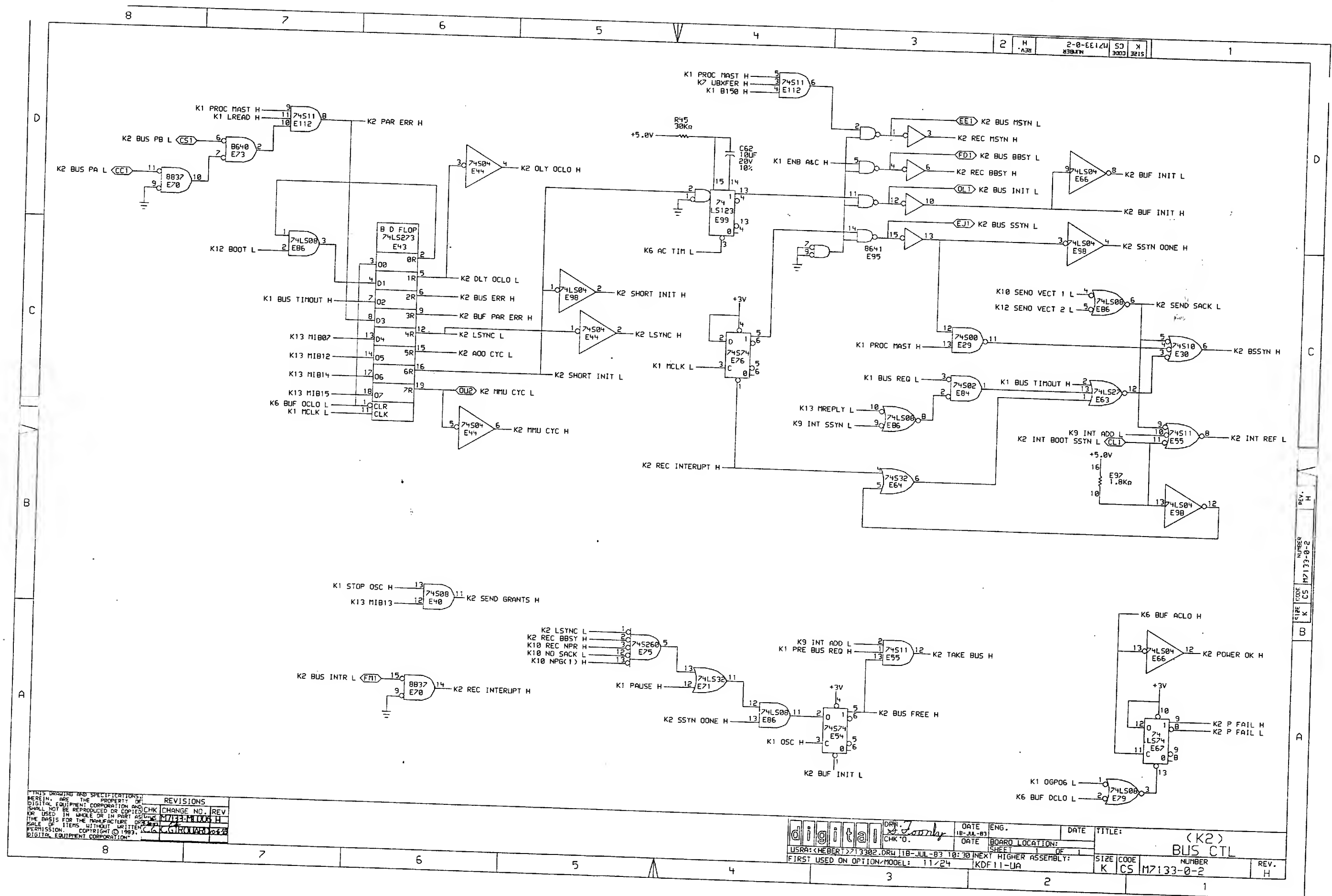
10.5 FROM R78 PIN2 TO FORMER EI09 PIN 20

WIRE ADDS

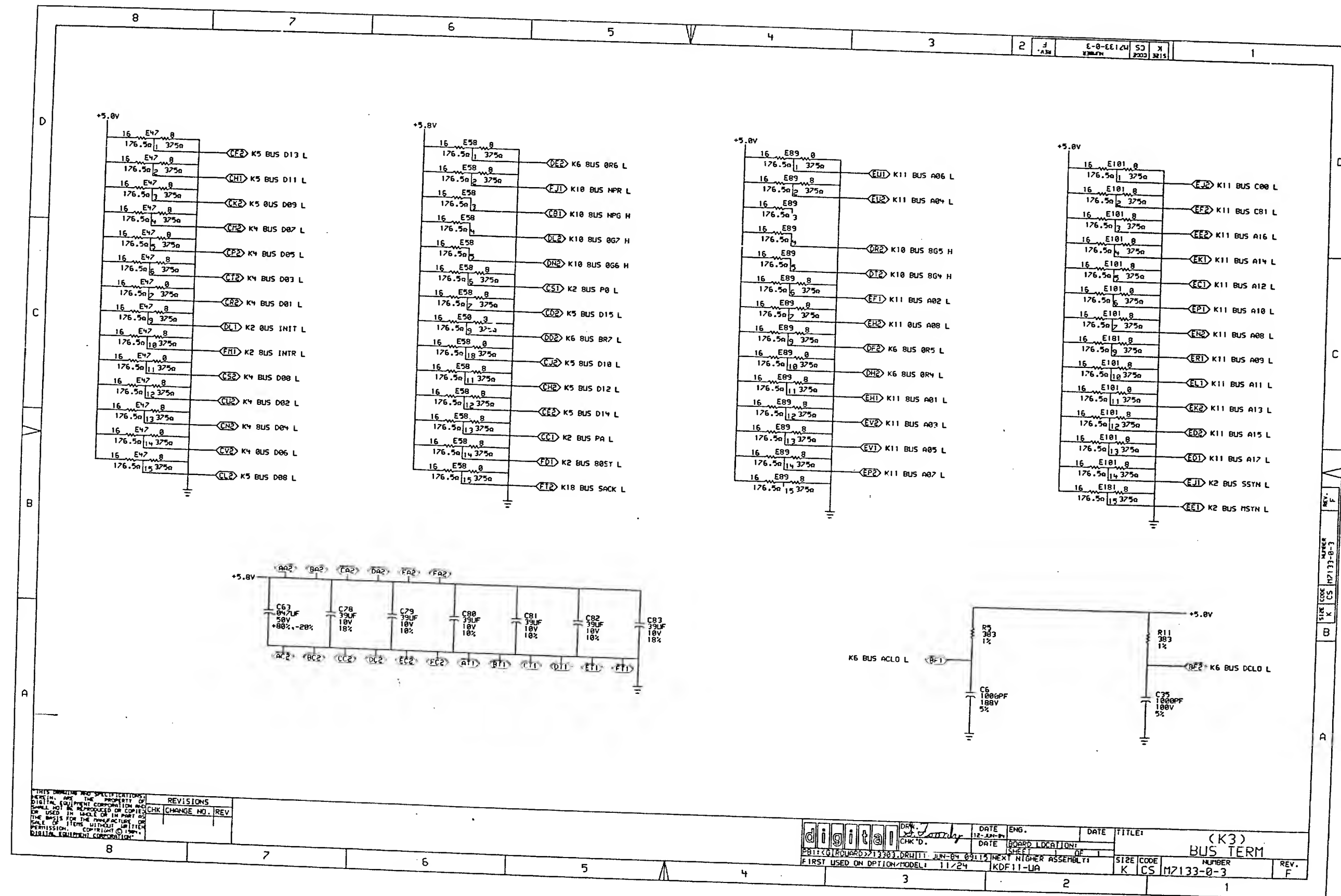
REVISION HISTORY		
DATE	ECO NUMBER	REV

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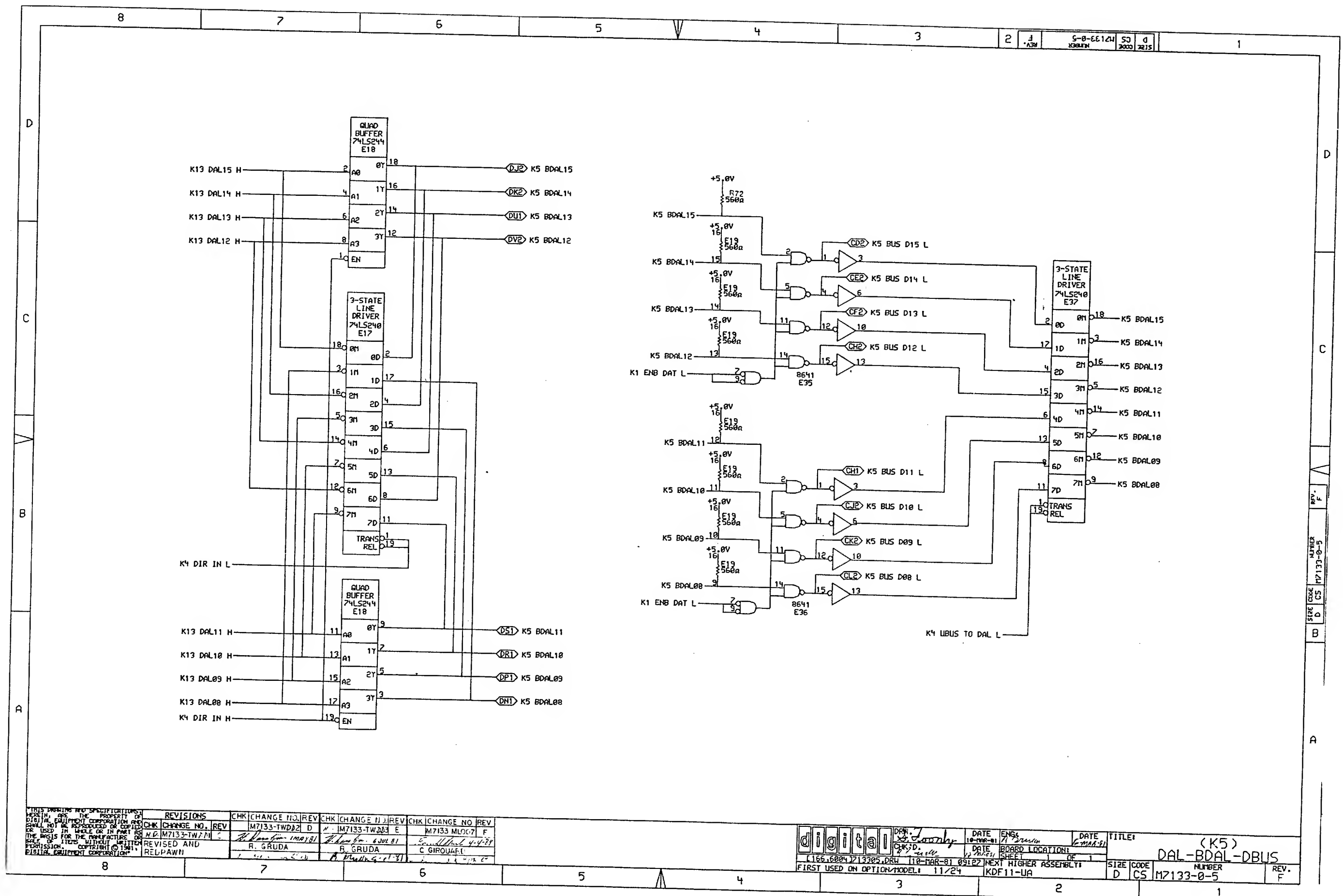
REVISIONS		
CHK	CHANGE NO.	REV

<b>digital</b>		DATE	ENG.	DATE	TITLE
P811C6104000713303.DR1111 JUN-84 09114		12-24-84			(K3) BUS TERM
FIRST USED ON OPTION/MODEL: 11/24		DATE	BOARD LOCATION	SHEET	
NEXT HIGHER ASSEMBLY: KDF11-UA					
SIZE	CODE	NUMBER	REV.		
K	CS	M7133-0-3	F		





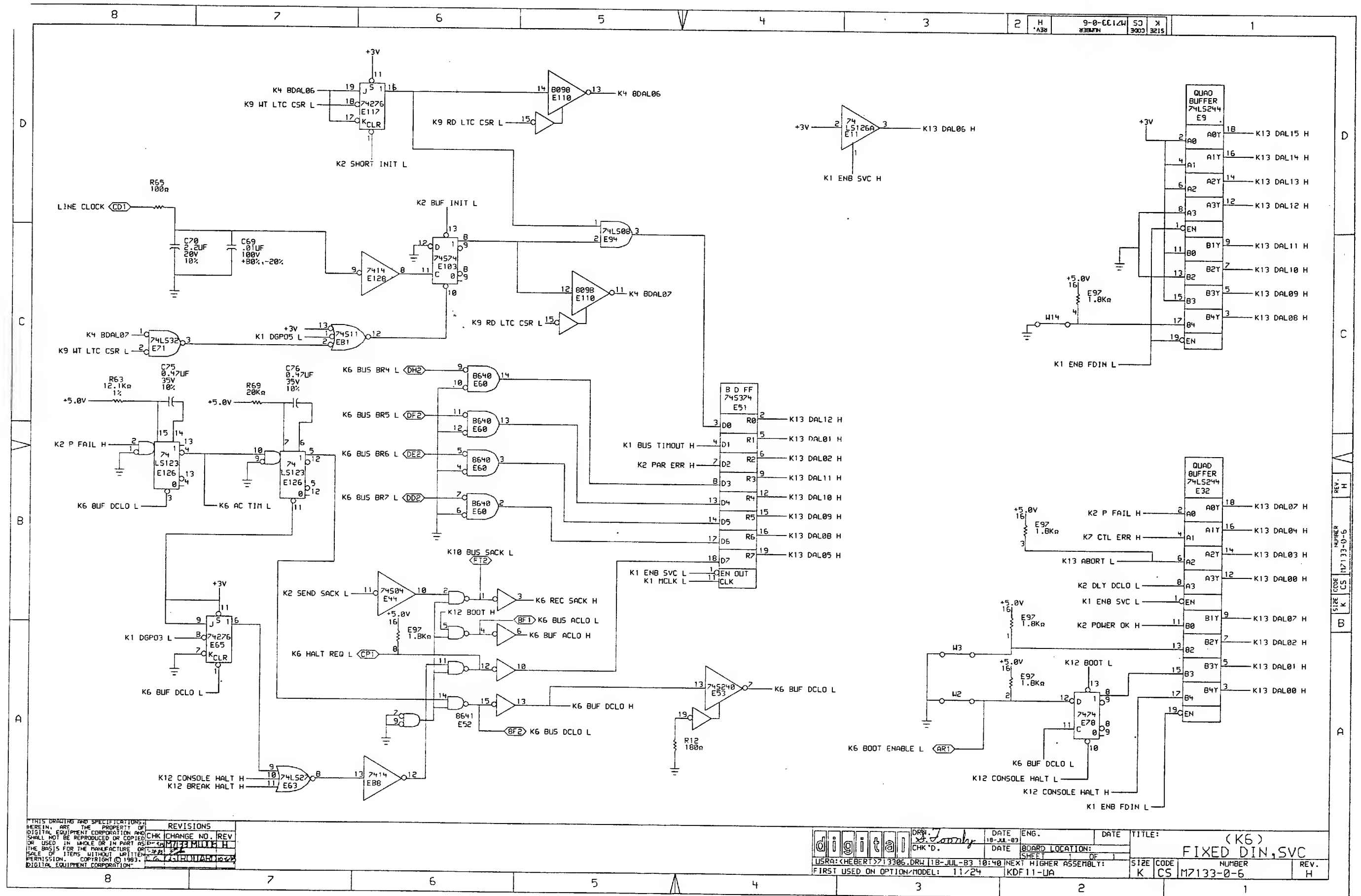




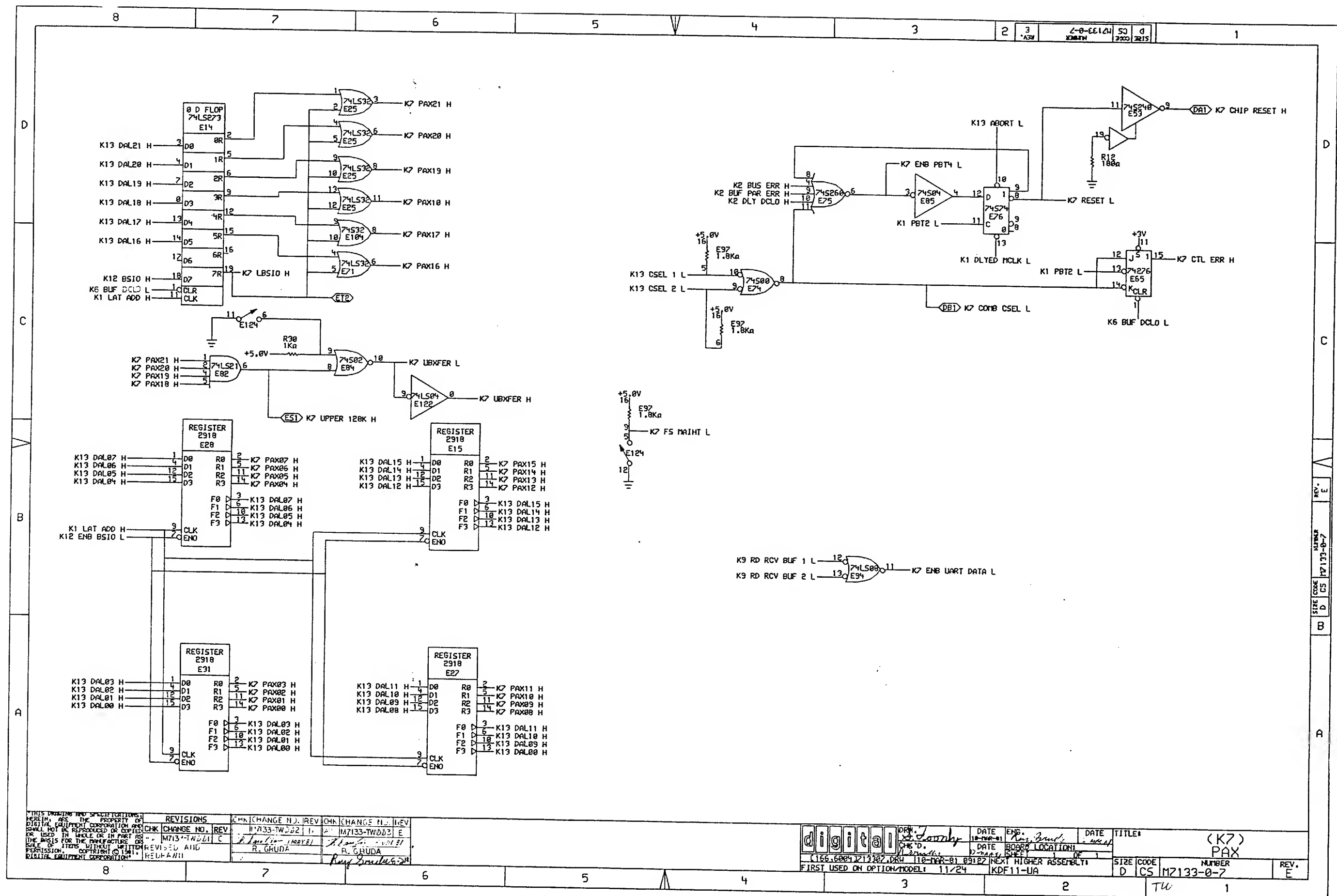
REVISIONS			
CHK	CHANGE NO.	REV	DESCRIPTION
M7133-TW002	D	1	INITIAL DESIGN
M7133-TW003	E	1	REVISION 1
M7133-TW004	F	1	REVISION 2

digital	DATE: 10-10-81	ENGINEER: J. L. L. L.	DATE: 10-10-81	TITLE: (K5) DAL-BDAL-DBUS
1155.6004.213305.DRW	110-MAR-81	09:27	NEXT HIGHER ASSEMBLY: KDF11-UA	SIZE: D CS M7133-0-5
FIRST USED ON OPTION MODEL: 11/24				REV: F

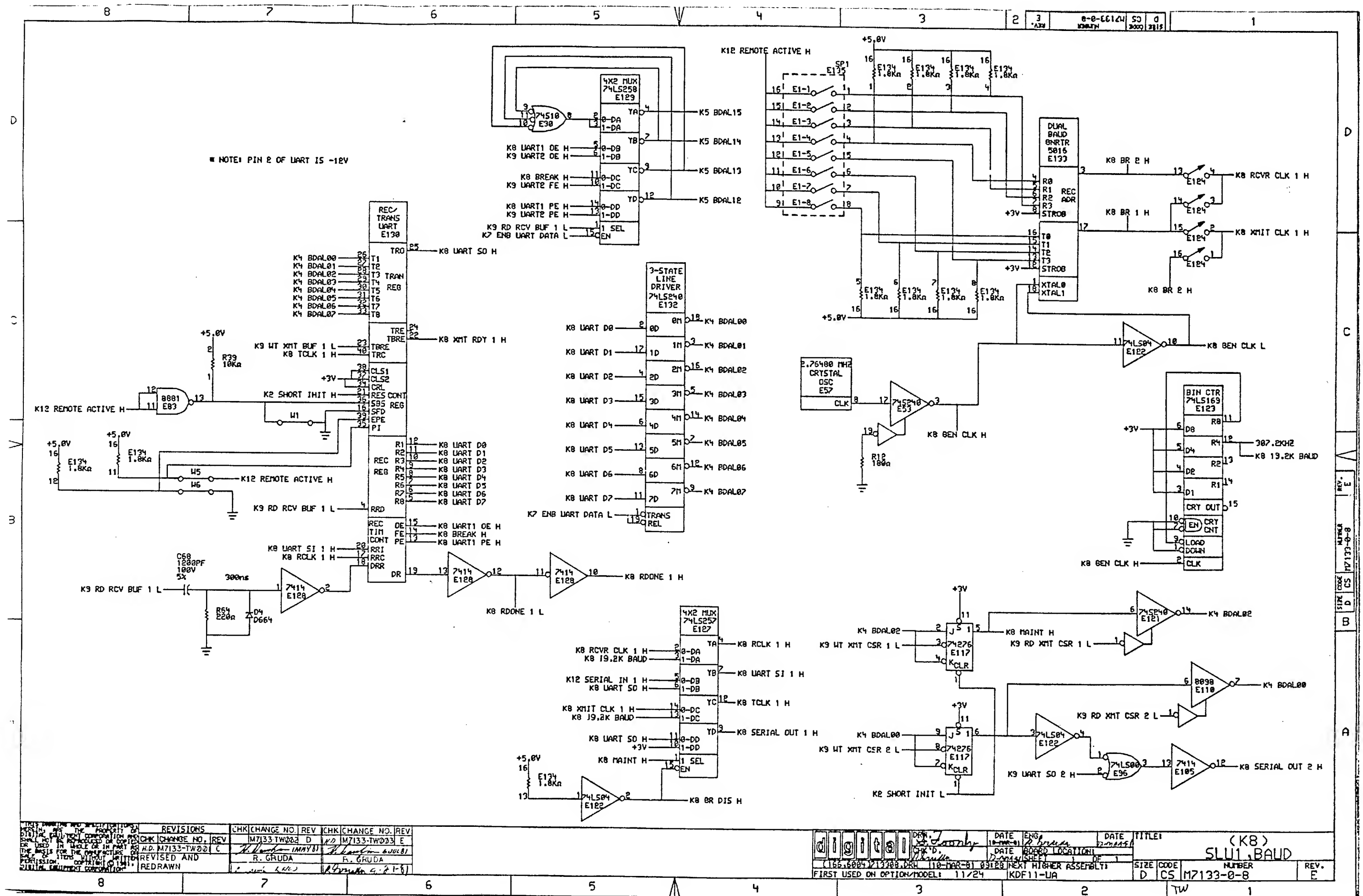




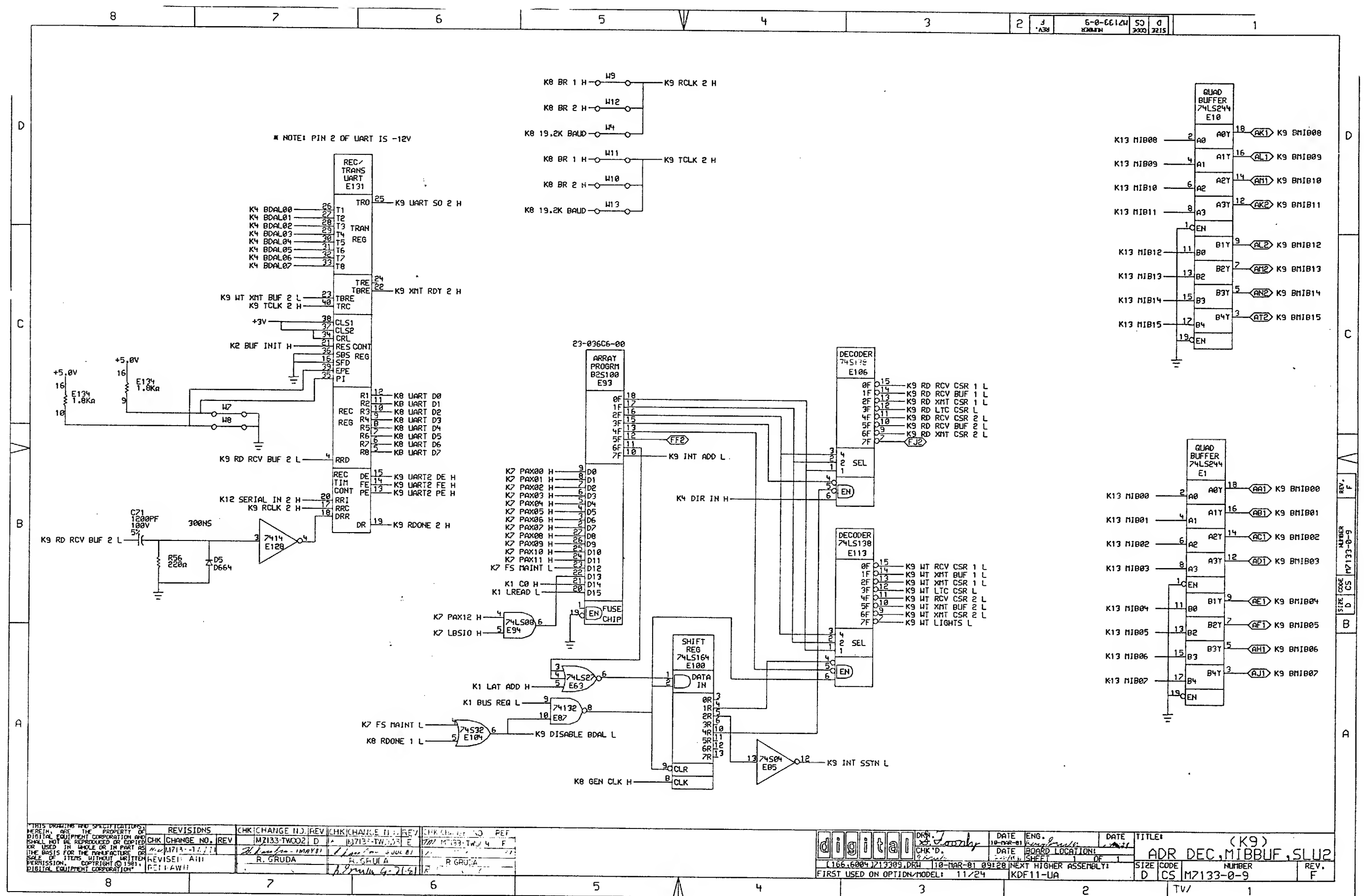




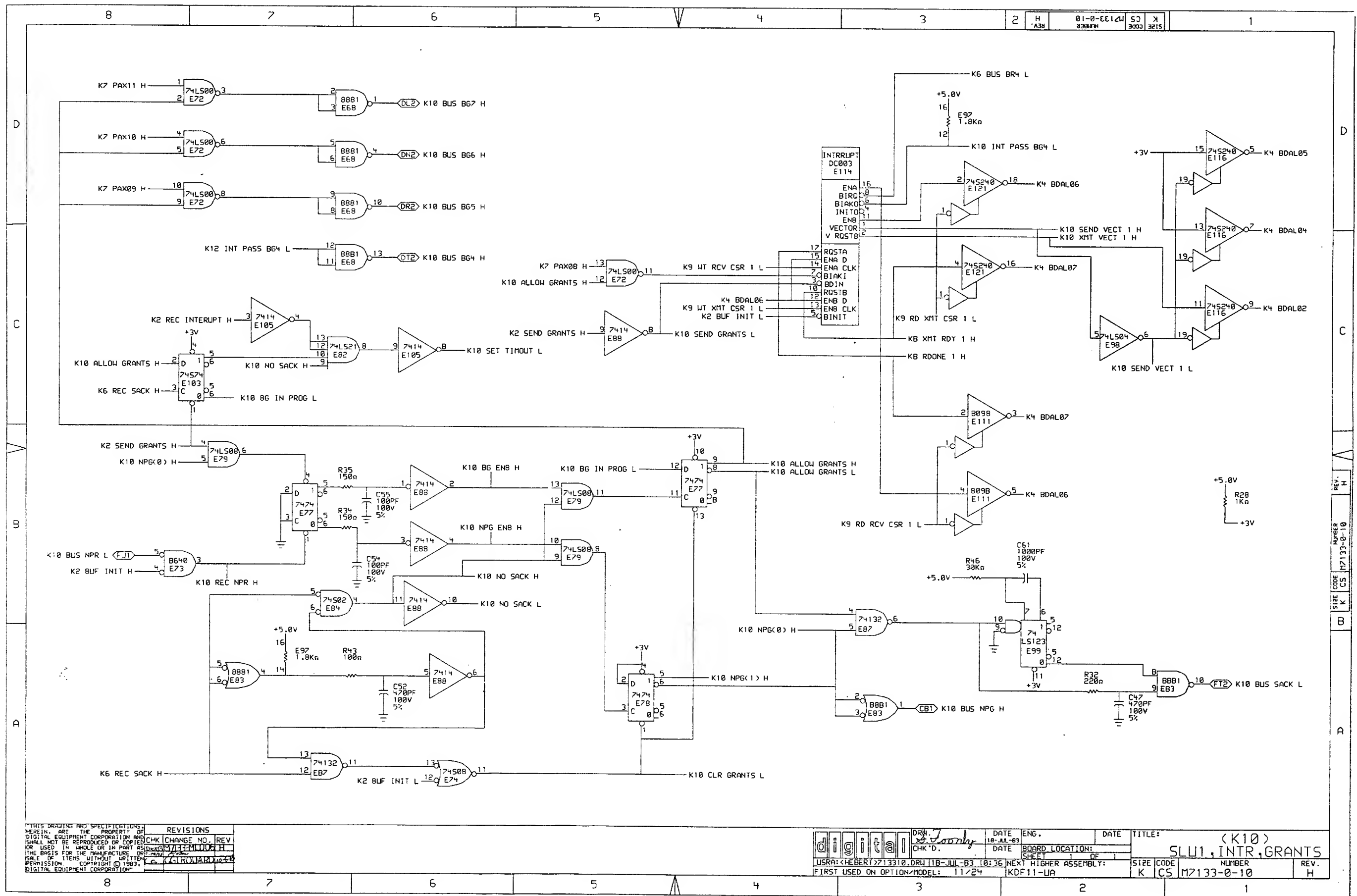




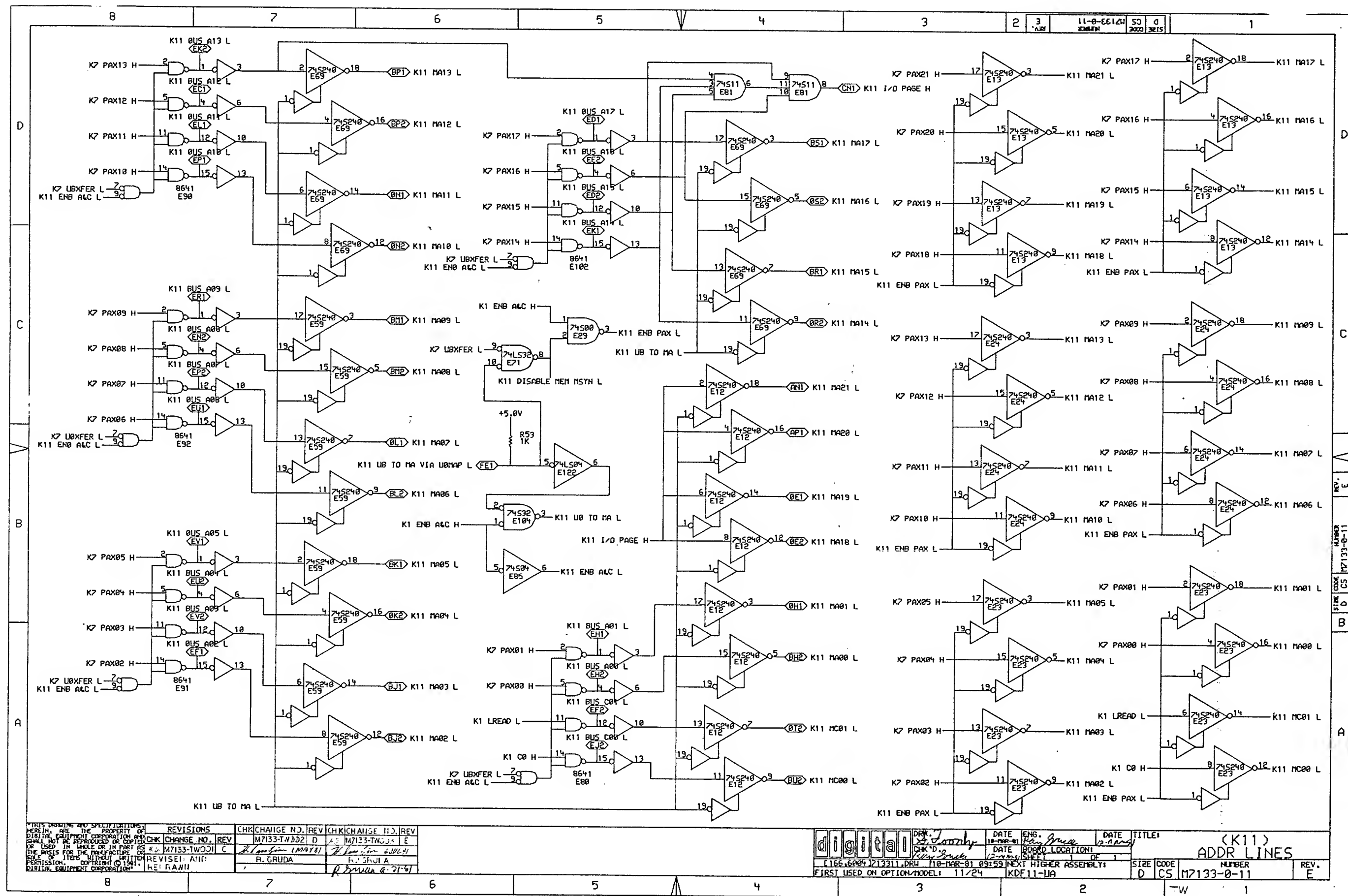




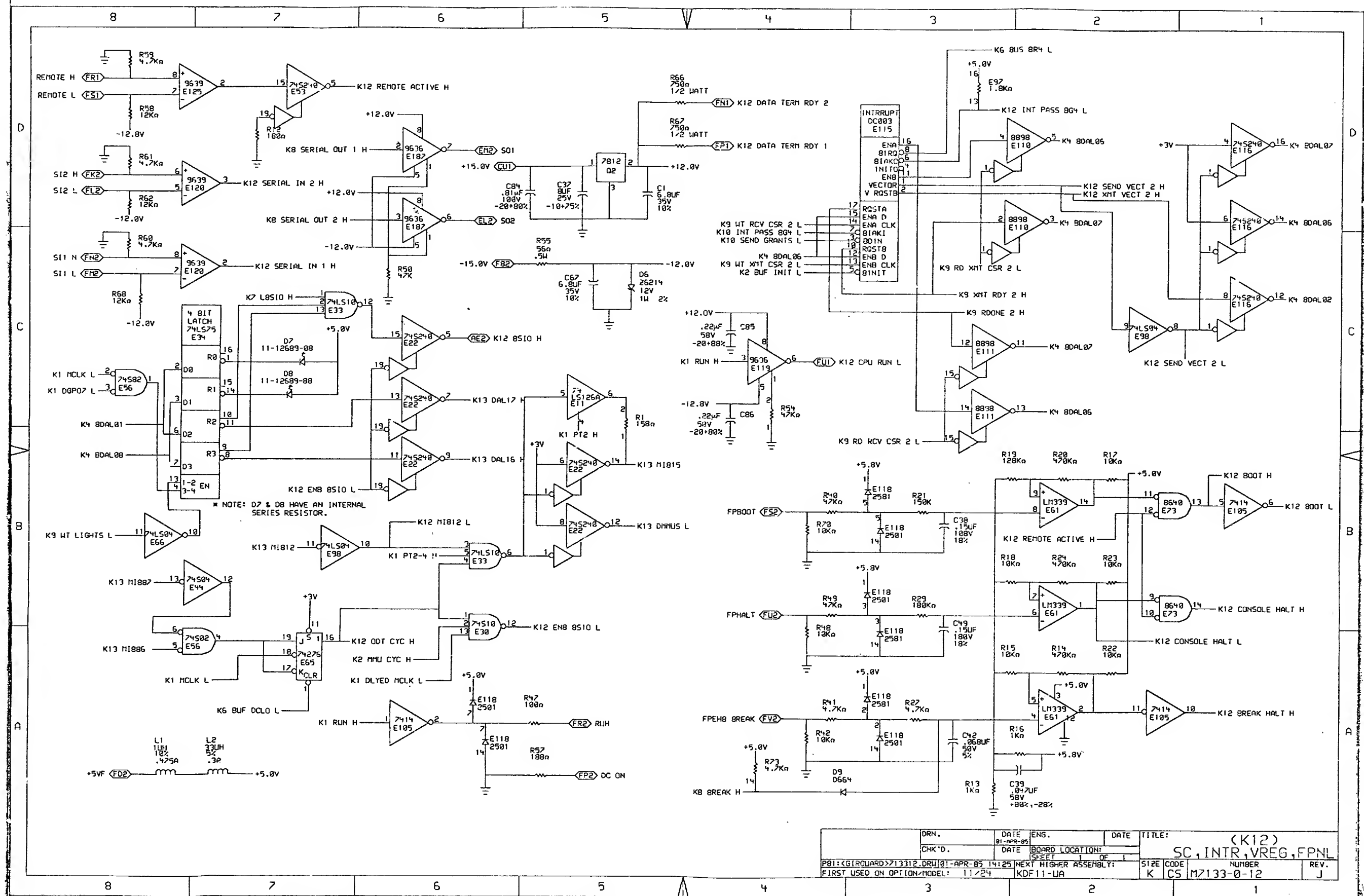




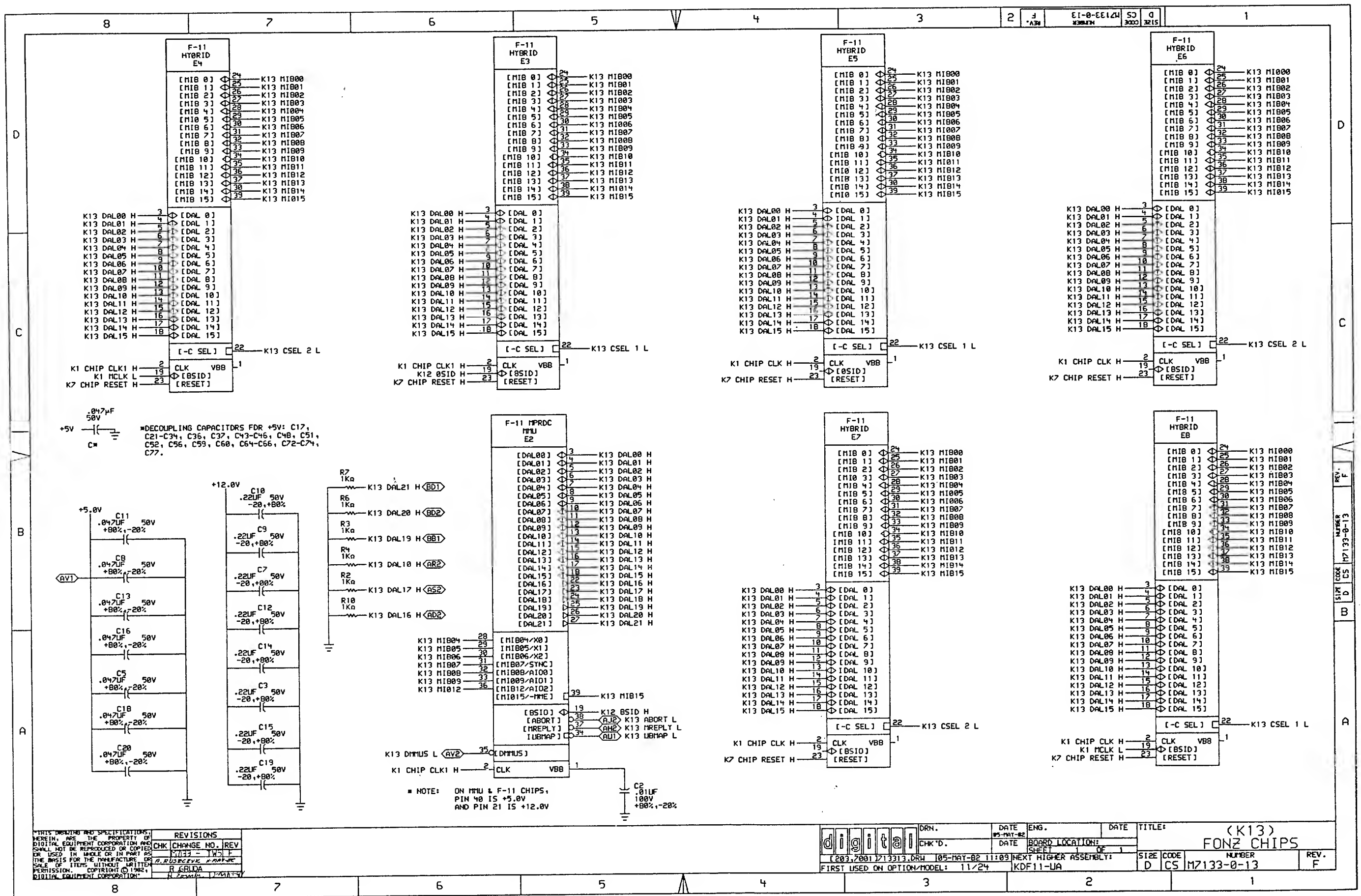












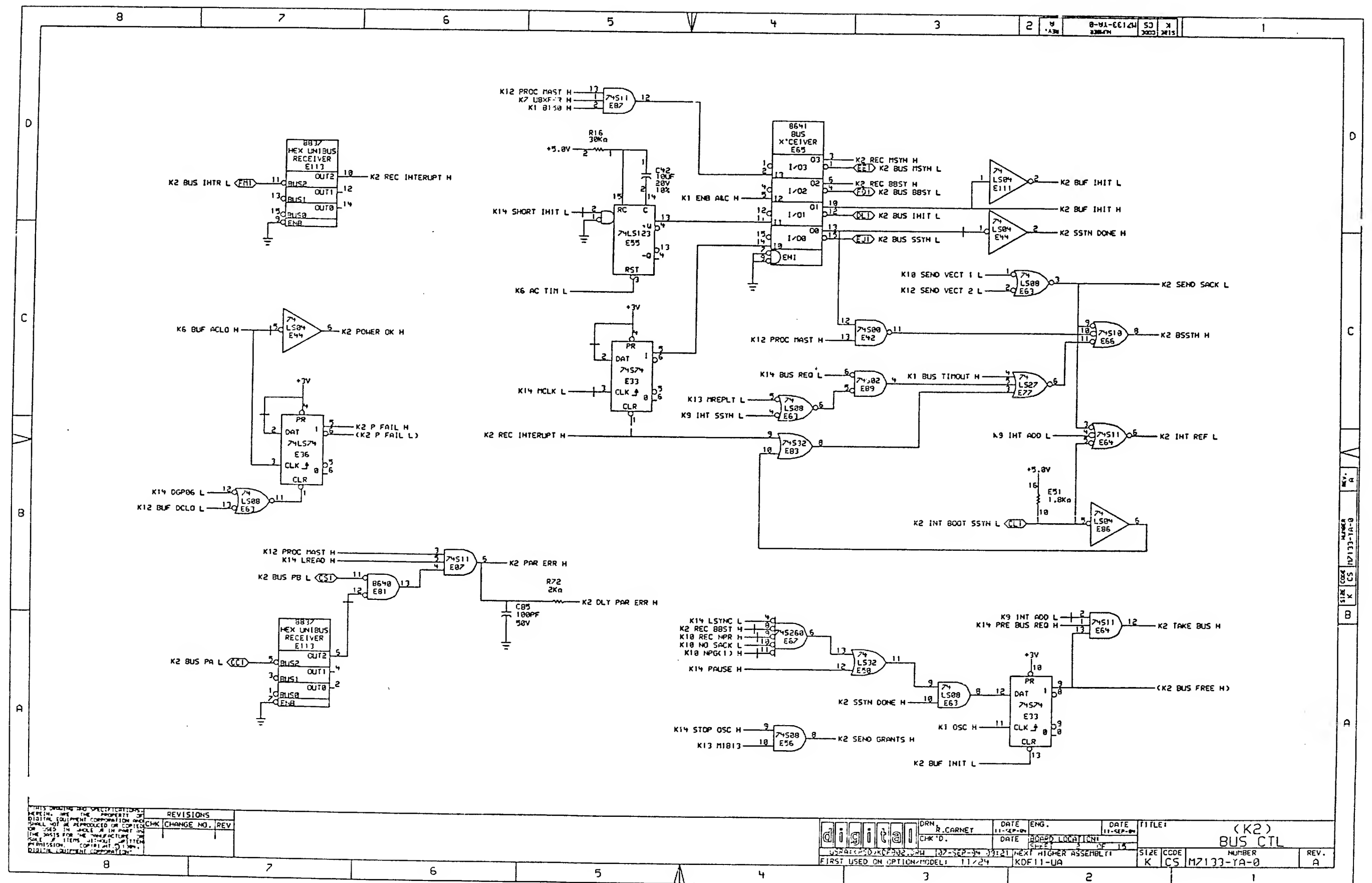


8	7	6	5	4	3	2	3	4	5	6	7	8																																																																																																																																																																																																																																																																																																																																										
<div>BAUD RATES</div> <div>THERE ARE TWO SIMULTANEOUS BAUD RATES AVAILABLE. BAUD RATE 1 IS SELECTED USING SWITCHES 5,6,7,&amp; 8 OF E135. BAUD RATE 2 IS SELECTED USING SWITCHES 1,2,3,&amp; 4 OF E135. BOTH ARE ACCORDING TO THE SWITCH SETTING PATTERN SHOWN BELOW.</div> <table><tr><th colspan="5">SWITCH E135</th></tr><tr><th>BAUD RATE 1 (KB BR1)</th><th>5</th><th>6</th><th>7</th><th>8</th></tr><tr><th>BAUD RATE 2 (KB BR2)</th><th>1</th><th>2</th><th>3</th><th>4</th></tr><tr><td>BAUD RATE 1</td><td>50</td><td>ON</td><td>ON</td><td>ON</td><td>ON</td></tr><tr><td></td><td>75</td><td>ON</td><td>ON</td><td>ON</td><td>OFF</td></tr><tr><td></td><td>110</td><td>ON</td><td>ON</td><td>OFF</td><td>ON</td></tr><tr><td></td><td>134.5</td><td>ON</td><td>ON</td><td>OFF</td><td>OFF</td></tr><tr><td></td><td>150</td><td>ON</td><td>OFF</td><td>ON</td><td>ON</td></tr><tr><td></td><td>200</td><td>ON</td><td>OFF</td><td>ON</td><td>OFF</td></tr><tr><td></td><td>300</td><td>ON</td><td>OFF</td><td>OFF</td><td>ON</td></tr><tr><td></td><td>600</td><td>ON</td><td>OFF</td><td>OFF</td><td>OFF</td></tr><tr><td></td><td>1200</td><td>OFF</td><td>ON</td><td>ON</td><td>ON</td></tr><tr><td></td><td>1800</td><td>OFF</td><td>ON</td><td>ON</td><td>OFF</td></tr><tr><td></td><td>2000</td><td>OFF</td><td>ON</td><td>OFF</td><td>ON</td></tr><tr><td></td><td>2400</td><td>OFF</td><td>ON</td><td>OFF</td><td>OFF</td></tr><tr><td></td><td>3600</td><td>OFF</td><td>OFF</td><td>ON</td><td>ON</td></tr><tr><td></td><td>4800</td><td>OFF</td><td>OFF</td><td>ON</td><td>OFF</td></tr><tr><td></td><td>9600</td><td>OFF</td><td>OFF</td><td>OFF</td><td>ON</td></tr><tr><td></td><td>19200</td><td>OFF</td><td>OFF</td><td>OFF</td><td>OFF</td></tr></table> <div>(IF REMOTE ACTIVE H IS ASSERTED, THEN 19200 IS ANY SETTING.)</div> <div>SLU 1/SLU 2 BAUD RATE SELECTION</div> <div>SLU 1</div> <table><tr><th colspan="4">SWITCH E124</th></tr><tr><th>1</th><th>2</th><th>3</th><th>4</th></tr><tr><td>XMIT RATE (KB XMIT CLK 1)</td><td></td><td></td><td></td></tr><tr><td>BAUD RATE 1</td><td>OFF</td><td>ON</td><td>—</td><td>—</td></tr><tr><td>BAUD RATE 2</td><td>ON</td><td>OFF</td><td>—</td><td>—</td></tr><tr><td>RCVR RATE (KB RCVR CLK 1)</td><td></td><td></td><td></td></tr><tr><td>BAUD RATE 1</td><td>—</td><td>—</td><td>ON</td><td>OFF</td></tr><tr><td>BAUD RATE 2</td><td>—</td><td>—</td><td>OFF</td><td>ON</td></tr></table> <div>SLU 2</div> <table><tr><th colspan="6">JUMPERS</th></tr><tr><th>W4</th><th>W9</th><th>W10</th><th>W11</th><th>W12</th><th>W13</th></tr><tr><td>XMIT RATE (KB TCLK 2)</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>BAUD RATE 1</td><td>—</td><td>—</td><td>OUT</td><td>IN</td><td>—</td><td>OUT</td></tr><tr><td>BAUD RATE 2</td><td>—</td><td>—</td><td>IN</td><td>OUT</td><td>—</td><td>OUT</td></tr><tr><td>19.2K BAUD</td><td>—</td><td>—</td><td>OUT</td><td>OUT</td><td>—</td><td>IN</td></tr><tr><td>RCVR RATE (KB RCLK 2)</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>BAUD RATE 1</td><td>OUT</td><td>IN</td><td>—</td><td>—</td><td>OUT</td><td>—</td></tr><tr><td>BAUD RATE 2</td><td>OUT</td><td>OUT</td><td>—</td><td>—</td><td>IN</td><td>—</td></tr><tr><td>19.2K BAUD</td><td>IN</td><td>OUT</td><td>—</td><td>—</td><td>OUT</td><td>—</td></tr></table> <div>ODD/EVEN PARITY; STOP BITS</div> <table><tr><th colspan="2">SLU 1</th><th colspan="2">SLU 2</th></tr><tr><th>W5</th><th>W6</th><th>W7</th><th>W8</th></tr><tr><td>ODD PARITY</td><td>—</td><td>IN</td><td>—</td><td>IN</td></tr><tr><td>EVEN PARITY</td><td>—</td><td>OUT</td><td>—</td><td>OUT</td></tr><tr><td>PARITY ENABLED</td><td>IN</td><td>—</td><td>IN</td><td>—</td></tr><tr><td>PARITY DISABLED</td><td>OUT</td><td>—</td><td>OUT</td><td>—</td></tr><tr><td colspan="2">W1</td><td colspan="2">W2</td></tr><tr><td>ONE STOP BIT</td><td>IN</td><td colspan="2">—</td></tr><tr><td>TWO STOP BIT</td><td>OUT</td><td colspan="2">—</td></tr></table> <div>(REMOTE ACTIVE H DEFAULTS TO ONE STOP BIT)</div> <div>CLOCK SETUP PROCEDURE</div> <ol style="list-style-type: none"><li>1. PUT PROBE ON K1 MCLK L AT PIN B82</li><li>2. PUT MACHINE IN WAIT LOOP.</li><li>3. ADJUST POTENTIOMETER R31 WITH SCREWDRIVER UNTIL K1 MCLK L HAS A PERIOD OF 300ns.</li></ol> <table><tr><th colspan="2">SWITCH</th></tr><tr><td>E124</td><td>5</td><td>ON</td><td>ENABLE FIELD SERVICE MAINT L</td></tr><tr><td></td><td></td><td>OFF</td><td>NO FS MAINT</td></tr><tr><td>E124</td><td>6</td><td>ON</td><td>NO EFFECT ON UBXFER</td></tr><tr><td></td><td></td><td>OFF</td><td>FORCES UBXFER L</td></tr></table> <div>JUMPERS</div> <table><tr><td>POWER UP TO1</td><td>W14</td></tr><tr><td>165000</td><td>IN</td></tr><tr><td>173000</td><td>OUT</td></tr><tr><td>ALLOW HALTS</td><td>W3</td></tr><tr><td>KERNEL HALT TRAPS TO 10</td><td>OUT</td></tr><tr><td>BOOT ON POWER UP</td><td>W2</td></tr><tr><td>POWER UP VIA 24</td><td>IN</td></tr><tr><td></td><td>OUT</td></tr></table> <div>REVISIONS</div> <table><tr><th>CHK</th><th>CHANGE NO.</th><th>REV</th><th>CHK</th><th>CHANGE NO.</th><th>REV</th></tr><tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr></table> <div>digital</div> <table><tr><td>DATE</td><td>ENG.</td><td>DATE</td><td>TITLE</td></tr><tr><td>11-0-88</td><td>11-0-88</td><td>11-0-88</td><td>(K14) SW, JMPR, CLK SETTINGS</td></tr><tr><td>11-0-88</td><td>11-0-88</td><td>11-0-88</td><td>11-0-88</td></tr><tr><td>11-0-88</td><td>11-0-88</td><td>11-0-88</td><td>11-0-88</td></tr></table> <table><tr><td>SIZE</td><td>CODE</td><td>NUMBER</td><td>REV.</td></tr><tr><td>D</td><td>CS</td><td>M7133-0-14</td><td>E</td></tr></table> <table><tr><td>8</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td></tr></table>												SWITCH E135					BAUD RATE 1 (KB BR1)	5	6	7	8	BAUD RATE 2 (KB BR2)	1	2	3	4	BAUD RATE 1	50	ON	ON	ON	ON		75	ON	ON	ON	OFF		110	ON	ON	OFF	ON		134.5	ON	ON	OFF	OFF		150	ON	OFF	ON	ON		200	ON	OFF	ON	OFF		300	ON	OFF	OFF	ON		600	ON	OFF	OFF	OFF		1200	OFF	ON	ON	ON		1800	OFF	ON	ON	OFF		2000	OFF	ON	OFF	ON		2400	OFF	ON	OFF	OFF		3600	OFF	OFF	ON	ON		4800	OFF	OFF	ON	OFF		9600	OFF	OFF	OFF	ON		19200	OFF	OFF	OFF	OFF	SWITCH E124				1	2	3	4	XMIT RATE (KB XMIT CLK 1)				BAUD RATE 1	OFF	ON	—	—	BAUD RATE 2	ON	OFF	—	—	RCVR RATE (KB RCVR CLK 1)				BAUD RATE 1	—	—	ON	OFF	BAUD RATE 2	—	—	OFF	ON	JUMPERS						W4	W9	W10	W11	W12	W13	XMIT RATE (KB TCLK 2)						BAUD RATE 1	—	—	OUT	IN	—	OUT	BAUD RATE 2	—	—	IN	OUT	—	OUT	19.2K BAUD	—	—	OUT	OUT	—	IN	RCVR RATE (KB RCLK 2)						BAUD RATE 1	OUT	IN	—	—	OUT	—	BAUD RATE 2	OUT	OUT	—	—	IN	—	19.2K BAUD	IN	OUT	—	—	OUT	—	SLU 1		SLU 2		W5	W6	W7	W8	ODD PARITY	—	IN	—	IN	EVEN PARITY	—	OUT	—	OUT	PARITY ENABLED	IN	—	IN	—	PARITY DISABLED	OUT	—	OUT	—	W1		W2		ONE STOP BIT	IN	—		TWO STOP BIT	OUT	—		SWITCH		E124	5	ON	ENABLE FIELD SERVICE MAINT L			OFF	NO FS MAINT	E124	6	ON	NO EFFECT ON UBXFER			OFF	FORCES UBXFER L	POWER UP TO1	W14	165000	IN	173000	OUT	ALLOW HALTS	W3	KERNEL HALT TRAPS TO 10	OUT	BOOT ON POWER UP	W2	POWER UP VIA 24	IN		OUT	CHK	CHANGE NO.	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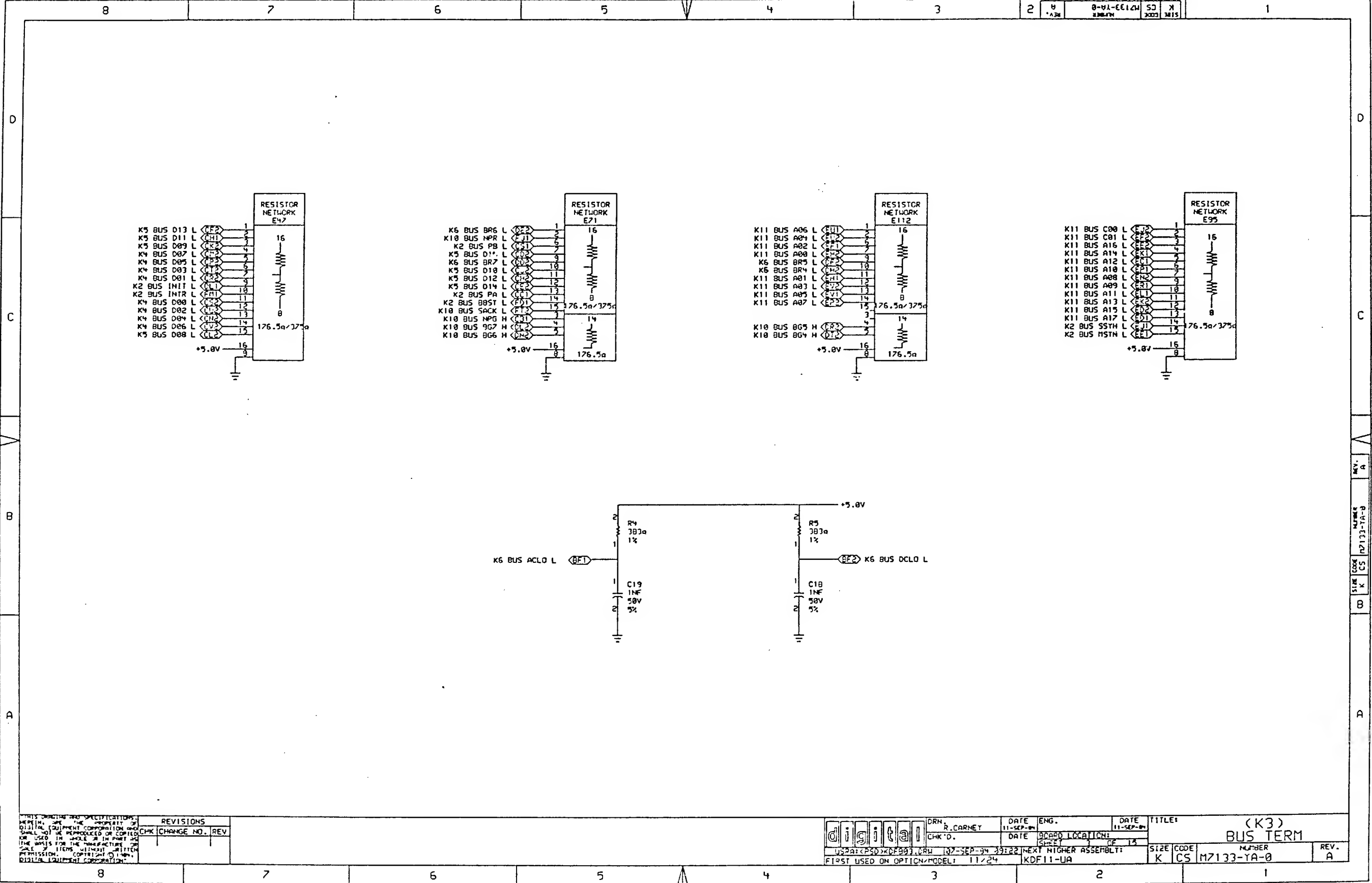




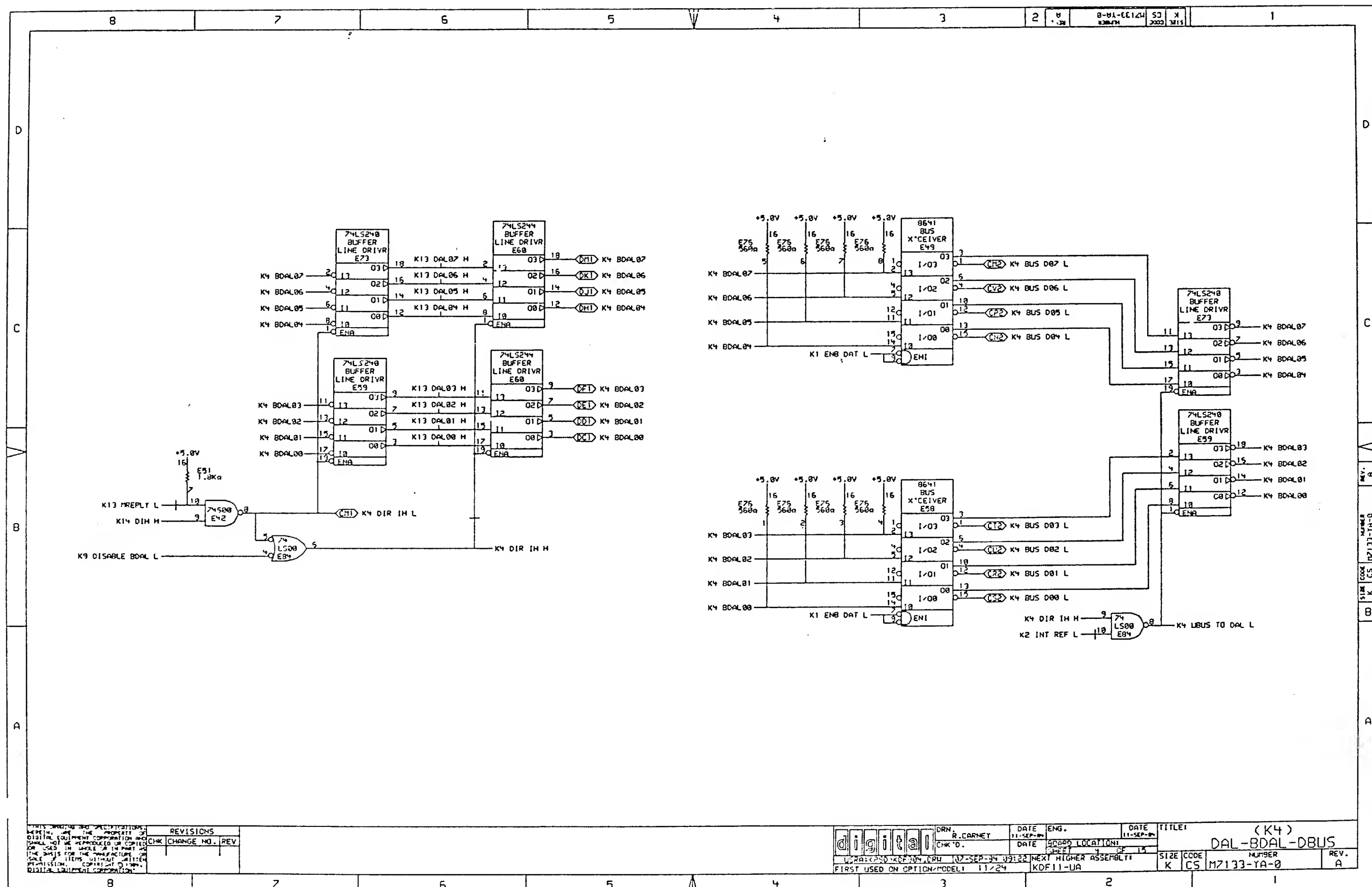




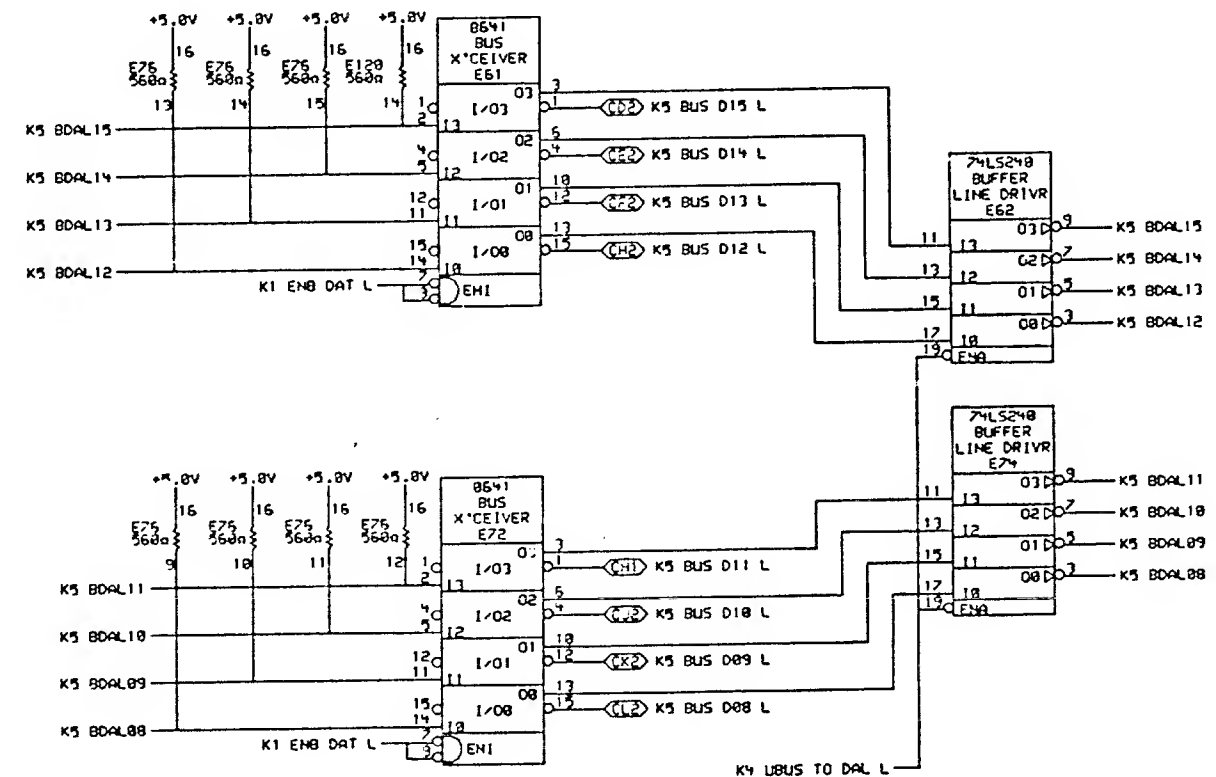




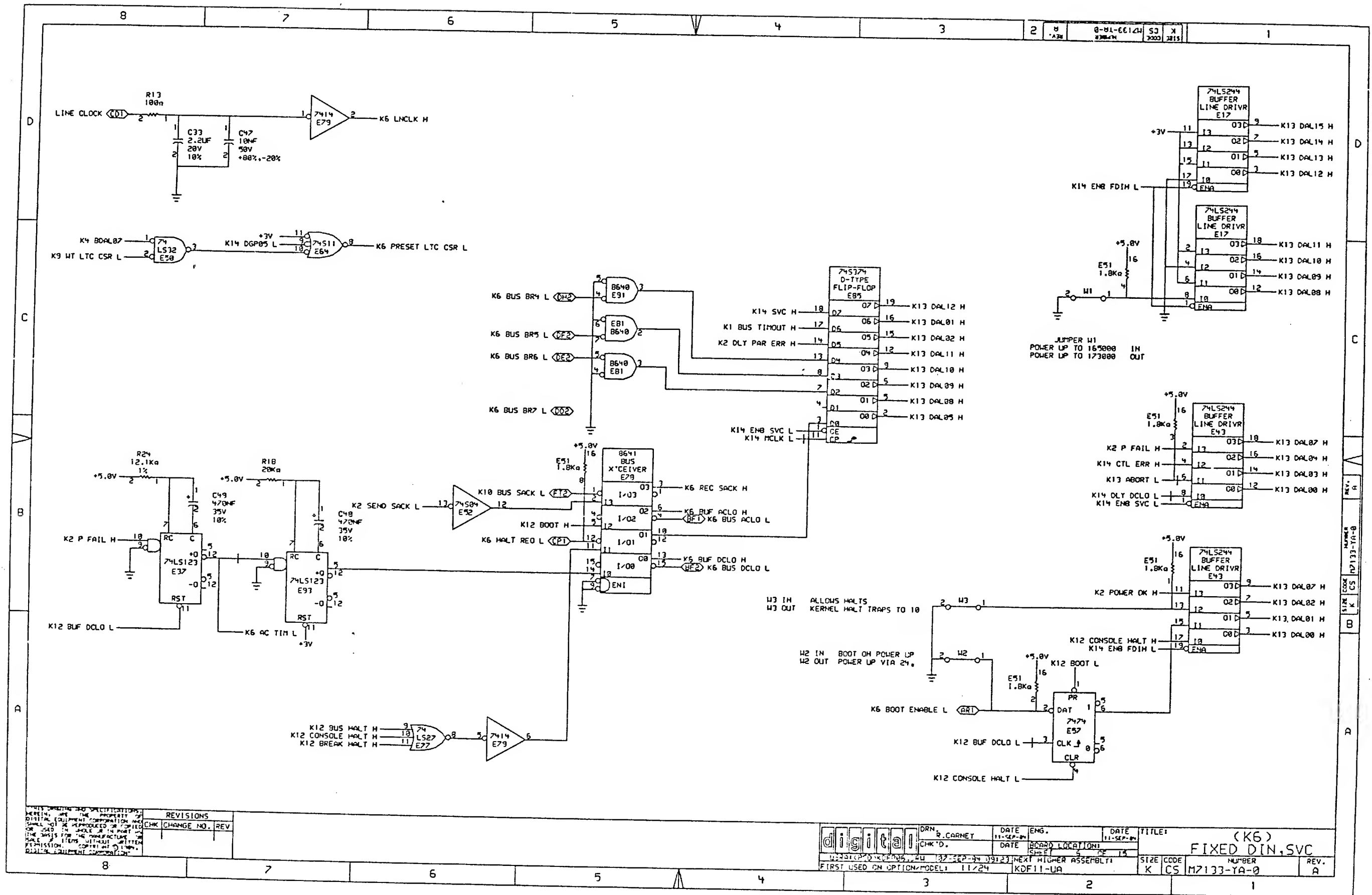




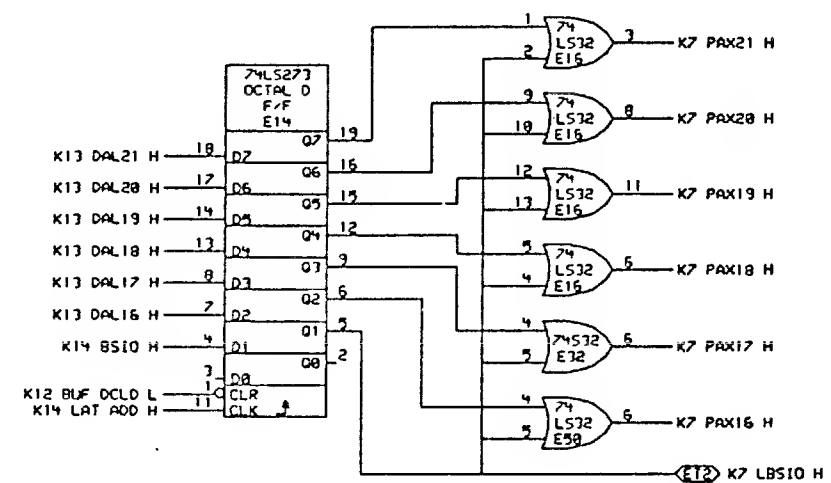
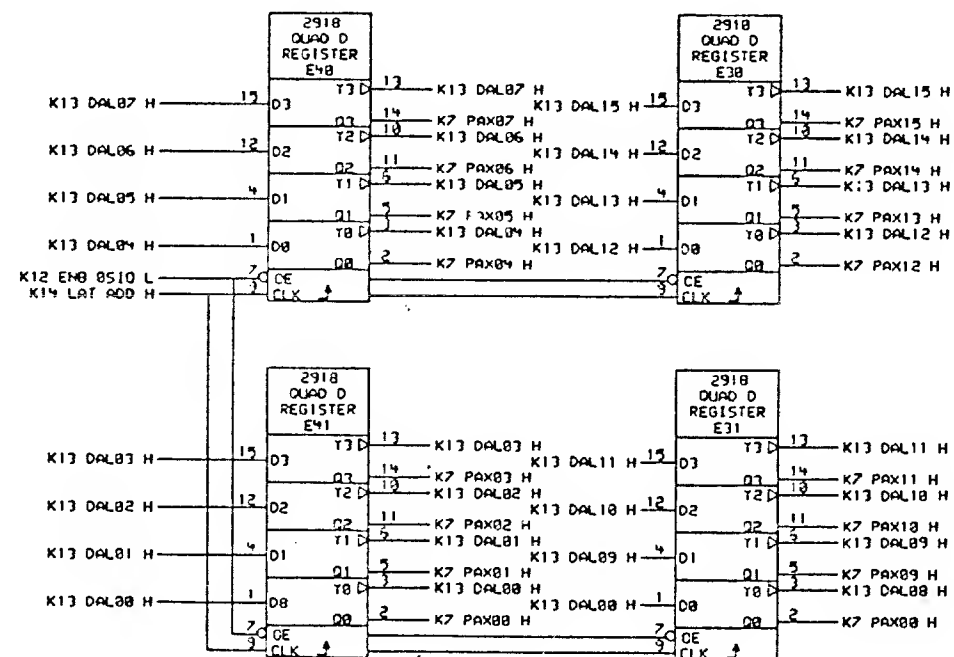
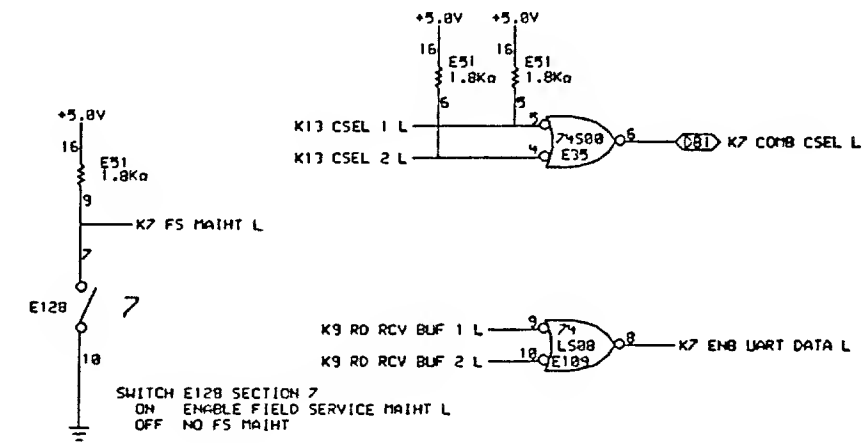












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	DATE 11-SEP-84	ENG.
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0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99
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DATE 11-SEP-64	TITLE:
SE 15	

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(K7)  
PAX

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USDA (PSS) KCFB07.52W 07-SEP-94  
FIRST USED ON OPTION/MODEL: 11/

09123	NEXT HIGHER ASSEMB
24	KDF11-UA

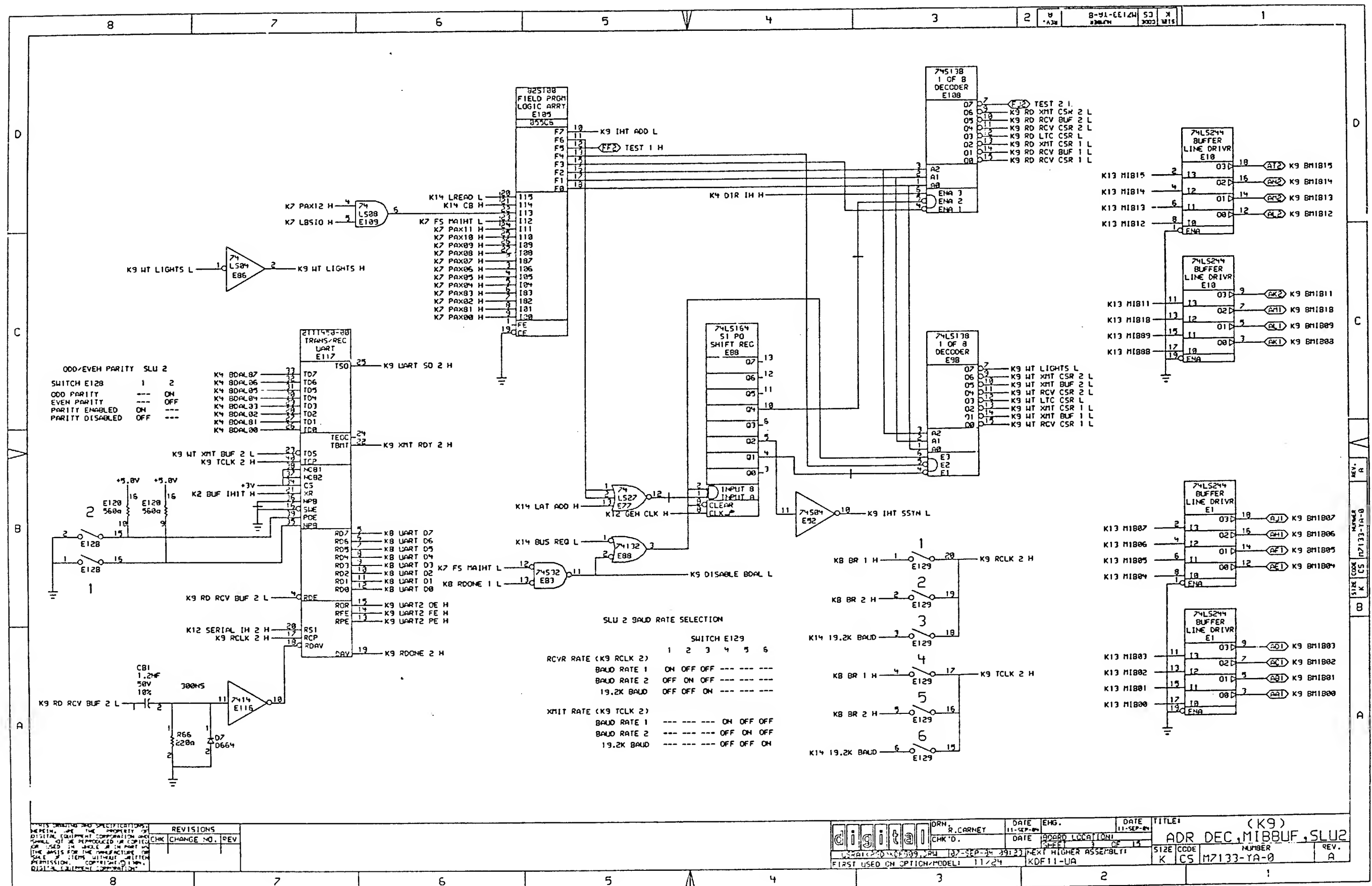
SIZE	CCOE	
K	CS	M7137

NUMBER	REV.
3-YA-0	A

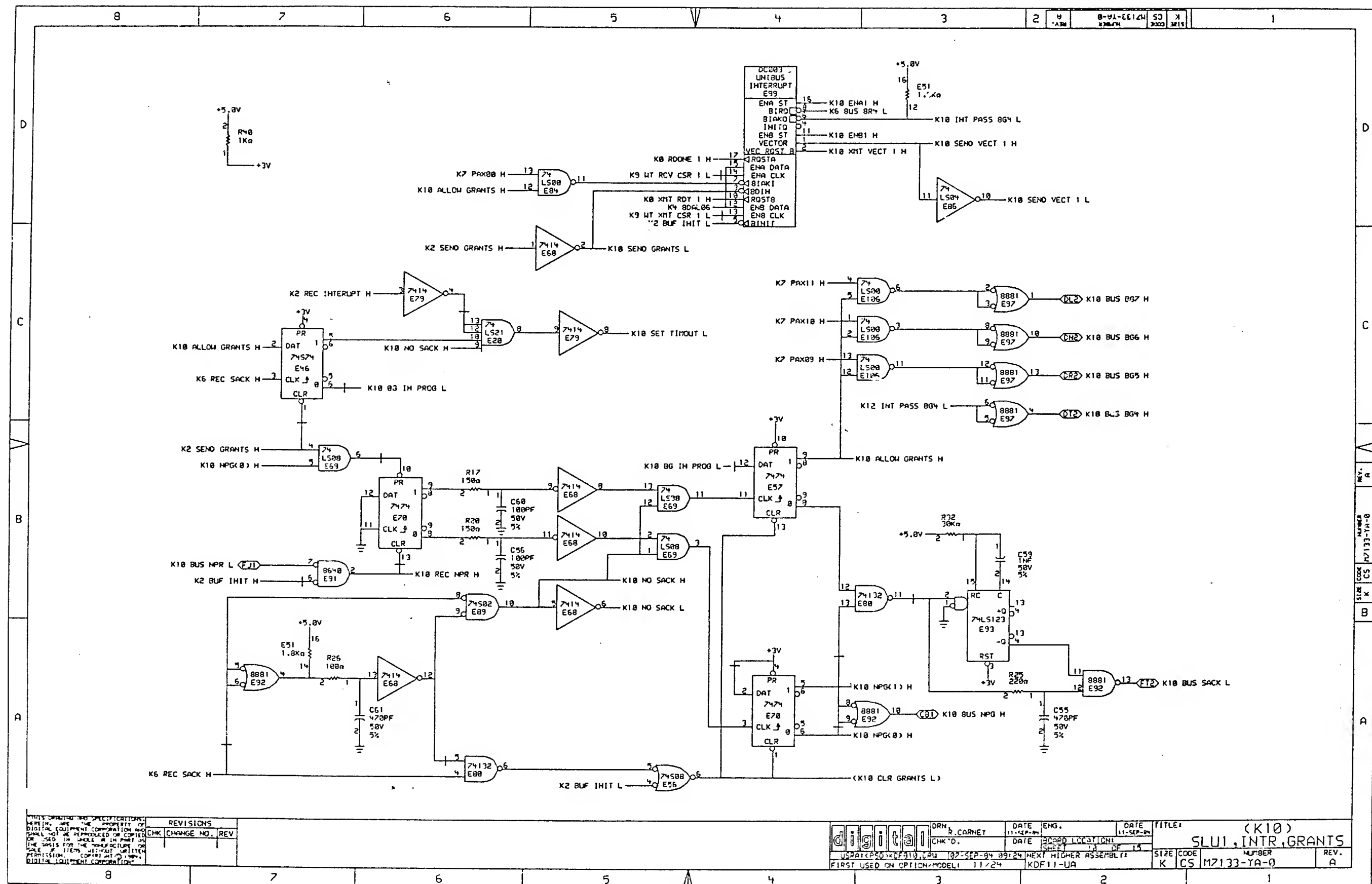








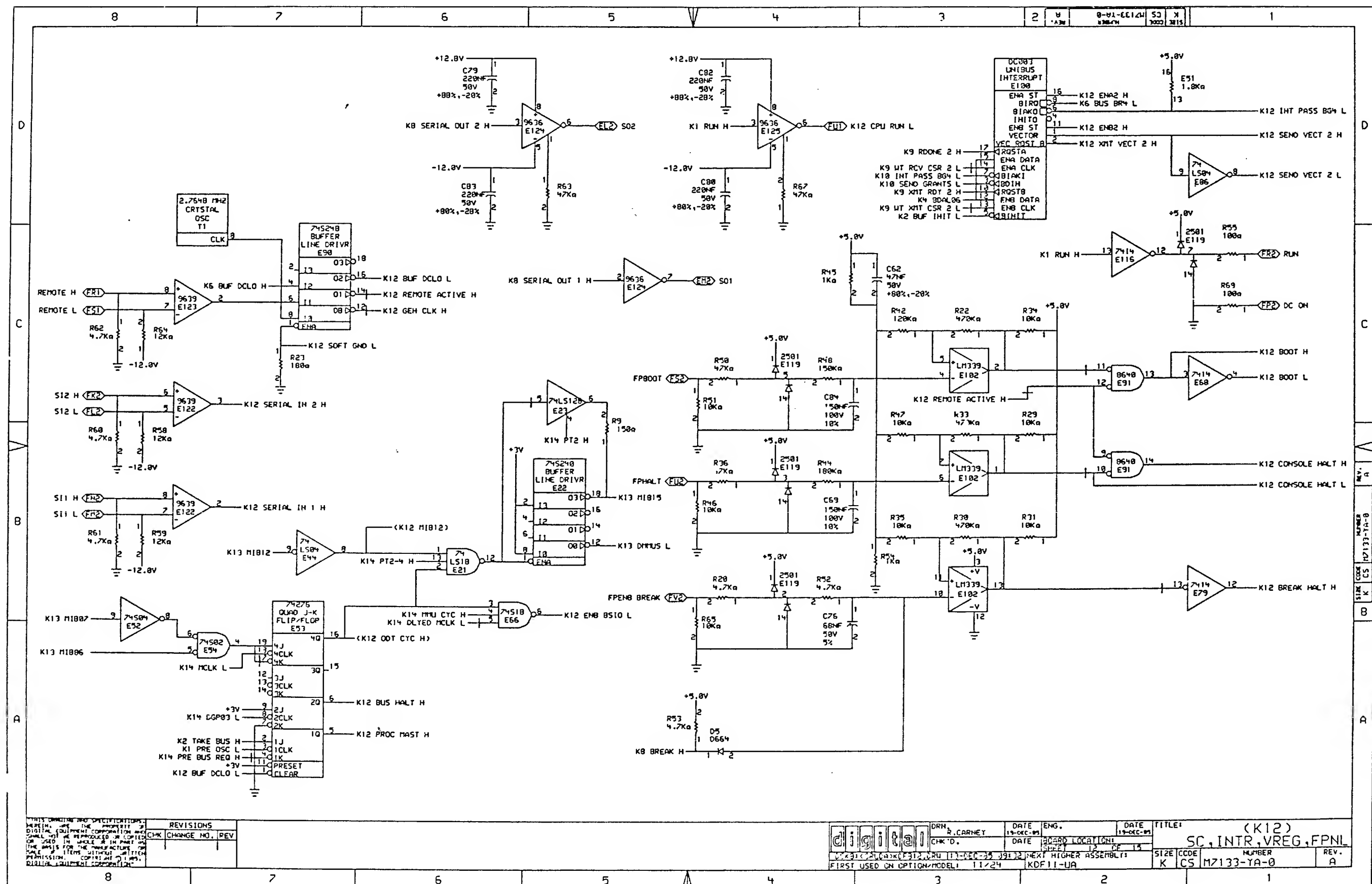




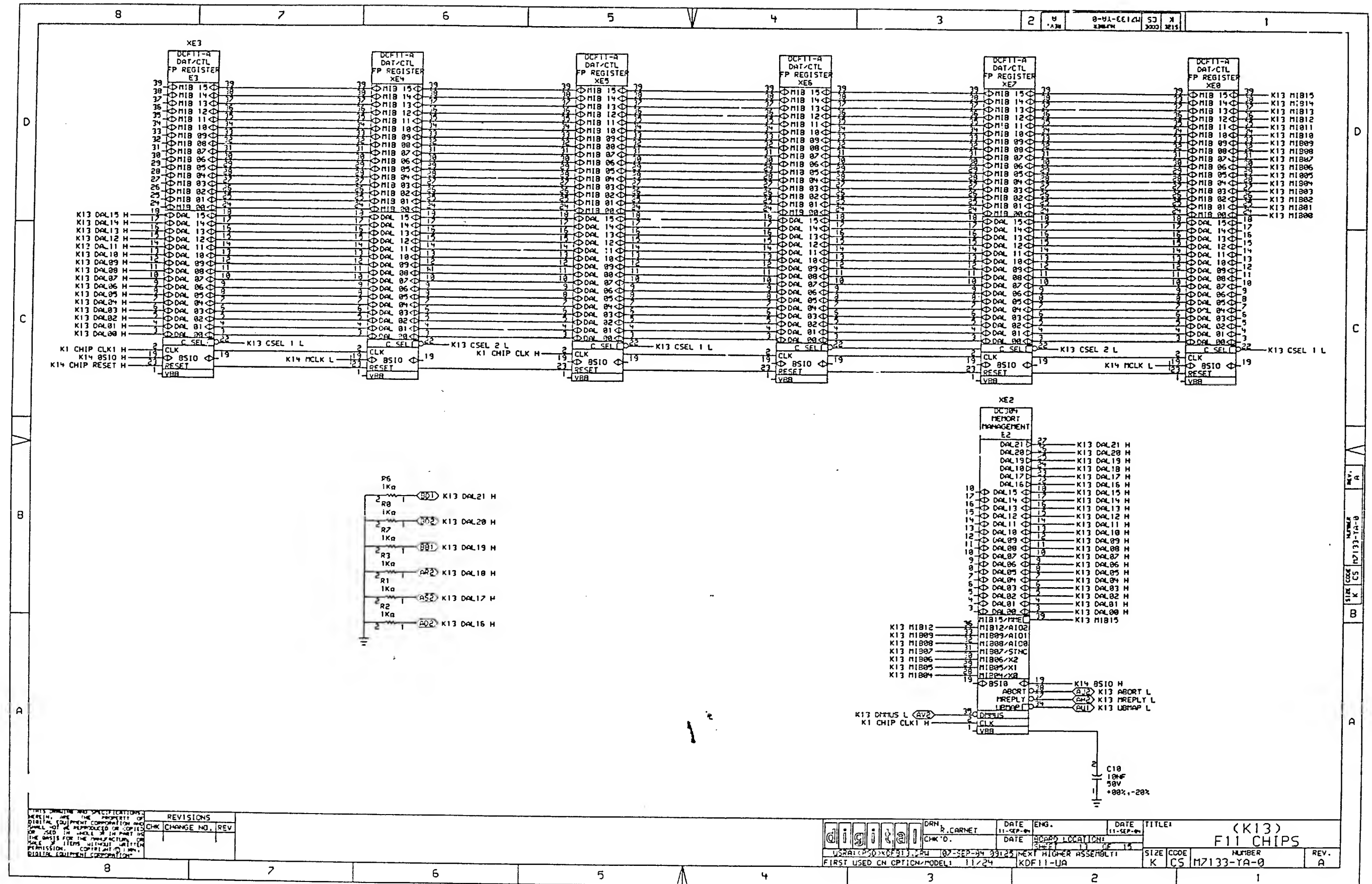




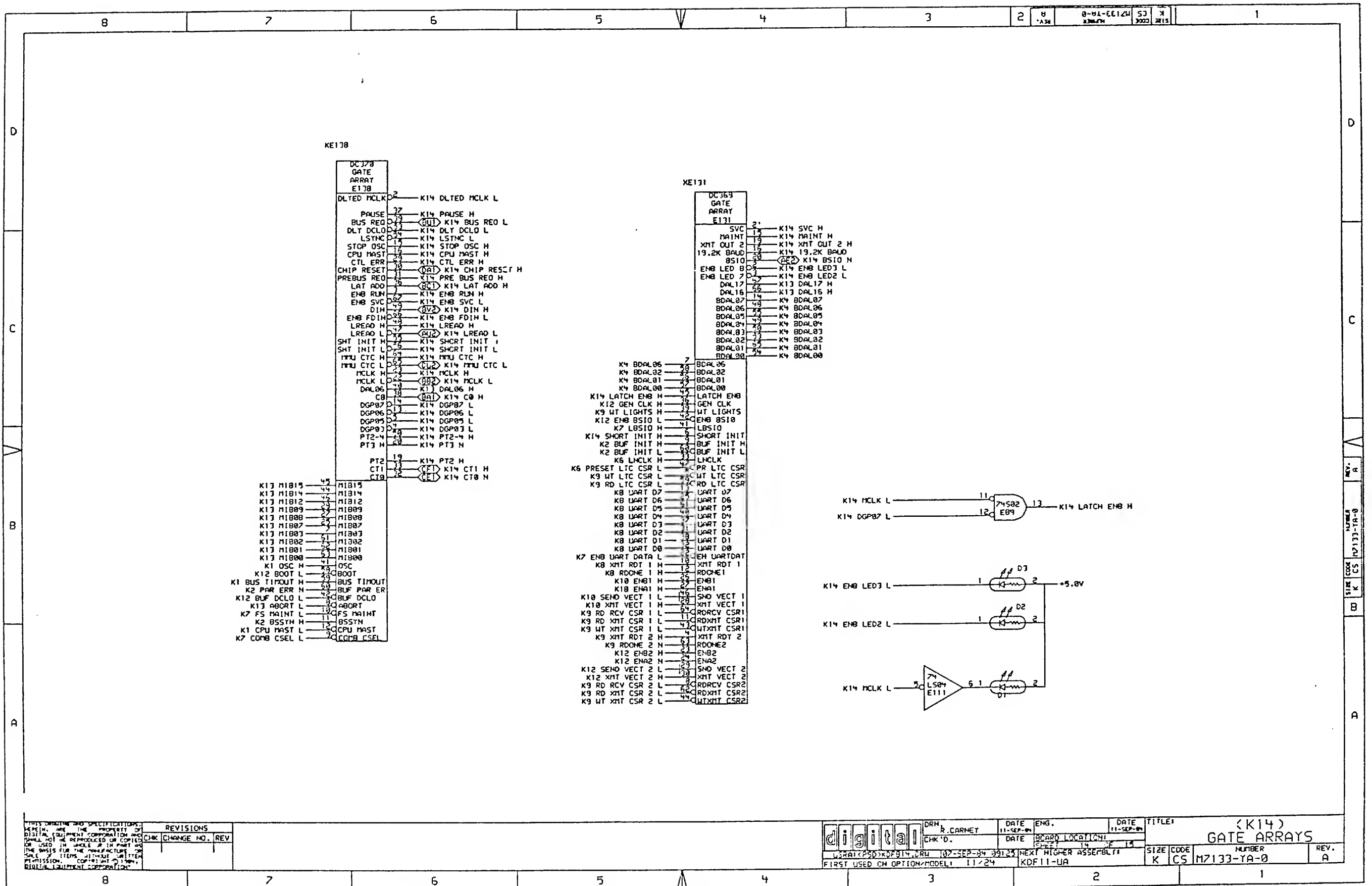










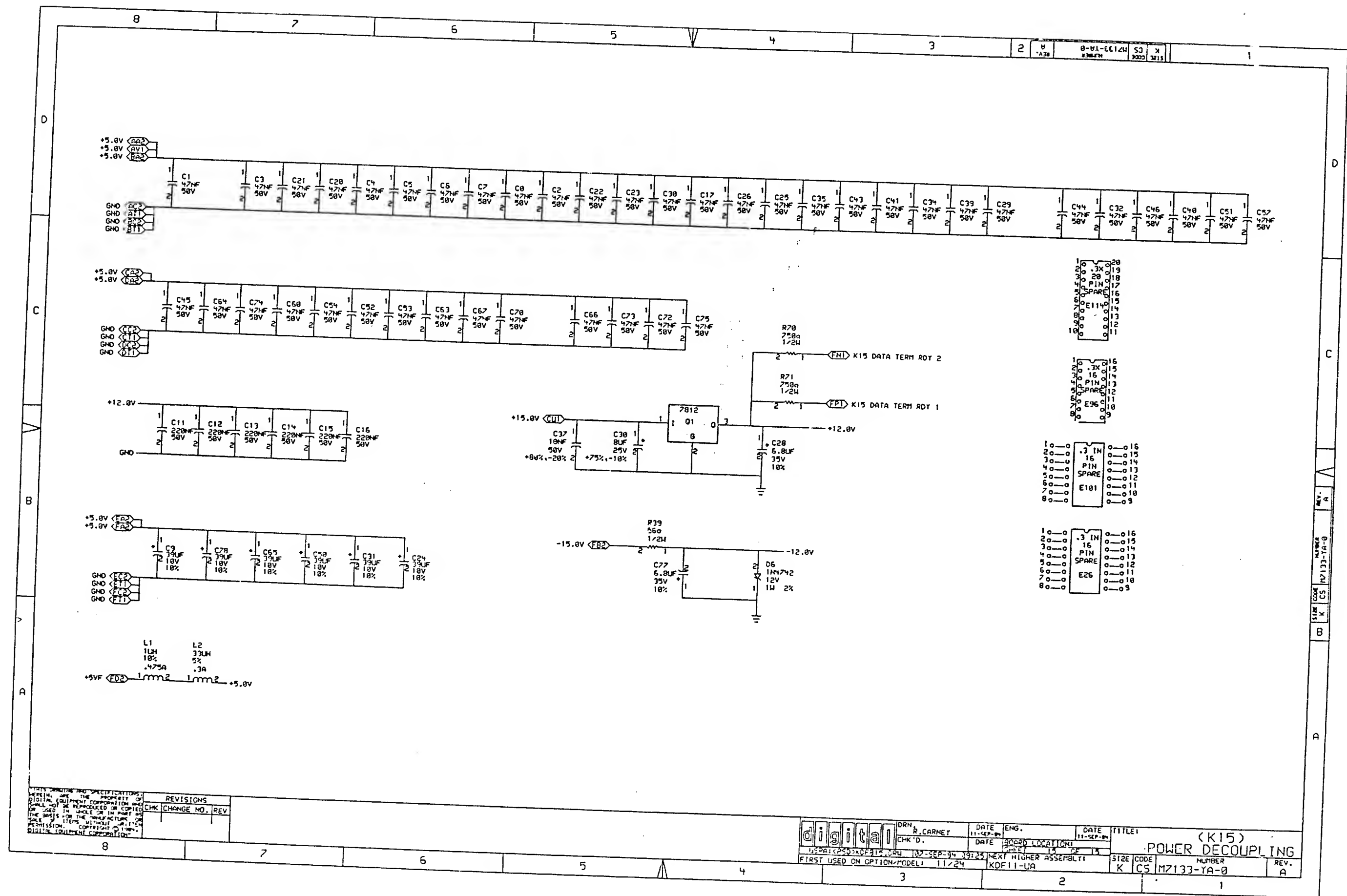


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REVISIONS	
CHK	CHANGE NO. REV

digital		DRH	R. CARNEY	DATE	ENG.	DATE	TITLE
CHK'D.				11-SEP-80			(K14) GATE ARRAYS
USHA1250X2F014.CRW		02-SEP-84		09124	NEXT HIGHER ASSEMBLY		SIZE CODE NUMBER REV.
FIRST USED ON OPTION MODEL 11/24		KDF11-UA					K CS M7133-YA-0 A







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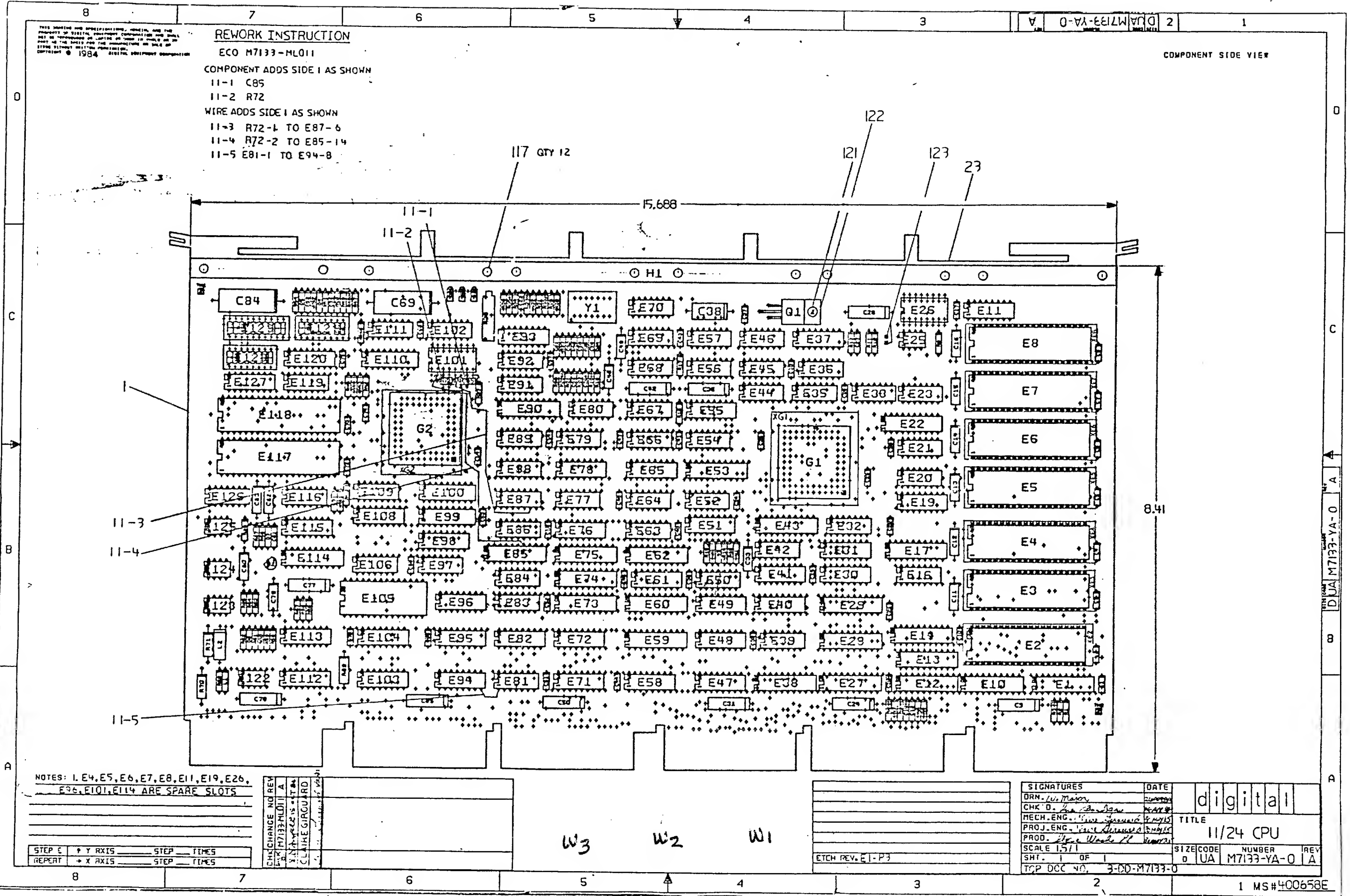
# REWORK INSTRUCTION

ECO M7133-ML011

COMPONENT ADDS SIDE 1 AS SHOWN

- 11-1 C85
- 11-2 R72
- WIRE ADDS SIDE 1 AS SHOWN
- 11-3 R72-4 TO E87-6
- 11-4 R72-2 TO E85-14
- 11-5 E81-1 TO E94-8

COMPONENT SIDE VIEW



NOTES: E4, E5, E6, E7, E8, E11, E19, E26, E96, E101, E114 ARE SPARE SLOTS

STEP	C	Y AXIS	STEP	TIMES
REPEAT		X AXIS	STEP	TIMES

CHANGE NO	REV
1	A
2	B
3	C
4	D
5	E
6	F
7	G
8	H
9	I
10	J
11	K
12	L
13	M
14	N
15	O
16	P
17	Q
18	R
19	S
20	T
21	U
22	V
23	W
24	X
25	Y
26	Z
27	AA
28	AB
29	AC
30	AD
31	AE
32	AF
33	AG
34	AH
35	AI
36	AJ
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38	AL
39	AM
40	AN
41	AO
42	AP
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44	AR
45	AS
46	AT
47	AU
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49	AW
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52	AZ
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54	BB
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628	XD
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633	XI
634	XJ
635	XK
636	XL
637	XM
638	XN</



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SIZE <b>B</b>	CODE <b>TC</b>	NUMBER <b>MDECS-AA-1</b>	REV. <b>A</b>
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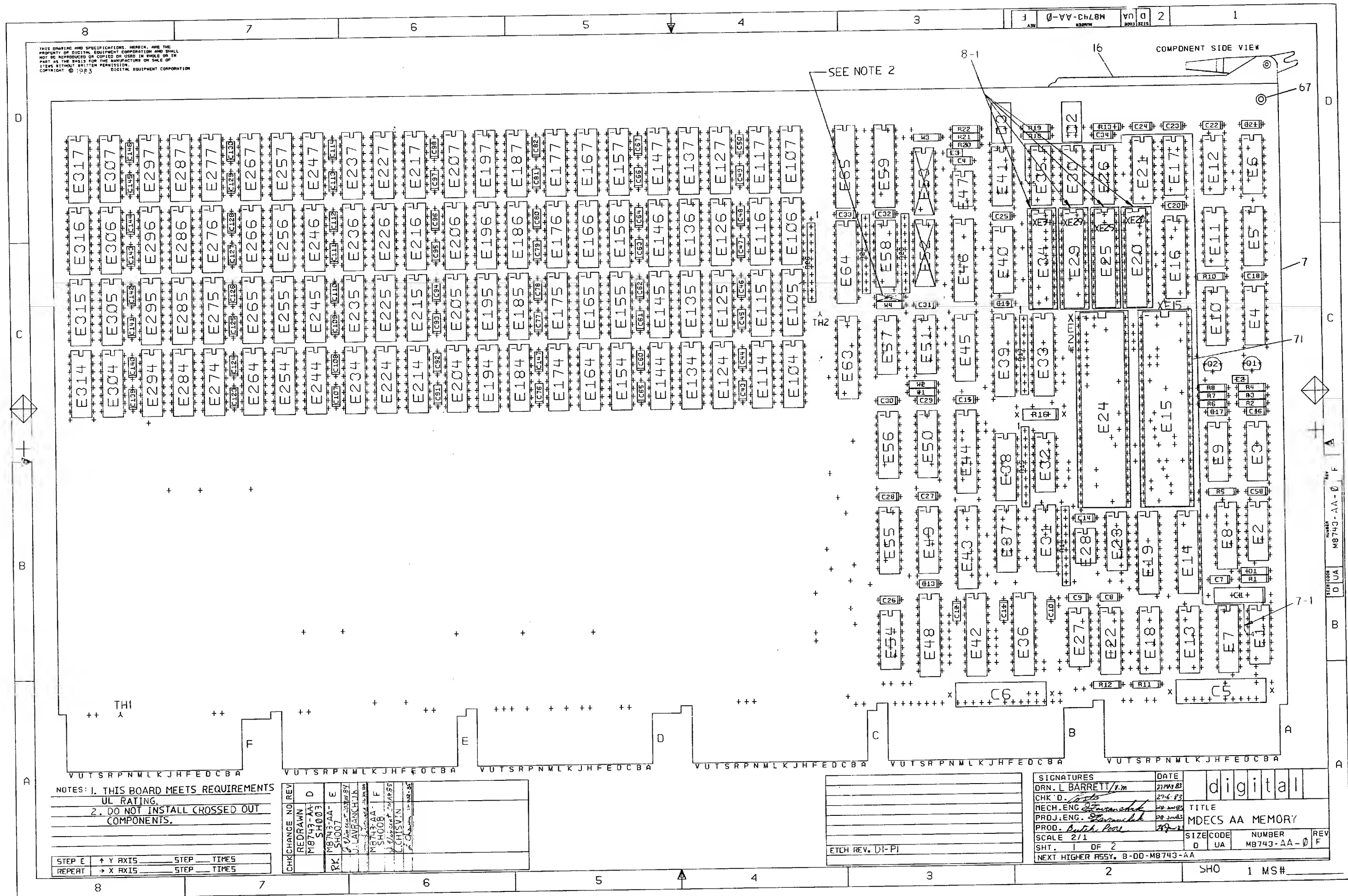
1. Revision codes for Drawing K-CS-M8743-AA-MS05 and part 5015117-00 are purposely out of sequence, so that their revision codes correspond to the module revision at the top of the column.
2. "-" in revision code means: "This line item does not describe this module revision."

[illegible]

digital

USED ON OPTION/MODEL		DRN. <i>P. Freeman</i>	13 AUG 82	TITLE											
DECSA-A		CHK'D <i>P. Freeman</i>	16 AUG 82	MDECS-AA MEMORY											
		ENG. <i>[Signature]</i>	28 JUN 83	SIZE	CODE	NUMBER						REV.			
		PROD. <i>RA 1 file</i>	10 SEP 82	B	DD	M8743-AA						K			
				SHEET 1 OF 1											



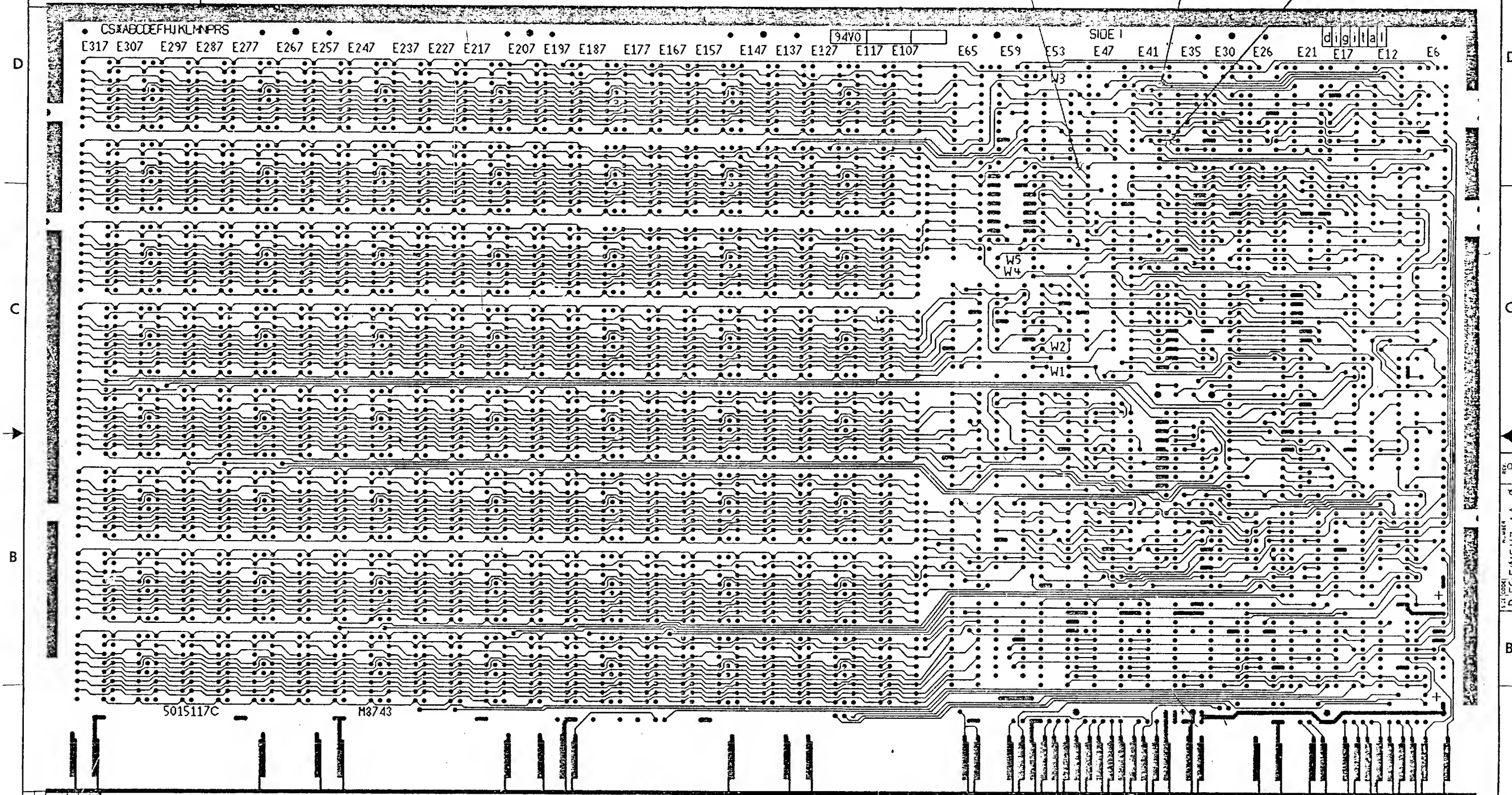








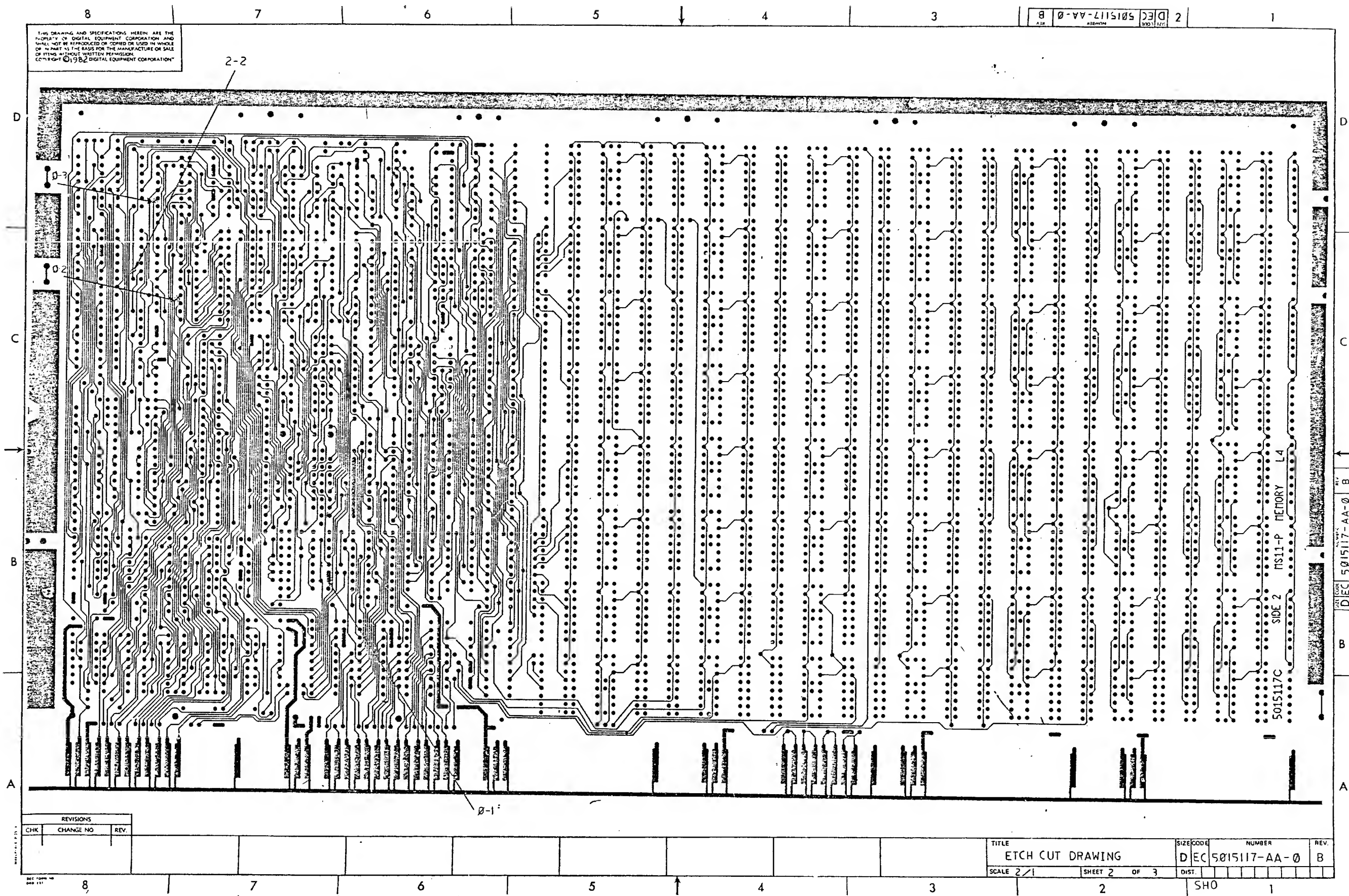
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REV	CHG	NO	DATE
B			
A			

DRN: J. J. J.	2/25/82	FIRST USED ON	MDECS-AA	digital
CHK: J. J. J.	11/17/82	TITLE	ETCH CUT DRAWING	
ENCL: J. J. J.	5/17/82	PROD. ENG. J. J. J.	6/17/82	
PROD. R. G. J.	6/17/82			
NEXT HIGHER ASSY.				
B-DD-M8743-AA		SIZE	CODE	NUMBER
SCALE 2/1		D	EC	5015117-AA-0
SHEET 1 OF 3		DIST.		





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REVISIONS		
CHK	CHANGE NO	REV.

TITLE		SIZE CODE	NUMBER	REV.
ETCH CUT DRAWING		D	EC 5015117-AA-0	B
SCALE 2/1	SHEET 2 OF 3	DIST.	SHO	1



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FIRST RELEASE:  
ETCH CUTS  
SIDE 2

- Ø-1: CUT ETCH AT E37-1Ø & 11.
  - Ø-2: CUT ETCH AT E16-8.
  - Ø-3: CUT ETCH AT E17-9.
- SIDE 1
- Ø-4: CUT ETCH AT E41-9.
  - Ø-5: CUT ETCH AT E46-1.

REWORK INSTRUCTIONS:

ETCH CUTS:

SIDE 1

- 2-1. CUT ETCH LEADING TO E41-1 NEAR D3.

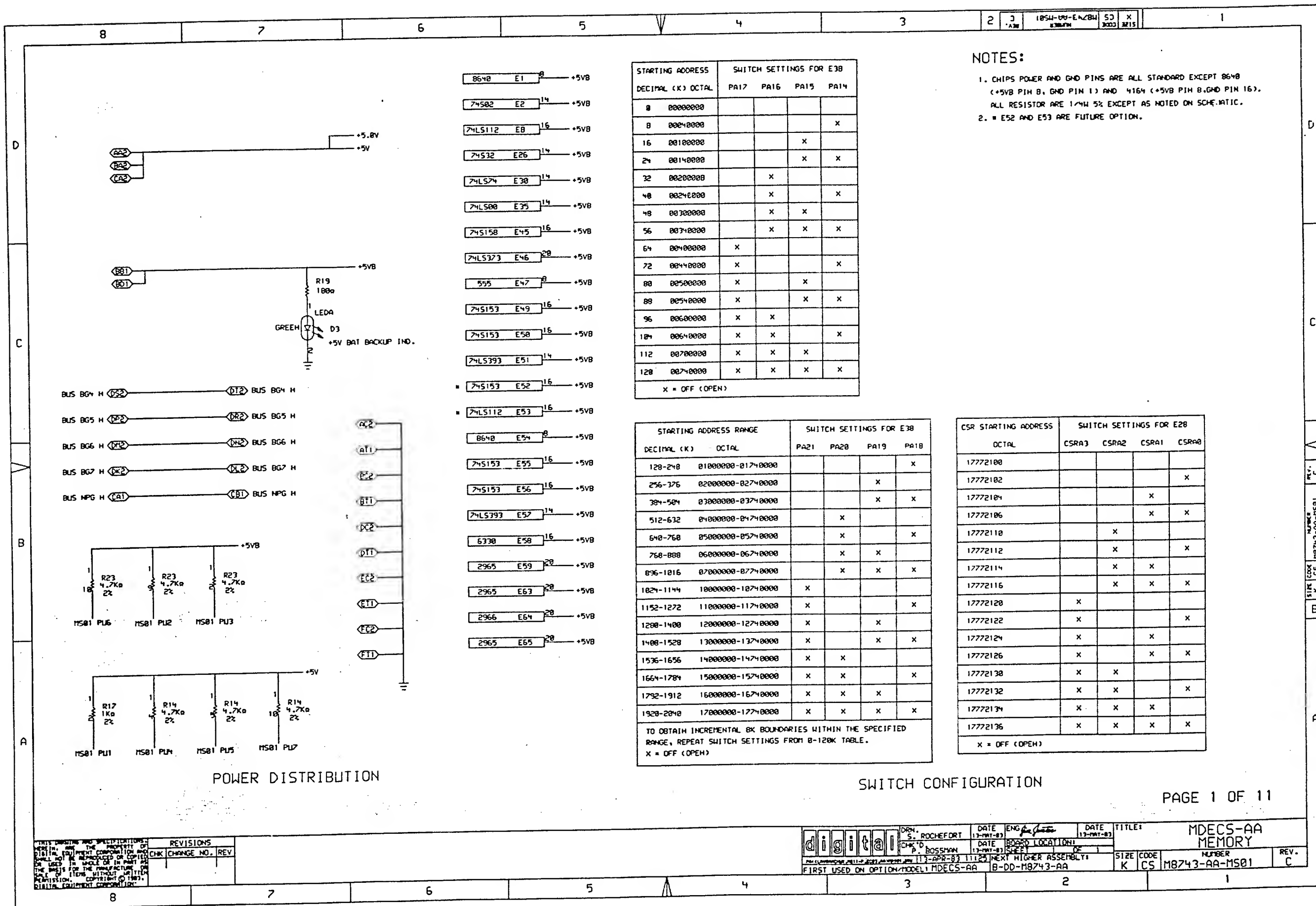
SIDE 2

- 2-2. CUT ETCH AT FEEDTHRU CONNECTED TO E11-8.

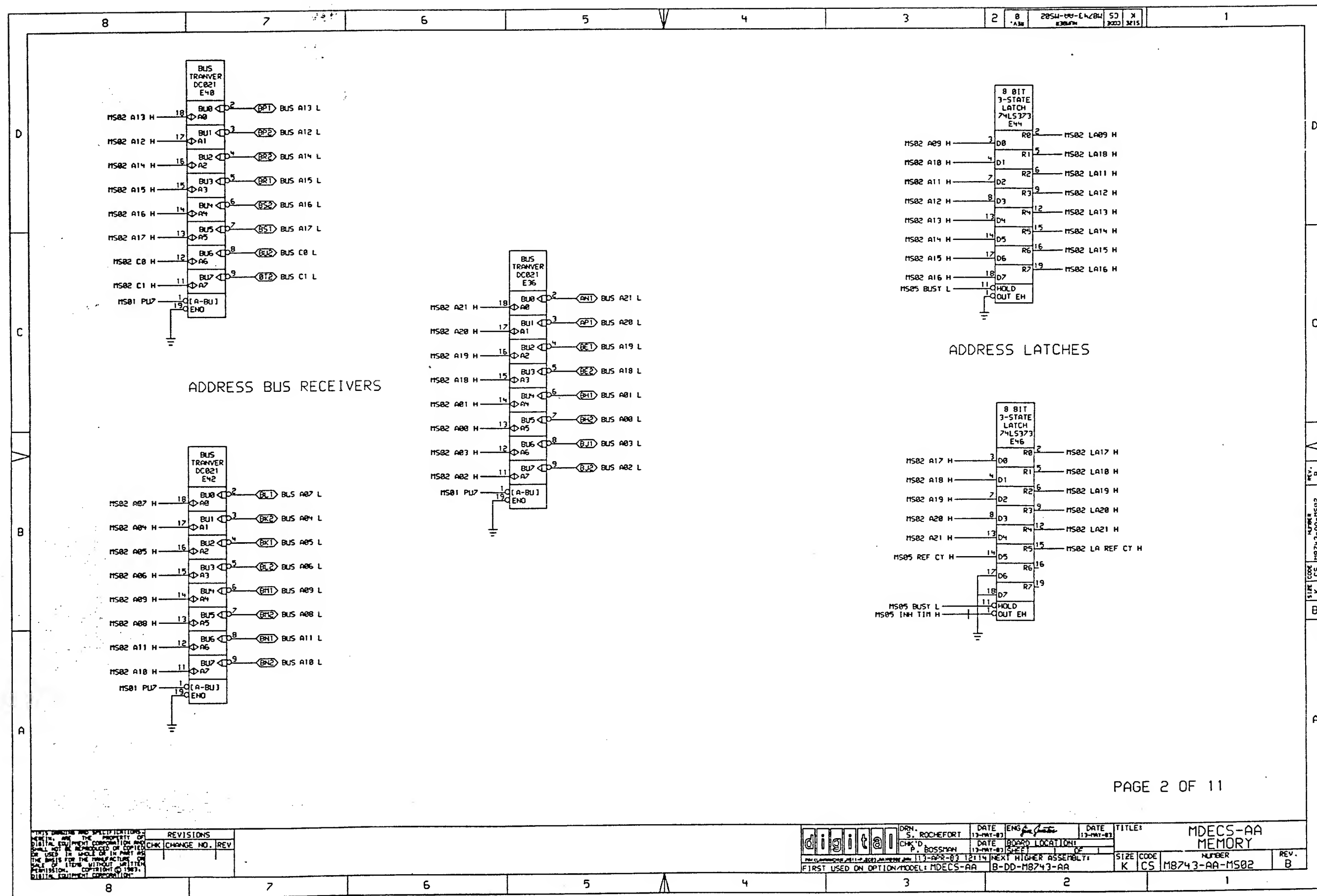
REVISIONS		
CHK	CHANGE NO	REV.

TITLE		ETCH CUT DRAWING	SIZE CODE	DEC	NUMBER	5Ø15117-AA-Ø	REV.	B
SCALE		SHEET 3 OF 3		DIST.	SHO			1







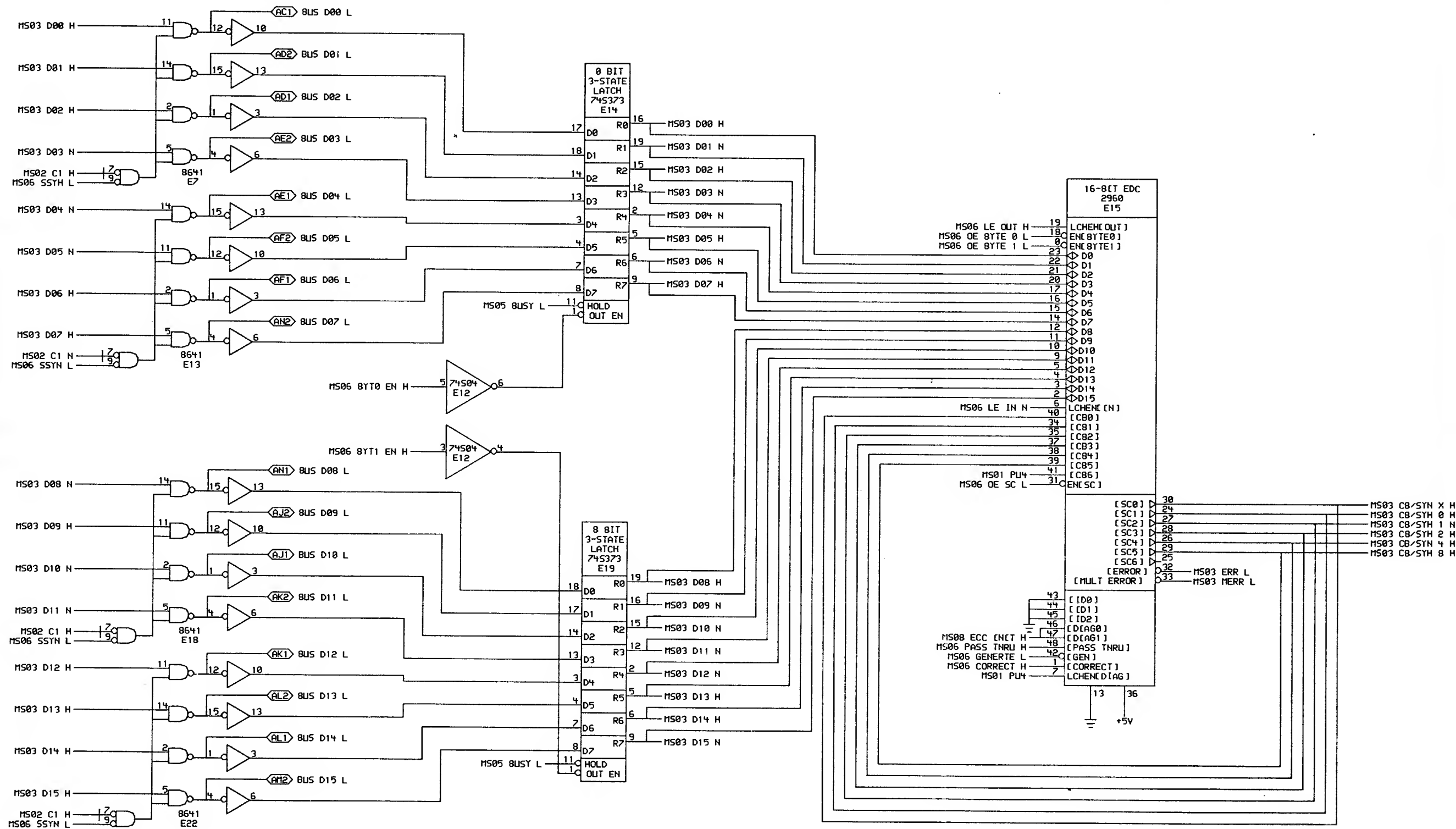


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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. S. ROCHEFORT	DATE 12-MAY-83	ENG. <i>[Signature]</i>	DATE 12-MAY-83	TITLE: MDECS-AA MEMORY
	CHK'D P. BOSSMAN	DATE 12-MAY-83	BOARD LOCATION: 12	DE: 1	
FIRST USED ON OPTION/MODEL: MDECS-AA B-DD-M8743-AA					
SIZE K	CODE CS	NUMBER M8743-AA-MS02		REV. B	





DATA BUS TRANSCEIVERS

DATA LATCHES

EDC LOGIC

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REVISIONS		
CHK	CHANGE NO.	REV

**digital**  
CHK'D  
P. BOSSHAN

DATE  
29-JUN-82

DATE  
29-JUN-82

DATE  
29-JUN-82

DATE  
29-JUN-82

DATE  
29-JUN-82

DATE  
29-JUN-82

DATE  
29-JUN-82

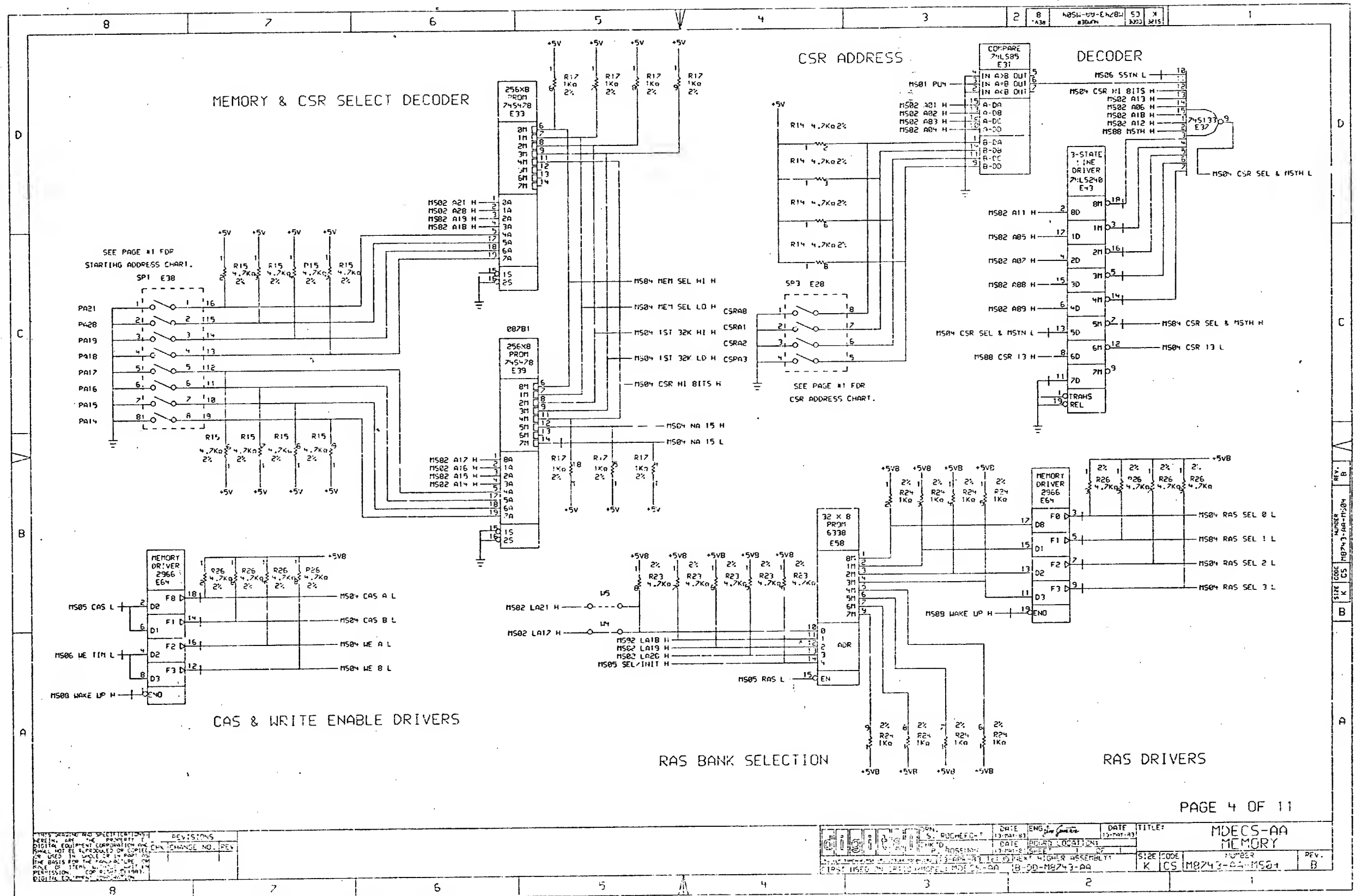
DATE  
29-JUN-82

710,1314.MOS03.DRW 125-JUN-82 13:14 JHEX NIGHER ASSEMBLY:  
FIRST USED ON OPTION/MODEL: MDECS-AA B-DD-M8743-AA

SIZE CODE NUMBER REV.  
D CS M8743-AA-MS03 A

ML2 1

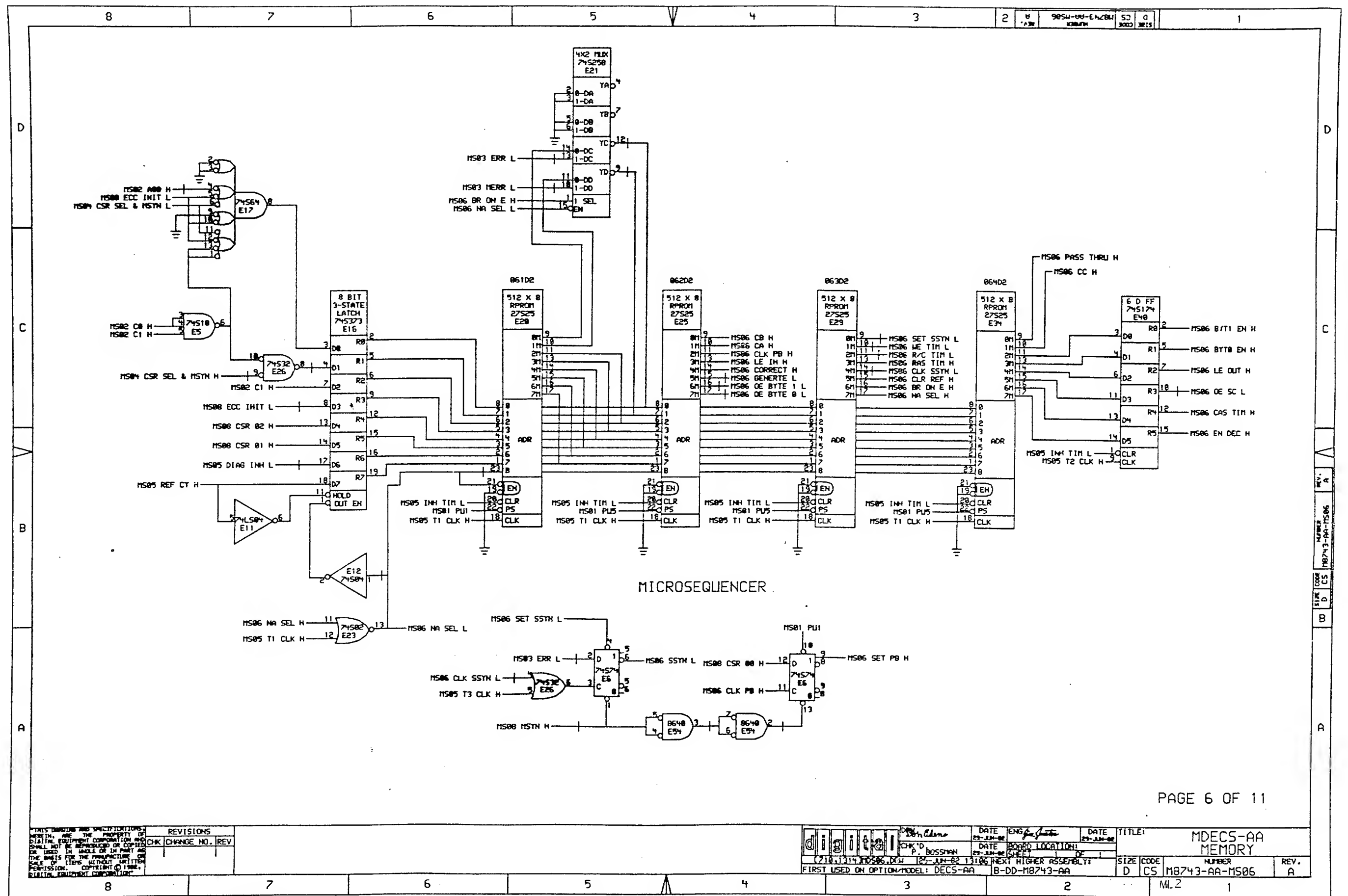








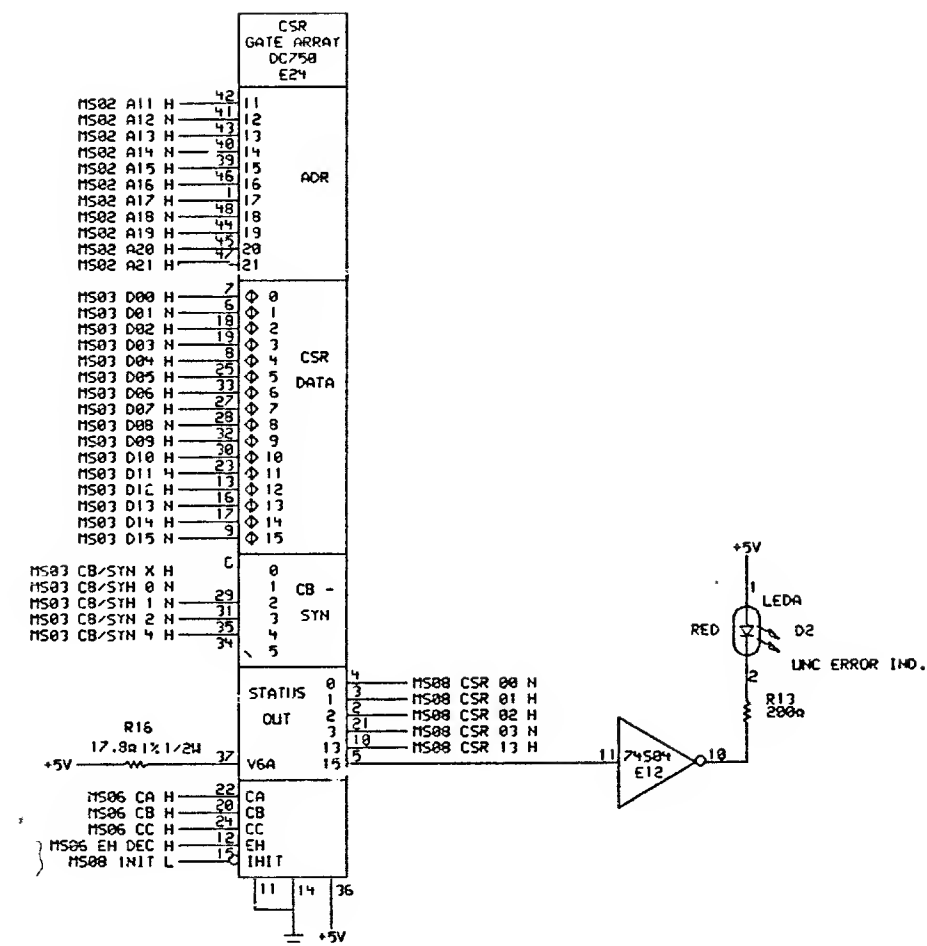
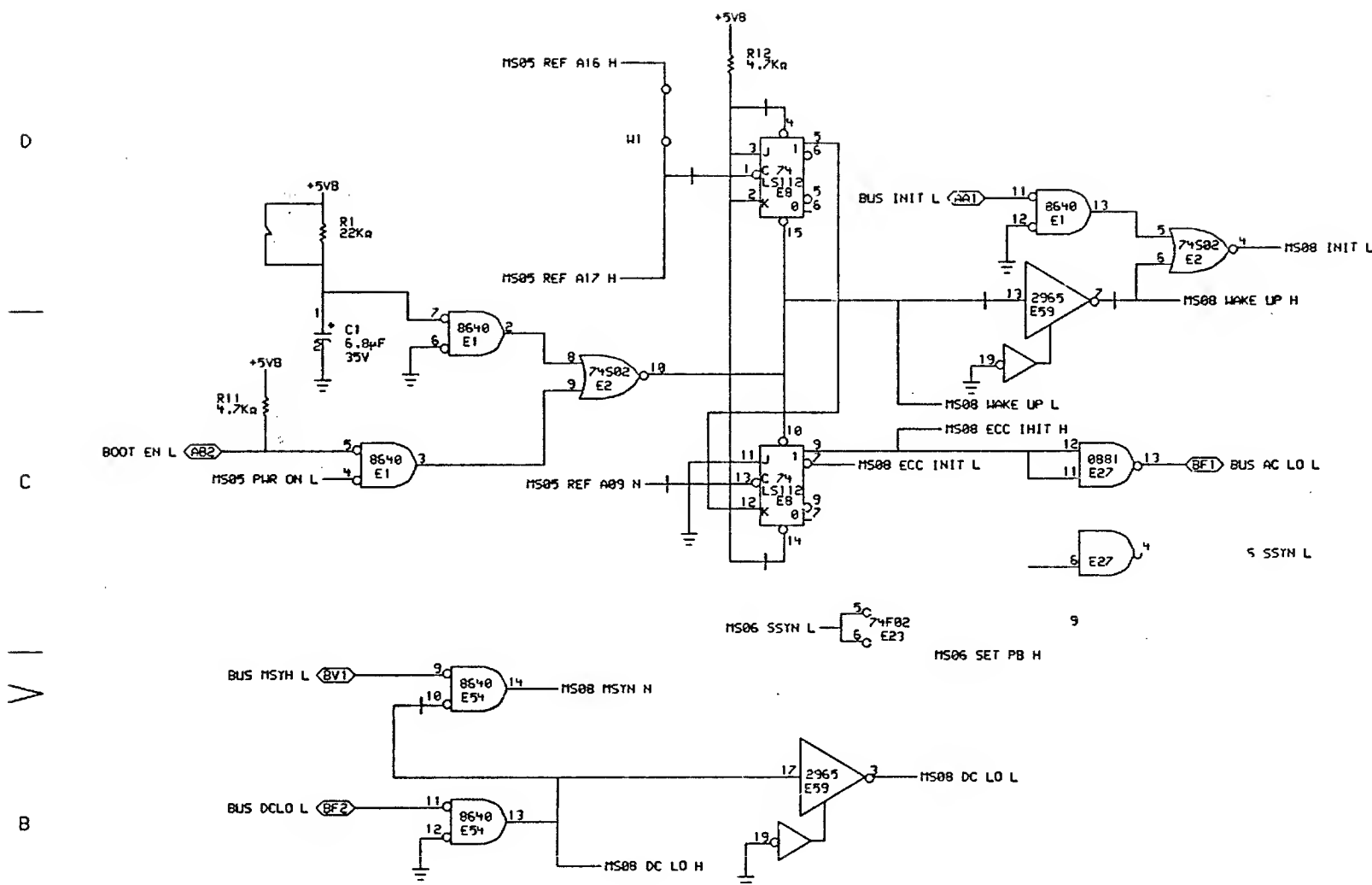








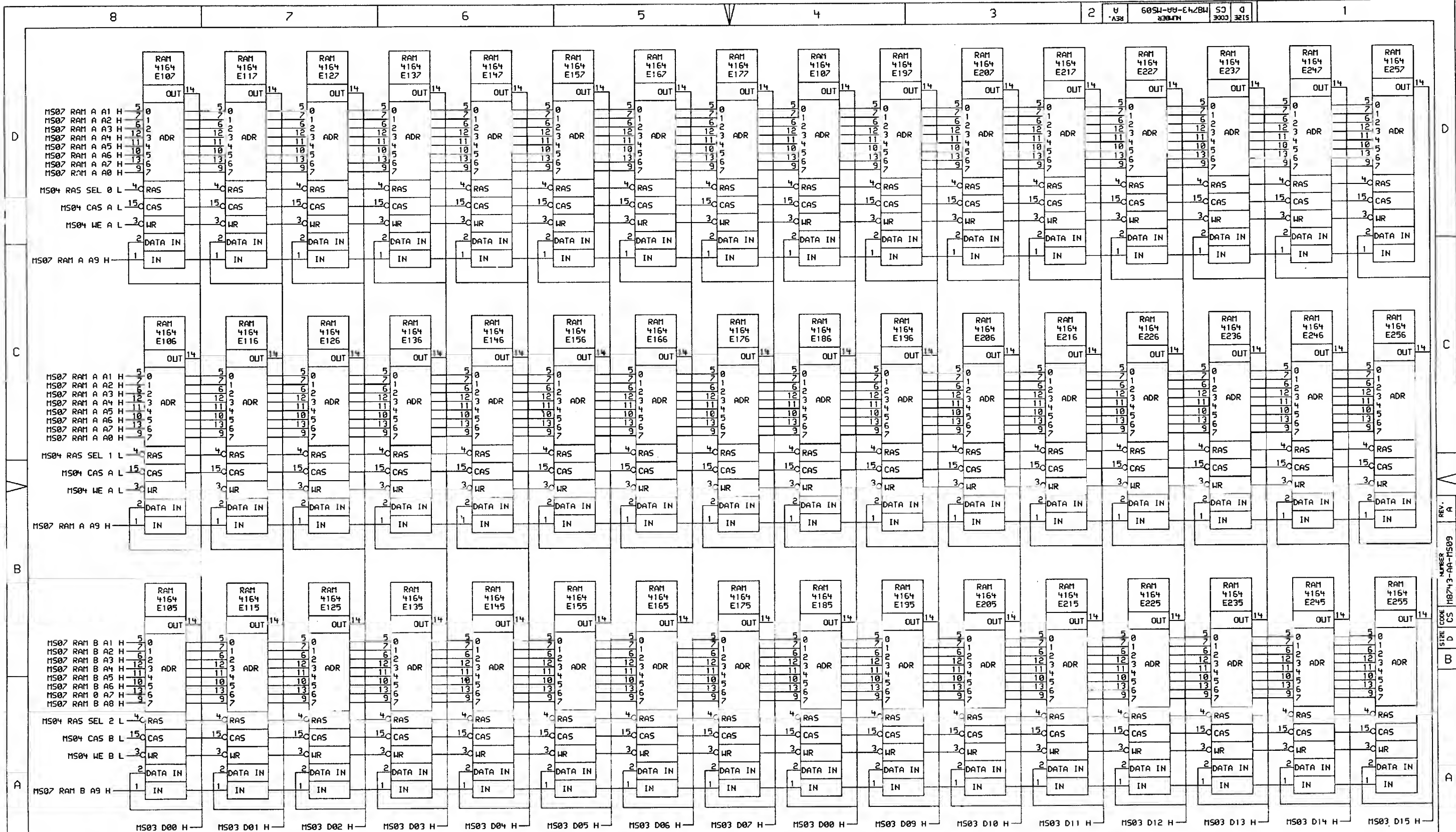




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SPECIFICATIONS		REVISIONS	
CHK	CHANGE NO.	REV	





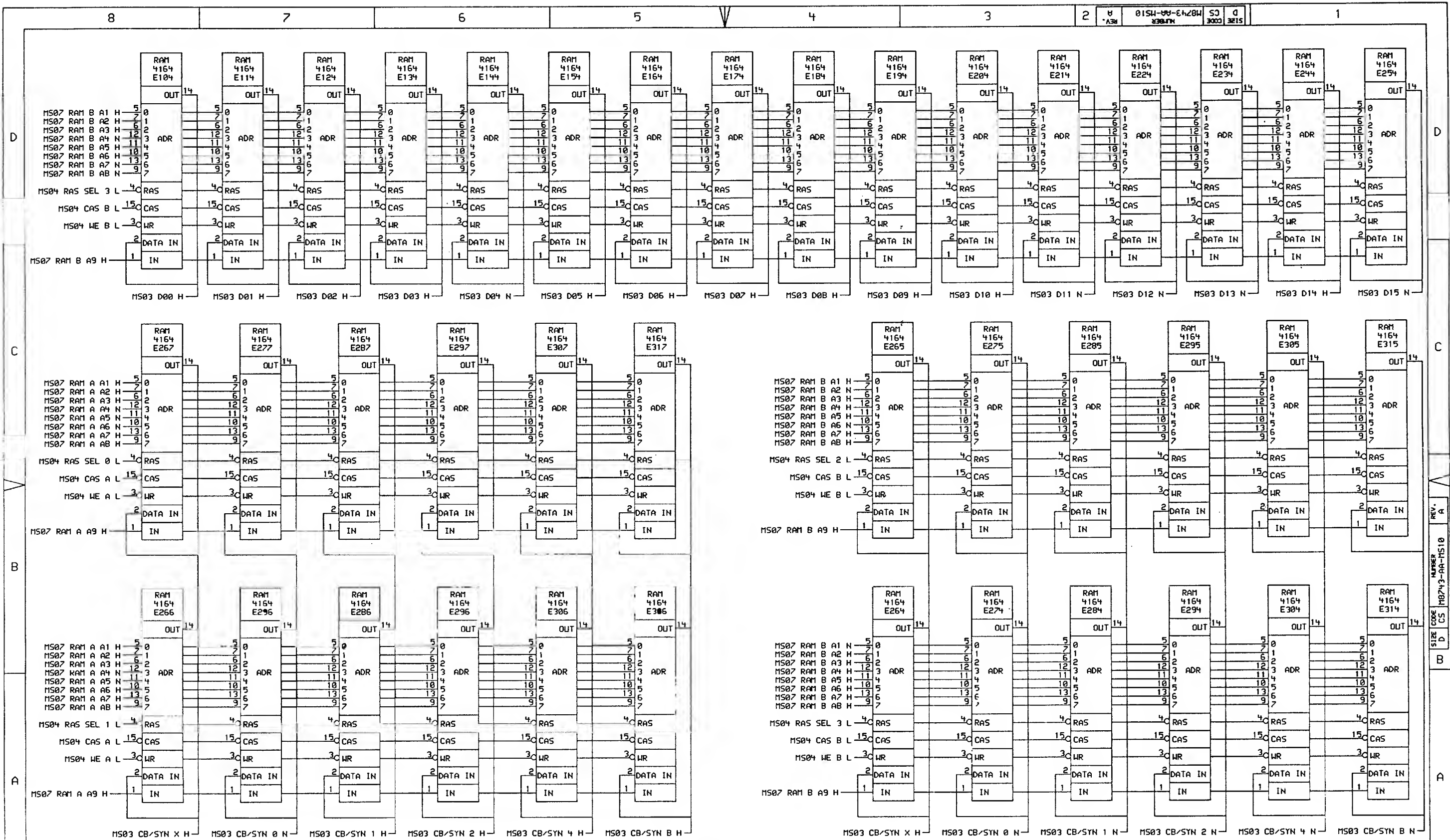
MEMORY ARRAY

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE 29-JUN-82	ENG Jm Jester	DATE 29-JUN-82	TITLE: MDECS-AA MEMORY
	CHK'D P. BOSSHAN	DATE 29-JUN-82	BOARD LOCATION: 1 OF 1	SIZE CODE D CS
FIRST USED ON OPTION/MODEL: MDECS-AA				NUMBER M8743-AA-MS09
				REV. A





MEMORY ARRAY

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[710,1314] MDS10.DRW [25-JUN-82 11:36] NEXT HIGHER ASSEMBLY: B-DD-M8743-AA				SIZE CODE D CS		NUMBER M8743-AA-MS10		REV. A	
FIRST USED ON OPTION MODEL: MDEC5-AA				SIZE CODE D CS		NUMBER M8743-AA-MS10		REV. A	



